

RoHS

COMPLIANT

HALOGEN

FREE

# Hyperfast Rectifier, 15 A FRED Pt®



TO-220 FullPAK 2L

PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub> 15 A						
$V_{R}$	600 V					
V <sub>F</sub> at I <sub>F</sub> 1.55 V						
t <sub>rr</sub> (typ.)	18 ns					
T <sub>J</sub> max.	175 °C					
Package	TO-220 FullPAK 2L					
Circuit configuration	Single					

#### **FEATURES**

- Hyperfast recovery time, extremely low Q<sub>rr</sub>
- Low forward voltage drop
- 175 °C operating junction temperature
- · Low leakage current
- Fully isolated package (V<sub>INS</sub> = 2500 V<sub>RMS</sub>)
- True 2 pin package
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912"><u>www.vishav.com/doc?99912</u></a>

#### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	$V_{RRM}$		600	V			
Average rectified forward current in DC	I <sub>F(AV)</sub>	T <sub>C</sub> = 71 °C	15	۸			
Non-repetitive peak surge current	I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	120	Α			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-65 to +175	°C			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	$V_{BR}$ , $V_{R}$	I <sub>R</sub> = 100 μA	600	-	-	.,
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 15 A	-	2.5	3.4	V
		I <sub>F</sub> = 15 A, T <sub>J</sub> = 150 °C	-	1.55	2	
Deverage legisers of mont	_	$V_R = V_R$ rated	-	0.02	36	
Reverse leakage current I <sub>R</sub>		T <sub>J</sub> = 150 °C, V <sub>R</sub> = V <sub>R</sub> rated	-	40	250	μΑ
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 600 V	-	12	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8	-	nH





<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST C	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	17	23	
Poverse receivery time		$I_F = 15 \text{ A}, dI_F/dt = 1$	I <sub>F</sub> = 15 A, dI <sub>F</sub> /dt = 100 A/μs, V <sub>R</sub> = 30 V		18	30	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	20	-	ns
		T <sub>J</sub> = 125 °C		-	45	-	
Dook room ourrent	1	T <sub>J</sub> = 25 °C	$I_F = 15 A$ , $dI_F/dt = 200 A/\mu s$ ,	-	2.7	-	- A - nC
Peak recovery current	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	$V_{\rm R} = 390 \text{ V}$	-	5.5	-	
Reverse recovery charge	0	T <sub>J</sub> = 25 °C	] "	-	26	-	
	$Q_{rr}$	T <sub>J</sub> = 125 °C		-	130	-	
Reverse recovery time	t <sub>rr</sub>	$T_{J} = 125  ^{\circ}\text{C} \qquad \qquad \begin{aligned} I_{F} &= 15  \text{A}, \\ dI_{F}/dt &= 8 \\ V_{R} &= 390 \end{aligned}$	I <sub>F</sub> = 15 A.	-	32	-	ns
Peak recovery current	I <sub>RRM</sub>		$dI_F/dt = 800 A/\mu s$ ,	-	17	-	Α
Reverse recovery charge	Q <sub>rr</sub>		$V_{R} = 390 \text{ V}$	-	290	-	nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-65	-	175	°C
Thermal resistance, junction-to-case	R <sub>thJC</sub>		-	3.7	4.3	
Thermal resistance, junction-to-ambient	R <sub>thJA</sub>	Typical socket mount	-	-	70	°C/W
Typical thermal resistance, case-to-heatsink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-	
Weight			-	2	-	g
Weight			-	0.07	-	OZ.
Mounting torque			6 (5)	-	12 (10)	kgf · cm (lbf · in)
Marking device		Case style TO-220 FullPAK 2L	ETX1506FP			



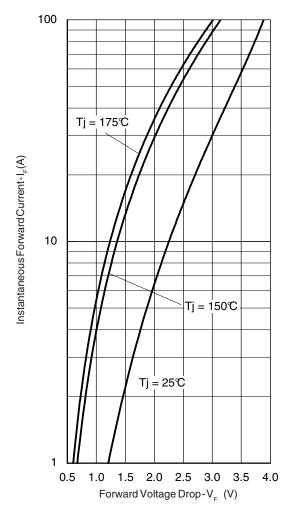


Fig. 1 - Typical Forward Voltage Drop Characteristics

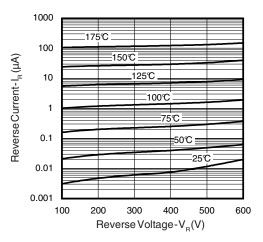


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

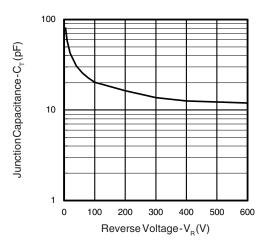


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

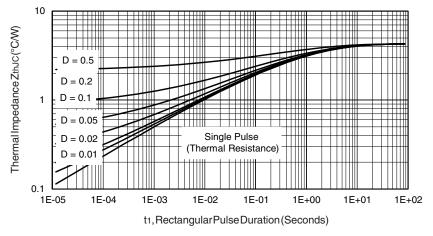


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

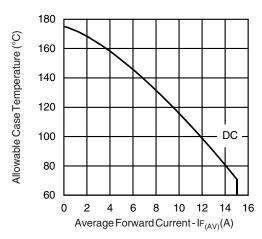


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

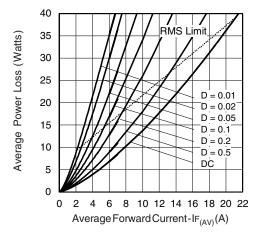


Fig. 6 - Forward Power Loss Characteristics

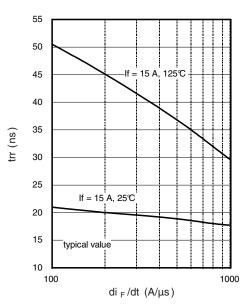


Fig. 7 - Typical Reverse Recovery vs. dl<sub>F</sub>/dt

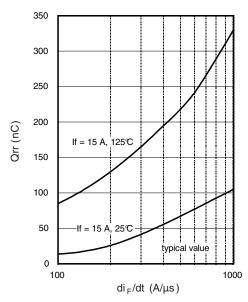
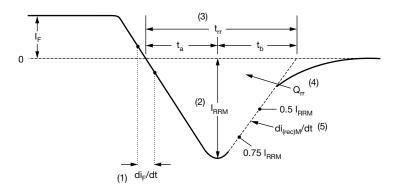


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

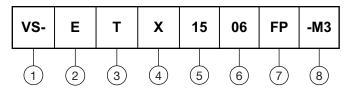


- (1) di<sub>F</sub>/dt rate of change of current through zero crossing
- (4)  $Q_{rr}$  area under curve defined by  $t_{rr}$  and  $l_{RRM}$
- (2)  $I_{RRM}$  peak reverse recovery current
- $Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$
- (3) t<sub>rr</sub> reverse recovery time measured from zero crossing point of negative going I<sub>F</sub> to point where a line passing through 0.75 I<sub>RRM</sub> and 0.50 I<sub>RRM</sub> extrapolated to zero current.
- (5)  $di_{(rec)M}/dt$  peak rate of change of current during  $t_b$  portion of  $t_{rr}$

Fig. 9 - Reverse Recovery Waveform and Definitions

#### **ORDERING INFORMATION TABLE**

Device code



- Vishay Semiconductors product
- **2** Circuit configuration:

E = single

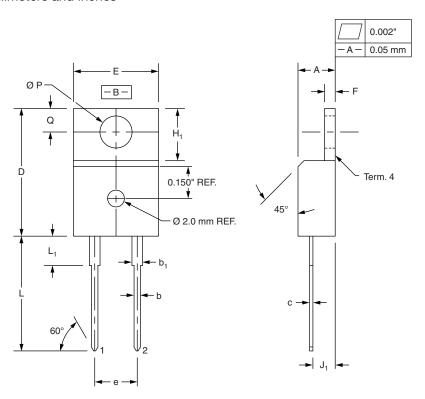
- **3** T = TO-220
- 4 X = hyperfast recovery time
- **5** Current code: 15 = 15 A
- 6 Voltage code: 06 = 600 V
- **7** FP = TO-220 FullPAK 2L
- 8 Environmental digit:
  - -M3 = halogen-free, RoHS-compliant, and terminations lead (Pb)-free

ORDERING INFORMATION (Example)							
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION							
VS-ETX1506FP-M3	50	1000	Antistatic plastic tube				

LINKS TO RELATED DOCUMENTS					
Dimensions <u>www.vishay.com/doc?96157</u>					
Part marking information <a href="https://www.vishay.com/doc?95392">www.vishay.com/doc?95392</a>					

### True 2 Pin TO-220

#### **DIMENSIONS** in millimeters and inches



SYMBOL	MILLIM	METERS	INCHES		
	MIN.	MAX.	MIN.	MAX.	
А	4.32	4.57	0.170	0.180	
b	0.71	0.91	0.028	0.036	
b <sub>1</sub>	1.15	1.39	0.045	0.055	
С	0.36	0.53	0.014	0.021	
D	14.99	15.49	0.590	0.610	
E	10.04	10.41	0.395	0.410	
е	5.08	5.08 BSC		BSC	
F	1.22	1.37	0.048	0.054	
H <sub>1</sub>	5.97	6.47	0.235	0.255	
J <sub>1</sub>	2.54	2.79	0.100	0.110	
L	13.47	13.97	0.530	0.550	
L <sub>1</sub> (1)	3.31	3.81	0.130	0.150	
Ø P	3.79	3.88	0.149	0.153	
Q	2.60	2.84	0.102	0.112	

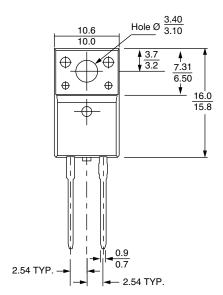
#### Notes

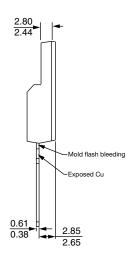
- $^{(1)}$  Lead dimension and finish uncontrolled in  $L_1$
- These dimensions are within allowable dimensions of JEDEC TO-220AB rev. J outline dated 3-24-87
- Controling dimension: Inch

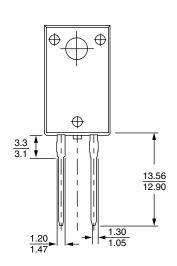


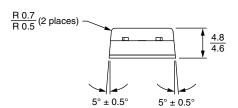
### 2L TO-220 FullPAK

#### **DIMENSIONS** in millimeters









Bottom view



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Vishay

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