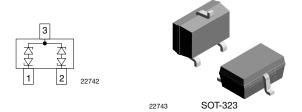
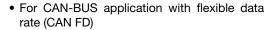


Vishay Semiconductors

Bidirectional Symmetrical (BiSy) Low Capacitance, Dual-Line ESD Protection Diode in SOT-323



FEATURES

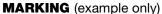


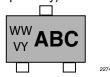




Bo

- Small SOT-323 package
- 2-line ESD protection
- Working range ± 26.5 V
- Low leakage current I_R < 0.05 μA
- Low load capacitance C_D = 3 pF (typ. at 5 V)
- ESD immunity acc. IEC 61000-4-2
 - ± 25 kV contact discharge
 - ± 30 kV air discharge
- ESD capability according to AEC-Q101: human body model: class H3B: > 8 kV
- e3 pins plated with tin (Sn)
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>





ABC = type code (see table below) WW = date code working week VY = date code year

LINKS TO ADDDITIONAL RESOURCES



ORDERING INFORMATION								
PART NUMBER (EXAMPLE)	ENVIRONMENTAL AND QUALITY CODE				PACKAG	ING CODE		
	AEC-Q101 QUALIFIE	()		TINI	3K PER 7" REEL (8 mm TAPE)	10K PER 13" REEL (8 mm TAPE)	ORDERING CODE (EXAMPLE)	
	D	STANDARD	GREEN	PLATED	15K/BOX = MOQ	10K/BOX = MOQ	<u> </u>	
VCAN26B2-03	-	Е		3	-08		VCAN26B2-03G-E3-08	
VCAN26B2-03	Н	Е		3	-08		VCAN26B2-03GHE3-08	
VCAN26B2-03	-	Е		3		-18	VCAN26B2-03G-E3-18	
VCAN26B2-03	Н	E		3		-18	VCAN26B2-03GHE3-18	

PACKAGE DATA							
DEVICE NAME	PACKAGE NAME	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS	
VCAN26B2-03G	SOT-323	BSY	5.65 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C	

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TER TEST CONDITIONS		VALUE	UNIT			
Peak pulse current	$T_A = 25$ °C, acc. IEC 61000-4-5; $t_p = 8/20 \mu s$; single shot	I _{PPM}	2	Α			
Peak pulse power	$T_A = 25$ °C; pin 1 or 2 to pin 3; acc. IEC 61000-4-5; $t_p = 8/20$ µs; single shot	P_{PP}	100	W			
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses, T _A = 25 °C	V	± 25	kV			
	Air discharge acc. IEC 61000-4-2; 10 pulses, T _A = 25 °C	V _{ESD}	±30	kV			
Operating temperature	Junction temperature	T_J	-55 to +150	°C			
Storage temperature		T _{STG}	-55 to +150	°C			

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ELECTRICAL CHARACTERISTICS (pin 1 to 3, 3 to 1, 2 to 3, or 3 to 2) (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N _{channel}	-	-	2	lines		
Reverse stand-off voltage	Max. reverse working voltage	V_{RWM}	-	26.5		V		
Reverse voltage	At I _R = 0.05 μA	V_R	26.5	-	-	V		
Reverse current	At V _{RWM} = 26.5 V	_	-	-	0.05	μА		
neverse current	At V _{RWM} = 26.5 V; T _J = 125 °C	- I _R	-	-	0.1			
Reverse breakdown voltage	At I _R = 1 mA	V_{BR}	28	30	32	V		
D	At I _{PP} 1 A; t _p = 8/20 μs	V	-	34	41	V		
Reverse clamping voltage	At $I_{PP} = I_{PPM} = 2 \text{ A}$; $t_p = 8/20 \mu\text{s}$	V _C	-	40	50			
	At $V_R = 0 V$, $f = 1 MHz$		-	4	5	- pF		
Capacitance	At $V_R = 5 V$, $f = 1 MHz$	- C _D	-	3	4			
Capacitarios	Diode capacitance matching at $V_R = 5 \text{ V}$, $C_{D13} \text{ vs. } C_{D23}$	dC _D	-	-	0.3	pF		

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

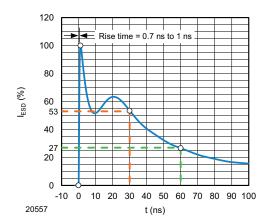


Fig. 1 - ESD Discharge Current Wave Form acc. IEC 61000-4-2 (330 Ω / 150 pF)

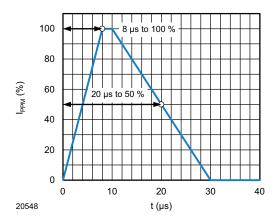


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

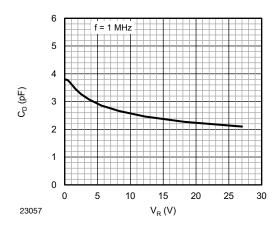


Fig. 3 - Typical Capacitance C_{D} vs. Reverse Voltage V_{R}

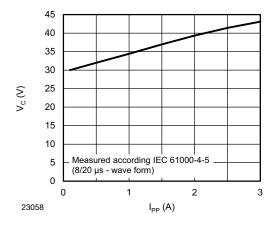


Fig. 4 - Typical Peak Clamping Voltage V_C vs. Peak Pulse Current I_{PP}





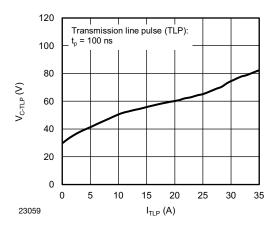
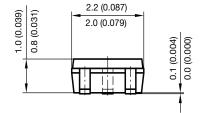
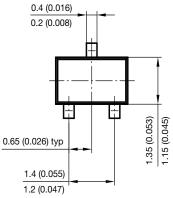
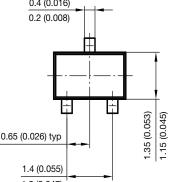


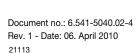
Fig. 5 - Typical Clamping Voltage V_{C-TLP} vs. Pulse Current I_{TLP}

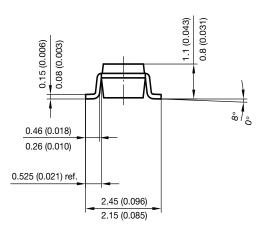
PACKAGE DIMENSIONS in millimeters (inches) SOT-323



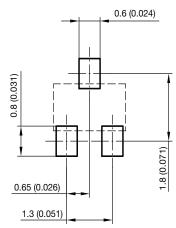






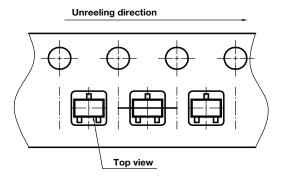


foot print recommendation:



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ORIENTATION IN CARRIER TAPE SOT-323

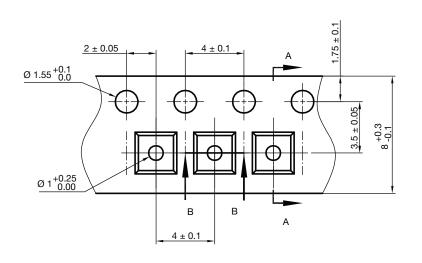


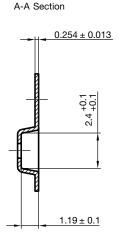
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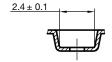
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CARRIER TAPE SOT-323





B-B Section



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