

## L1007C101KDWST

Aliases (L1007C101KDWST)

Not for New Design

KEMET, L-DWS, Power, 100 uH, 10%



Click [here](#) for the 3D model.

Dimensions	
Chip Size	1007
L	2.5mm +/-0.2mm
W	1.8mm +/-0.2mm
T	1.8mm +/-0.2mm
LL	0.5mm +/-0.2mm
B	0.5mm +/-0.2mm

Packaging Specifications	
Packaging	T&R
Packaging Quantity	2000

General Information	
Series	L-DWS
Style	SMD Chip
Description	Surface Mount Inductor
Features	Standard Type
RoHS	Yes
Notes	Last Order Date June 30, 2023.

Specifications	
Inductance	100 uH (0.796 MHz)
Inductance Tolerance	10%
Temperature Range	-40/+105°C
Current	60 mAmps (Isat, 30% Drop In Inductance), 260 mAmps (Irms, 40C Rise By Self Heating)
DC Resistance	2.1 Ohms
Self-Resonant Frequency	9 MHz MIN

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