



Product Discontinuation Notice for Development Systems Products

XCN08022 (v2.0) January 12, 2009

Product Discontinuation Notice

Overview

The purpose of this notification is to communicate that Xilinx is discontinuing certain Development Systems Products.

Description

As part of standard Xilinx product pruning activities, the following products have been identified for obsolescence. This notice should be used in conjunction with previous discontinuance notices which can be found at http://www.xilinx.com/support/documentation/customer_notices.htm.

Products Affected

1. H.264 LogiCORE™ IP Products
2. HDLC Controller LogiCORE IP Product
3. MPEG4 Simple Profile LogiCORE IP Products
4. Spartan®-3A FPGA DDR Development Kit
5. Virtex®-4 FPGA Video Starter Kit
6. Xilinx Development Systems Kits and Boards Products on While Supplies Last

Table 1: H.264 LogiCORE™ IP Products

Part Number	Description	Replacement Part
DO-DI-H264-CABAC	LogiCORE IP H.264 CABAC	No direct replacement
SC-DI-H264-CABAC	LogiCORE IP H.264 CABAC, Support Contract	No direct replacement
SR-DI- H264-CABAC	LogiCORE IP H.264 CABAC, Support Reinstate	No direct replacement
DO-DI-H264-DEBLOCK	LogiCORE IP H.264 Deblocker	No direct replacement
SC-DI-H264-DEBLOCK	LogiCORE IP H.264 Deblocker, Support Contract	No direct replacement
SR-DI-H264-DEBLOCK	LogiCORE IP H.264 Deblocker, Support Reinstate	No direct replacement
DO-DI-H264-ME	LogiCORE IP H.264 Motion Estimation Engine	No direct replacement
SC-DI-H264-ME	LogiCORE IP H.264 Motion Estimation Engine Support Contract	No direct replacement
SR-DI-H264-ME	LogiCORE IP H.264 Motion Estimation Engine Support Reinstate	No direct replacement
DO-DI-H264-ME-WW	LogiCORE IP H.264 Motion Estimation Engine World Wide	No direct replacement

Replacement Products

No direct LogiCORE IP replacements are planned.

NOTE: LogiCORE IP H.264 CABAC was last released against ISE® software 9.1i SP1.
LogiCORE IP H.264 Deblocker was last released against ISE 8.2i SP3.
LogiCORE IP H.264 Motion Estimation Engine was last released against ISE 9.2i SP1.

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Table 2: HDLC Controller LogiCORE IP Product

Part Number	Description	Replacement Part
DO-DI-HDLC32	LogiCORE IP HDLC Controller Multi-Channel	No direct replacement
SC-DI-HDLC32	LogiCORE IP HDLC Controller Multi-Channel, Support Contract	No direct replacement
SR-DI-HDLC32	LogiCORE IP HDLC Controller Multi-Channel, Support Reinstate	No direct replacement

Replacement Products

No direct LogiCORE IP replacements are planned.

NOTE: LogiCORE IP HDLC Controller was last released against ISE software 10.1 and EDK SP3.

Table 3: MPEG4 Simple Profile LogiCORE IP Products

Part Number	Description	Replacement Part
DO-DI- MPEG4-2-ENC	LogiCORE IP MPEG4 Simple Profile Encoder	No direct replacement
SC-DI- MPEG4-2- ENC	LogiCORE IP MPEG4 Simple Profile Encoder Support Contract	No direct replacement
SR-DI- MPEG4-2- ENC	LogiCORE IP MPEG4 Simple Profile Encoder Support Reinstate	No direct replacement
DO-DI-MPEG4-2-DEC	LogiCORE IP MPEG4 Simple Profile Decoder	No direct replacement
SC-DI- MPEG4-2-DEC	LogiCORE IP MPEG4 Simple Profile Decoder Support Contract	No direct replacement
SR-DI- MPEG4-2-DEC	LogiCORE IP MPEG4 Simple Profile Decoder Support Reinstate	No direct replacement

Replacement Products

No direct LogiCORE IP replacements are planned.

NOTE: LogiCORE IP MPEG4 Simple Profile Encoder was last released against ISE software 8.2i SP2.
LogiCORE IP MPEG4 Simple Profile Decoder was last released against ISE 8.2i SP2.

Table 4: Spartan-3A FPGA DDR Development Kit

Part Number	Description	Replacement Part
HW-SPAR3ADDR2-DK-UNI-G	Spartan-3A FPGA DDR Development Kit	HW-SPAR3A-SK-UNI-G
HW-SPAR3ADDR2-DK-UNI-G-J	Spartan-3A FPGA DDR Development Kit – Japanese Version	HW-SPAR3A-SK-UNI-G-J

NOTE: The Spartan-3A FPGA DDR Development Kit is supported in ISE software and ChipScope™ Pro tool 9.2i.

Table 5: Virtex-4 FPGA Video Starter Kit

Part Number	Description	Replacement Part
HW-V4SX35-VIDEO-SK-US	Virtex-4 FPGA Video Starter Kit – US Version	No direct replacement
HW-V4SX35-VIDEO-SK-EC	Virtex-4 FPGA Video Starter Kit – EC Version	No direct replacement
HW-V4SX35-VIDEO-SK-UK	Virtex-4 FPGA Video Starter Kit – UK Version	No direct replacement
HW-V4SX35-VIDEO-SK-J	Virtex-4 FPGA Video Starter Kit – Japanese Version	No direct replacement

NOTE: The Virtex-4 FPGA Video Starter Kit is supported in ISE software and EDK 9.1i.

Table 6: Xilinx Development Systems Kits and Boards Products on While Supplies Last

The purpose of this notice is to communicate that the following products are being placed on an Available While Supplies Last status. The products listed are in stock, but will NOT be replenished when depleted. At that time the product will enter the obsolescence process.

Part Number	Description	Replacement Part
HW-XGI-SCLK	Super Clock Module	HW-XGI-SCLK-G
HW-AFX-FG456-300	Virtex-II Pro FPGA FG456 Prototype Board	No Direct Replacement
HW-AFX-FF672-300	Virtex-II Pro FPGA FF672 Prototype Board	No Direct Replacement
HW-AFX-FF1152-300	Virtex-II Pro FPGA FF1152 Prototype Board	No Direct Replacement
HW-AFX-SF363-400	Virtex-II Pro FPGA SF363 Prototype Board	No Direct Replacement
HW-V4-ML405-US HW-V4-ML405-UK HW-V4-ML405-EC	Virtex-4 FX FPGA ML405 Evaluation Platform, US, UK and EC versions	HW-V4-ML405-UNI-G
HW-V4-ML403-USA HW-V4-ML403-UK HW-V4-ML403-EC	Virtex-4 FPGA ML403 Embedded Platform	HW-V4-ML403-UNI-G
HW-SPAR3E-DISP-DK-UNI-G HW-SPAR3E-DISP-DK-UNI-G-J HW-SPAR3E-DISP-DK-UNI-G-PROMO HW-SPAR3E-DISP-DK-UNI-G-J-PROMO	Spartan-3E FPGA Display Development Kit	No Direct Replacement
DO-CPLD-DK DO-CPLD-DK-J	CPLD Design Kit, including Japanese Version	DO-CPLD-DK-G

NOTE:

1. Reference designs provided with all the above Development Systems are intended to be used as is.
2. The last version of design environment to support the reference designs is ISE software 9.2i.

Key Dates and Ordering Information

H.264 CABAC Encoder LogiCORE IP Product

Final orders are accepted until December 31, 2008.
Final deliveries must occur on or before January 14, 2009.
No technical support available after December 31, 2009.

Existing Warranty Contracts will be honored after product discontinuance, but will not be renewable.

HDLC Controller LogiCORE IP Product

Final orders are accepted until December 31, 2008.
Final deliveries must occur on or before January 14, 2009.
No technical support available after December 31, 2009.

Existing Warranty Contracts will be honored after product discontinuance, but will not be renewable.

MPEG4 Simple Profile LogiCORE IP Product

Final orders are accepted until December 31, 2008.
Final deliveries must occur on or before January 14, 2009.
No technical support available after December 31, 2009.

Existing Warranty Contracts will be honored after product discontinuance, but will not be renewable.

Spartan-3A FPGA DDR Development Kit

Final orders are accepted until November 14, 2008.
Final deliveries must occur on or before November 28, 2008.
No technical support available after May 14, 2009.

Existing Warranty Contracts will be honored 90 days after product discontinuance.

Virtex-4 FPGA Video Starter Kit

Final orders are accepted until December 1, 2008.
Final deliveries must occur on or before December 15, 2008.
No technical support available after June 1, 2009.

Existing Warranty Contracts will be honored 90 days after product discontinuance.

Multiple Xilinx Development Systems Kits and Boards

Available While Supplies Last. The products listed are in stock, but will NOT be replenished when depleted. At that time, the product will enter the obsolescence process.

Six months after the obsolescence process no technical support will be available for the product.

Response

No response is required. For additional information or questions, please contact [Xilinx Technical Support](#).

Note: In accordance with JESD48, this change is deemed accepted by the customer if no acknowledgement is received within 30 days from this notice.

Important Notice: Xilinx Customer Notices (PCNs, PDNs, and Quality Alerts) can be delivered via e-mail alerts sent by the MySupport website (<http://www.xilinx.com/support>). Register today and personalize your “MyAlerts” area to include Customer Notices. This change provides many benefits, including the ability to receive alerts for new and updated information about specific products, as well as alerts for other publications such as data sheets, errata, application notes, etc. For information on how to sign up, refer to [Xilinx Answer Record 18683](#).

Revision History

The following table shows the revision history for this document.

Date	Version	Revision
10/13/08	1.0	Initial release.
1/12/09	2.0	1) Support Dates were added to all products and moved to the "Key Dates and Ordering Information" Section. 2) DO-DI-H264-ME-WW was added to Table #1. 3) Support in a specific software version was added to all the boards, including Table #6. 4) Added Disclaimer section.

Disclaimer

THE XILINX HARDWARE FPGA AND CPLD DEVICES REFERRED TO HEREIN (“PRODUCTS”) ARE SUBJECT TO THE TERMS AND CONDITIONS OF THE XILINX LIMITED WARRANTY WHICH CAN BE VIEWED AT <http://www.xilinx.com/warranty.htm>. THIS LIMITED WARRANTY DOES NOT EXTEND TO ANY USE OF PRODUCTS IN AN APPLICATION OR ENVIRONMENT THAT IS NOT WITHIN THE SPECIFICATIONS STATED ON THE XILINX DATA SHEET. ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE. PRODUCTS ARE NOT DESIGNED OR INTENDED TO BE FAIL-SAFE, OR FOR USE IN ANY APPLICATION REQUIRING FAIL-SAFE PERFORMANCE, SUCH AS LIFE-SUPPORT OR SAFETY DEVICES OR SYSTEMS, OR ANY OTHER APPLICATION THAT INVOKES THE POTENTIAL RISKS OF DEATH, PERSONAL INJURY OR PROPERTY OR ENVIRONMENTAL DAMAGE (“CRITICAL APPLICATIONS”). USE OF PRODUCTS IN CRITICAL APPLICATIONS IS AT THE SOLE RISK OF CUSTOMER, SUBJECT TO APPLICABLE LAWS AND REGULATIONS. ALL SPECIFICATIONS ARE SUBJECT TO CHANGE WITHOUT NOTICE.