FLEXGUIUG Flexible Graphical User Interface (FlexGUI) Rev. 1 – 13 May 2016

User guide

1 Flexible Graphical User Interface Principle

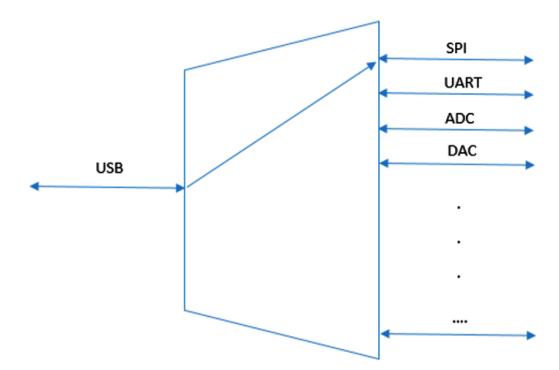
FlexGUI Software



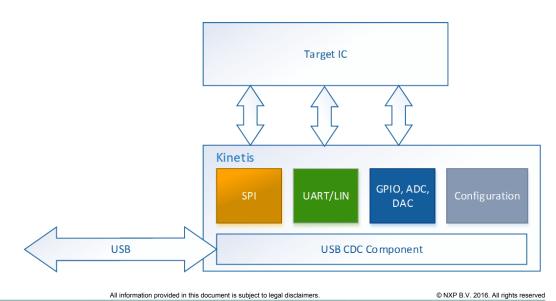
2 Introduction

FlexGUI is a set of hardware and software tools that allow the user to program and configure a dedicated integrated circuit.

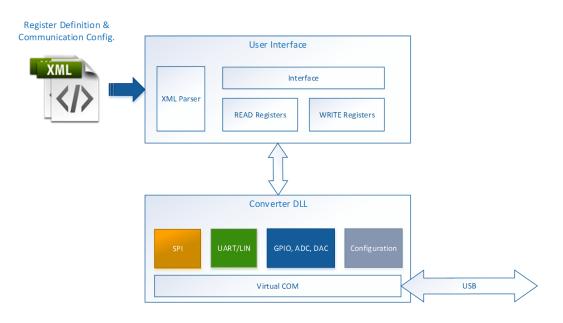
The hardware part (converter) converts analog and digital signals and different communication protocols to USB. It can function as a multiplexer:



This hardware can be implemented as a stand-alone board or integrated on any other evaluation board. The block schematics is shown here below. Communication with the hardware can be established easily by using the FlexGUI software or just the Converter DLL.



The software part consists of two different layers. The lower layer (Converter DLL) handles communication with the hardware and translates signals. Communication is established via a virtual serial COM port. The higher layer (User Interface) uses functions that are provided by the Converter DLL to communicate with the target device. The Converter exists and can be used as an independent GUI as well. In this case it can serve to convert different types of signals and protocols to USB.



2.1 System requirements

FlexGUI requires the following setup:

- Windows compatible PC
- .NET framework 4.0 or higher
- Microsoft Office (Microsoft Excel is used to generate and store special sequences. The sequencing functionality is not supported if MS Excel is not installed).

2.2 Installing the FlexGUI on Your Computer

FlexGUI is compatible with Windows operating systems (Windows 7 and higher).

1. To install the FlexGUI application and necessary drivers, launch **setup.exe** and follow the instructions.

Flexible Graphical User Interface (FlexGUI)

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☆ Favorites	-	Name	*	Date modified	Туре	Size		_
Contraction Desktop	=	6 setup.exe		16/09/2015 16:43	Application	3.038 KB		
🐌 Downloads	=							
Recent Places								
ConeDrive - Freescale Semi	nicondu							
SharePoint								
💿 one drive CA								

2. Connect hardware to your PC and wait for its installation. When the hardware is installed properly, the following screen is displayed.

Driver Software Installation		×
Freescale Converter (COM73)) installed	
Freescale Converter (COM73)	Ready to use	
		Close

If the driver is not signed by Microsoft, it cannot be installed on machines where the signature is mandatory. On Windows 7, there is a possibility during the installation process to omit this signature (necessary for proper installation). On Windows 8, the user has no choice during installation process and the signature is requested (default Windows 8 configuration). This setting on Windows 8 can be changed in advanced options (for details, see the Windows 8 driver installation troubleshooting).

3. Launch the FlexGUI application.

Note: Do not launch the installed application before the hardware is connected and installed.

3 Getting Started With the FlexGUI

The GUI allows the user to program all SPI features by using a friendly interface as well as modifying the register table manually for advanced users. GUI consists of two separate windows; **Converter** and the **FlexGUI**. The **Converter** window enables to convert different protocols such as SPI, UART, GPIOs or analog signals to USB. FlexGUI is an extension of the converter that enables sending commands that are predefined for each IC in a special .XML file.

3.1 Launching FlexGUI

1. Launch the **FlexGUI.exe**.

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- Select your device (XML configuration file). Navigate to the right folder (by default the configuration files are placed in C:\Program Files (x86)\FlexGUI \Sequences&Config).
- 3. If the hardware works properly, init config is loaded and two windows appear.

FlexGUI - M			a																x
Read Reg 1 F	Read Reg 2	Read Reg 3	Read Reg 4	Read Reg 5						Write Reg 1 Writ	e Reg 2 W	te Reg 3							
MUX	MUX_2	MUX_1	MUX_0	Int 2K	I/O-att	0	0	0		MUX	MUX_2	MUX_1	MUX_0	Int 2K	I/O-att	0	0	0	
RAM_A	Ram a7	Ram a6	Ram a5	Ram a4	Ram a3	Ram a2	Ram a1	Ram a0		RAM_A	Ram a7	Ram a6	Ram a5	Ram a4	Ram a3	Ram a2	Ram a 1	Ram a0	
RAM_B	Ram b7	Ram b6	Ram b5	Ram b4	Ram b3	Ram b2	Ram b1	Ram b0		RAM_B	Ram b7	Ram b6	Ram b5	Ram b4	Ram b3	Ram b2	Ram b1	Ram b0	
RAM_C	Ram c7	Ram c6	Ram c5	Ram c4	Ram c3	Ram c2	Ram c1	Ram c0] 🗖	RAM_C	Ram c7	Ram c6	Ram c5	Ram c4	Ram c3	Ram c2	Ram c1	Ram c0	
RAM_D	Ram d7	Ram d6	Ram d5	Ram d4	Ram d3	Ram d2	Ram d1	Ram d0] 🗖	RAM_D	Ram d7	Ram d6	Ram d5	Ram d4	Ram d3	Ram d2	Ram d1	Ram d0	
Init REG	I/0-x	Vddl	Vddl	Vddl	Vddl	Vaux	Cyclic	Cyclic		Init REG	I/O-x	Vddl	Vddl	Vddl	Vddl	Vaux	Cyclic	Cyclic	
Init Watchdog	WD2IN	MCU_O	OC-TIM	WD	WD_spi	WD_spi	WD	Crank] 🗖	Init Watchdog	WD2IN	MCU_O	OC-TIM	WD	WD_spi	WD_spi	WD	Crank	
Init LIN I/O	I/0-1	LIN_T2[LIN_T2[LIN_T1[LIN_T1[I/0-1	I/O-0	Cyc_Inv		Init LIN I/O	I/0-1	LIN_T2[LIN_T2[LIN_T1[LIN_T1[I/0-1	1/0-0	Cyc_Inv	
Save B	Batch	Re	efresh GUI		Se	nd	Cher	ck All 🔲		Save Bate	ch		Debug		Ser	nd	Chec	k Al 🔳	

4 Converter

The Converter hardware and software can be used in any application and in a standalone mode. In this case the FlexGUI interface is not used. The Converter software can be launched directly by launching the **Converter.exe**.

4.1 SPI converter

4.1.1 Features

Parameter	Setting	Description
CPOL	0	
CPUL	1	Clock polarity
CPHA	0	Clock Phase
CFNA	1	Clock Fliase
	8	
Length	16	Frame length (in bits)
Lengin	24	
	32	
Start bit	MSB	Bit starting the communication
	LSB	
	25 MHz	
	12.5 MHz	
	7.69 MHz	
	4 MHz	
	2 MHz	
Baud rate	1 MHz	SPI baud rate
Dauditale	500 kHz	Siribauditate
	250 kHz	
	125 kHz	
	62.5 kHz	
	31.25 kHz	
	15.625 kHz	

4.1.2 Using the interface

SPI Converter has an internal buffer with a default size of 1024 bytes. Depending on the configured SPI length, the user can write a number of SPI frames in one write access <u>Table 2</u>. One write access means the maximum length of the sequence sent (when the GUI is used) or the maximum length of a message written to the virtual COM port (when a terminal is used instead of the standard GUI).

able 2. SPI buffer size							
SPI length	Max . number of frames written in one write access						
8	1024						
16	512						
24	256						
32	128						

4.1.3 Delay between SPI commands

The delay between two SPI commands depends on the software execution time (for example, function, calls) and is not defined. An additional delay is can be induced by adding a "zero" command. An SPI command that contains only zeros is considered to be a delay of **xy**.

4.1.4 Sending a sequence

SPI Converter allows the user to create and load sequences that are stored in an Excel format. Clicking the **Excel Log** button causes a new sequence file to be created using the current values in the **Received** and **Sent** textboxes. This Excel file can then be saved and loaded again when necessary. Loading the file can be done by clicking on the **Load Sequence** button.

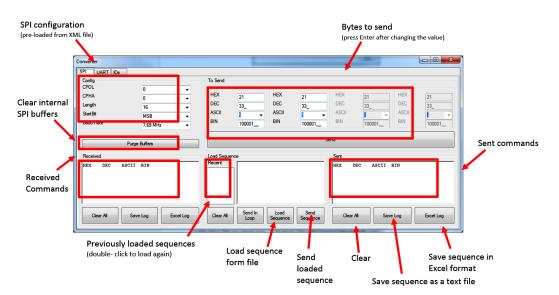
A sequence that has already been loaded can be re-loaded by double-clicking on the sequence name (for example, InitSequence.xlsx from the previous image). By loading several sequences in the same time, sequences can be combined as needed. A newly loaded sequence is always put after the previously loaded one.

Sequences can be generated by the program or created manually in Excel. The important requirement for the Excel sheet is the format of the first column and name of the worksheet. The name Converter_log must be used for the worksheet where the sequence is located. The data to send must be placed in the first column in a hexadecimal format without any special formatting as shown in Figure 1. The other columns are used for information only and are not important when the data is sent through SPI.

Flexible Graphical User Interface (FlexGUI)

x	1 5	- ¢- ;	-			Abbung Taitu	ley Event			?	A –	
_						ANvas_Init.x				-		
		ME INSER			RMULAS	DATA REV	/IEW VIEW	/ DEVELOP	PER ACRO	BAT TEAM	Marek To	- O
	* *	Calibri	- 11 -							🚰 Insert 🔹		
		в <i>I</i> <u>U</u>	- A A	′≡≡:	- 🖽 -	달 - %	> 🐺 For	mat as Table	-	🏝 Delete 🔹	👽 - 🕯	6 -
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Clij	pboard 🗔	Fo	nt r	Alignm	ent 🕞	Number	G.	Styles		Cells	Editin	g /
	-											
F5	5	▼ :]	\times \checkmark	<i>fx</i>								
	Α	В	С	D	E	F	G	н	I	J	к	L
1	HEX - TX	DEC - TX	ASCII - TX	BIN - TX			HEX - RX	DEC - RX	ASCII - RX	BIN - RX		
2	DF80	57216		110111111	0000000		2502	9474	%	1001010000	00010	
3	5A00	23040	Z	101101000	000000		2408	9224	\$	100100000	01000	
4	60C0	24768	•	110000011	000000		2408	9224	\$	100100000	01000	
5												
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		Conve	rter_log	Log_data	Sheet1	•		: •				
REA	ADY 🔠							III			+	100 %
Fi	gure 1.	Conver	ter log f	ormat								

4.1.5 Converter SPI description



4.2 UART converter

4.2.1 Features

UART Converter allows the user to receive and transmit asynchronous frames with the following settings:

Table 3. UART con	verter settings	
Parameter	Setting	Description
	None	
Parameter	Odd	Frame Parity
	Even	
HW handshake	None	No hardware handshake used (fixed configuration)
Length	8	Frame length (in bits) – fixed configuration
Stop bito	1	Number of step bits
Stop bits	2	- Number of stop bits
Start bit	MSB	Bit starting the communication
	LSB	
Baud rate	From 1 466 to 11 999 999	UART Baud Rate. Baud rate can be set to any value in the specified range. To set the value, write the value to the edit box and press "Enter".

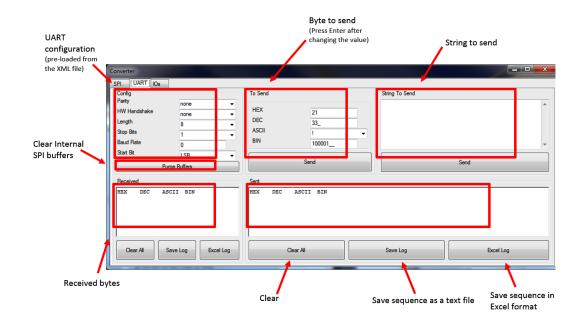
4.2.2 Using the interface

UART Converter works as a standard non-buffered UART/USB converter. Before operation, the converter must be configured properly. The baud rate must be entered in the edit box and validated by pressing **Enter**. If the baud rate is not within the acceptable limits, the converter will not take it into account. Data can be sent in a byte mode or as a string of characters. Received data can be stored in a text or Excel format.

Note:

The value of the Send data may be written in any format (HEX, DEC, ASCII, BIN), but the **Enter** key must always be pressed to validate the choice. When **Enter** is pressed, the other fields synchronize automatically.

Flexible Graphical User Interface (FlexGUI)



4.2.3 Converter UART description

4.3 IO converter

The IO Converter tab includes all special IO functions such as analog inputs, analog outputs, digital IOs, and square generator.

4.3.1 IO converter description

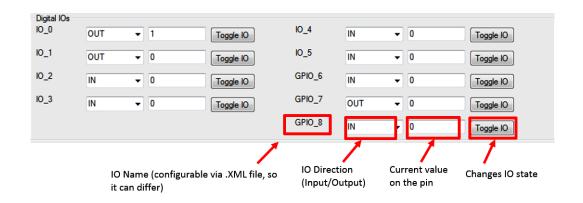
							Refresh period	of analog and digital IOs	
	G	PIO configu	ration					1	
Converter								×	
SPI UART	T KOs		4						
- Digital IOs IO_0	OUT	▼ 1	Toggle IO	IO_4	IN	▼ 0	Toggle IO	Config Refresh Period Analog IOs DISABLED	Launch a single ADC measurement
10_1	OUT	▼ 0	Toggle IO	IO_5	IN	- 0	Toggle IO	Refresh Period	
10_2	IN	• 1	Toggle IO	GPIO_6	IN	• 0	Toggle IO	Single Measurement	ſ
IO_3	IN	• 0	Toggle IO	GPIO_7	OUT	v 0	Toggle IO	Purge Buffers	L
				GPIO_8	IN	• 0	Toggle IO		
Analog IN MUX_OUT		6,545		Analog O AN_OUT		r(]	Square Generator OUT Square signal ON/OFF	Purge internal
Vcore Vcca		3,279		AN_OUT	1	,			IO buffers
Vaux		3,237		-	-	,		250 kHz 👻	
Vcan		4,965		AN_OUT	_2	-		I I	
AN_IN_5 AN_IN_6		2,492		AN OUT	3	,		I I	
AN_IN_7		0,715				,	<u></u>		
		1				1		1	
	Analog	g inputs (V)			Ana	alog output	ts Squar	e signal generator	

FLEXGUIUG

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4.3.2 Digital IOs

IO Converter provides nine general purpose IOs. These can be configured as inputs or outputs. Default pin direction and the pin name are loaded from the configuration file. The pin direction can be changed any time. The change is applied immediately once the selection is made in the combo box. The current value of each digital output can be changed by pushing the **Toggle IO** button.



Note:

Be careful when configuring the pins as outputs. Configuring an input pin as an output may damage the MCU. Protection of the digital IOs can differ on different types of hardware. See the appropriate schematics for details.

4.3.2.1 Refreshing IOs

Current state of the digital IOs is read (refreshed) periodically only if the refresh period is enabled. To enable this refreshing, select one of the pre-defined period in the appropriate combo box as shown below. Refresh period is a product of the following equation:

$$T_{REF} = x * T_{CYC}$$

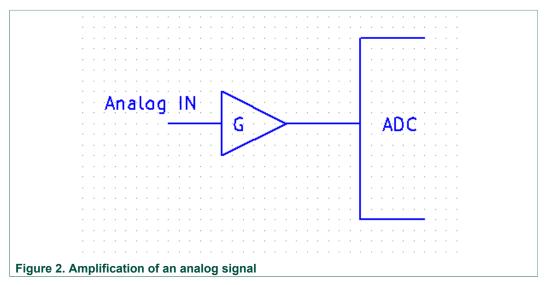
Where "x" is the selected number and T_{CYC} is the cycle time.

Flexible Graphical User Interface (FlexGUI)

Config Refresh Period Analog IOs	DISABLED 👻]
Refresh Period Digital IOs	1 DISABLED	
Single Mea	2	
Purge E	3 4 5	
	6 7	
	8 9	

4.3.3 Analog inputs

Eight analog inputs are available in the Analog IN group box. The analog signals must be amplified before going to the ADC. This can be done using an operational amplifier as shown in Figure 2. The amplifier has a gain G, which must be defined in the configuration file.



The final value of each analog input displayed in the GUI is then computed using the formula shown in Figure 3. The constants used in this formula are defined in the .XML configuration file. The number coming from the ADC is "x". An example is given for x =2646.

Flexible Graphical User Interface (FlexGUI)

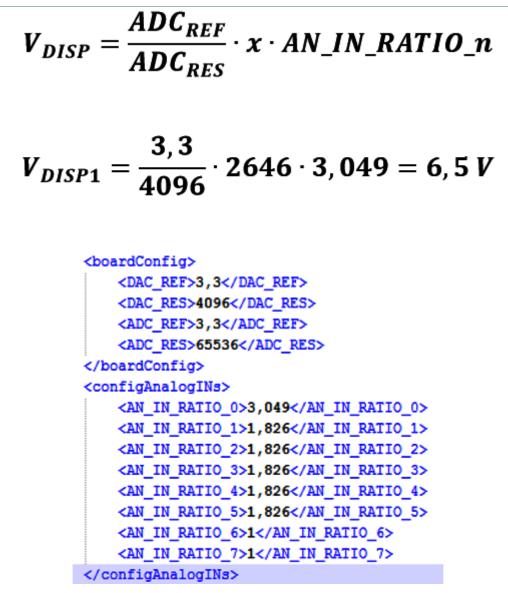
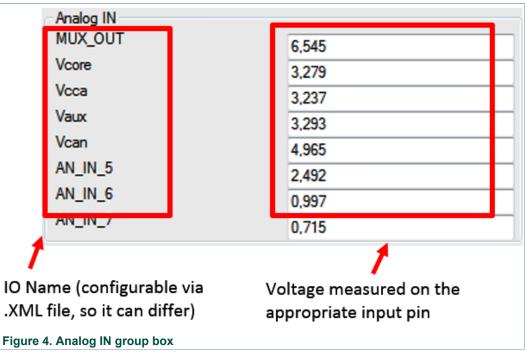


Figure 3. ADC constants in the .XML configuration file

Group box **Analog IN** shows the values previously measured (see <u>Figure 4</u>). All values are the final values in Volts (already multiplied by the appropriate coefficient).

Flexible Graphical User Interface (FlexGUI)



Note:

Analog values can be refreshed manually (using the **Single Measurement** button) or automatically with a refreshing period.

4.3.4 Analog outputs

Converter provides four analog outputs generated by a 12-bit DAC. The out-going value can be changed using a slider. Actual value of the appropriate output is displayed in a tooltip help. The first value gives the number of points from the 12-bit scale and the second value gives the output voltage.

Analog OUT AN_OUT_0	
AN_OUT_1	632 0,5091797 V
AN_OUT_2	
AN_OUT_3	
Figure 5. Analog outputs	

4.3.5 Square Signal Generator

Converter includes one square signal generator with several fix frequencies. The frequency can be changed using the combo box.

	Square Generator OUT	
	Square signal ON/OFF	
	250 kHz 👻	
Figure 6. Square sign	al generator	

4.3.6 Configuration

Analog and digital input values can be read with different periods. These periods are predefined and set by using the appropriate combo boxes. Analog values can also be measured on request using the **Single Measurement** button. The reading period is a product of multiplication.

FLEXGUIUG

16 / 23

Flexible Graphical User Interface (FlexGUI)

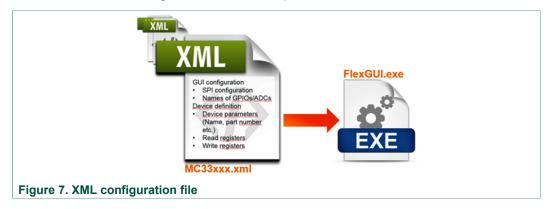
Config Refresh Period Analog IOs	DISABLED -					
Refresh Period Digital IOs	1 -					
Single Measurement						
Purge Buffers						

 $T_{REF} = x * T_{CYC}$

5 **FlexGUI**

5.1 Using FlexGUI interface

FlexGUI is an interface that can be adapted to the needs of a specific IC without compiling code. The target IC is fully described in an XML file. The XML file specifies the configuration of communication interfaces, registers, names of signals and so forth. This file is then selected during the FlexGUI start-up.



5.2 XML configuration file

The XML configuration file is easy to read and to modify. All the .XML files that are delivered with the GUI can be found in the **Sequences&Config** folder (in the default installation directory). Structure of the .XML file is defined and must be maintained.

XML parameter	Description
<device></device>	
<configuration></configuration>	SPI, UART, GPIOs, ADC, DAC configuration
<pre><parameters></parameters></pre>	Partnumber, version, date etc.
<comconfig></comconfig>	SPI length
<readregsparameter< td=""><td>Parameters of the read registers (number of bits etc.)</td></readregsparameter<>	Parameters of the read registers (number of bits etc.)
<writeregsparamete< td=""><td>Parameters of the write registers (number of bits etc.)</td></writeregsparamete<>	Parameters of the write registers (number of bits etc.)
<readtabnames></readtabnames>	Names of the read tabs
<readregistersorde< td=""><td>Crder of the read registers in the GUI</td></readregistersorde<>	Crder of the read registers in the GUI
<readregisters></readregisters>	Definition of all read registers
<writetabnames></writetabnames>	Names of the write tabs
<writeregistersord< td=""><td>ers> Order of the write registers in the GUI</td></writeregistersord<>	ers> Order of the write registers in the GUI
<writeregisters></writeregisters>	Definition of all write registers

5.3 Interface description

FlexGUI application is divided into two parts: Read registers and Write registers. Read registers are placed on the left side, Write ones on the right side. Both types of commands can be sent by clicking the **Read**" or "**Write** buttons.

Only the commands that are checked and that are on the active tab are sent. All commands that were sent are stored in the **Received** and **Sent** textboxes in the Converter application. Read commands cannot be configured using GUI. A Read command consists of an address and a mask as defined in the .XML file. A Write command can be changed by clicking directly on the appropriate bit.

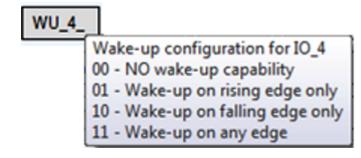
lead Reg 1 Rea	nd Reg 2 R	ead Reg 3	Read Reg 4	Read Reg	5 Read Reg	6				Write Reg 1 Writ	e Reg 2 Wi	te Reg 3 V	Vnte Reg 4						
INIT_VREG	ICCA_LI	TCCA_	IPFF_DI	VCAN_	RESER	TAUX_L	VAUX_	BA FAI		INIT_VREG	ICCA_LI	TCCA_	IPFF_DI	VCAN_	_0	TAUX_L	VAUX_	_	E
INIT_WU1	WU_IO0	WU_100	WU_102	WU_IO2	WU_IO3	WU_IO3	WU_104	WI 104		INIT_WU1	WU_100	WU_100	WU_IO2	WU_IO2	WU_103	WU_IO3	WU_104	w104	•
INIT_WU2	WU_IO5	WU_IO5	CAN_DI	CAN_W	RESER	LIN_J28	LIN_SR	LI		INIT_WU2	WU_105	WU_IO5	CAN_DI	CAN_W	_0	LIN_J28	LIN_SR	L LSR	
INIT_INT	INT_DU	INT_IN	INT_IN	INT_IN	INT_IN	INT_IN	INT_IN	IN _IN		INIT_INT	INT_DU	INT_IN	NT.AN		INT_IN	INT_IN	INT_IN	IN _IN	
INIT_INH_INT	RESER	RESER	RESER	INT_R	Sters	INT_IN	INT_IN	IN IN		INIT_INH_INT	_0	_0	_0	INT_IN	INT_IN	INT_IN	INT_IN	IN _IN	
LONG_DURA	F2	F1	FO	REG_S	LDT_R	MODE	LDT	LD _IN		LONG_DURA	F2	F1	FO	REG_S	CLEAR	MODE	LDT_E		
HW_CONFIG	LS_DE	RESER	VCCA_	VCCA_	VAUX_	1	DFS_H	DE 9_H		MODE	VKAM_	LP_OFF	GO_LP	INT_RE	Secure_	Secure_	Secure_	S cure_	. 8
WU_SOURCE	10_5_W	10_4_W	10_3_W	10_2_W	10_0_W	AUTO_	LDT_W	PHY_W		REG_MODE	VCORE	VCCA_	VAUX_	VCAN_	Secure_	Secure_	Secure_	Secure	. 8
Status SPI		u_g S	tatus	Rod	ietor	VPRE-G	VCORE	NOT	HERS										

Figure 9. FlexGUI Read, Write and Status registers

Note:

Tooltip help - Description for each bit can be displayed by putting mouse over the specific bit.

18 / 23



6 Windows 8/10 driver installation and troubleshooting

By default, Windows 8 requires verification of the driver's signature. The provided driver is not verified by Microsoft, so the verification always fails and the driver cannot be installed. To install this driver successfully, this verification must be disabled.

For Windows 8 systems, follow the below instructions to disable the verification:

- 1. Hold the **Shift** key down and click **Restart** (under Charms on the login screen) to reboot the computer.
- 2. Once the computer has rebooted, choose the **Troubleshoot** option.
- 3. Choose Advanced options.
- 4. Choose Startup Settings.
- 5. Click **Restart**. Computer reboots.
- 6. After rebooting, change the startup settings.
- 7. Change **Disable driver signature enforcement** by pressing **F7**. The PC reboots.

The driver can now be installed manually. Follow, the below instructions to install the driver:

- 1. Connect the Converter hardware.
- Go to Control Panel > System > Device Manager, and then select Ports (COM & LPT).
- 3. Select the Freescale Converter and right-click.
- 4. Select Update Driver Software....
- 5. Select Browse my computer for driver software.
- 6. Browse the FlexGUI installation folder and select **drivers/cdc.inf**. By default, the FlexGUI path is: **C:\Program Files (x86)\FlexGUI**\ or **C:\Program Files\FlexGUI**\.
- 7. Update the driver.

7 Revision history

Revision	Date	Description of changes
1.0	5/2016	Initial release

Flexible Graphical User Interface (FlexGUI)

8 Legal information

8.1 Definitions

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Flexible Graphical User Interface (FlexGUI)

Tables

Tab. 1.	SPI converter features6	
Tab. 2.	SPI buffer size7	

Tab. 3. UART converter settings9

Flexible Graphical User Interface (FlexGUI)

Figures

Fig. 1. Fig. 2. Fig. 3. Fig. 4.	Converter log format	
Fig. 4. Fig. 5.	Analog IN group box	

Fig. 6.	Square signal generator16
Fig. 7.	
Fig. 8.	XML file structure
Fig. 9.	FlexGUI Read, Write and Status registers18

Flexible Graphical User Interface (FlexGUI)

Contents

1	Flexible Graphical User Interface Principle	1
2	Introduction	2
2.1	System requirements	
2.2	Installing the FlexGUI on Your Computer	
3	Getting Started With the FlexGUI	4
3.1	Launching FlexGUI	4
4	Converter	
4.1	SPI converter	6
4.1.1	Features	
4.1.2	Using the interface	
4.1.3	Delay between SPI commands	
4.1.4	Sending a sequence	7
4.1.5	Converter SPI description	
4.2	UART converter	
4.2.1	Features	
4.2.2	Using the interface	
4.2.3	Converter UART description	
4.3	IO converter	
4.3.1	IO converter description	
4.3.2	Digital IOs	
4.3.2.1	Refreshing IOs	
4.3.3	Analog inputs	
4.3.4	Analog outputs	
4.3.5	Square Signal Generator	
4.3.6	Configuration	
5	FlexGUI	
5.1	Using FlexGUI interface	
5.2	XML configuration file	
5.3	Interface description	. 18
6	Windows 8/10 driver installation and	
	troubleshooting	
7	Revision history	
8	Legal information	. 20

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