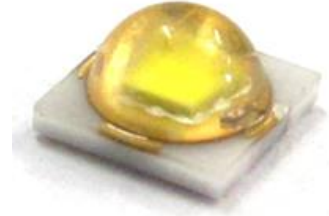


Applicable for automotive exterior light

Z Power LED – Z5-Series

SZ5-P0-W0-00



Product Brief

Description

- The Z-Power series is designed for high flux output applications with high current operation capability.
- It incorporates state of the art SMD design and low thermal resistant material.
- The Z5 Power LED is ideal light sources for automotive

Features and Benefits

- High Lumen Output and Efficacy
- Low Thermal Resistance
- ANSI compliant Binning
- ESD Class 3B
- MSL 2 Level
- Viewing angle 120°
- AEC-Q101 Qualified
- RoHS compliant

Key Applications

- Automotive Exterior Lighting
- Daytime running lamp (DRL)
- Fog lamp
- Position lamp

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- Precaution For Use
- Company Information

Performance Characteristics

Table 1. Electro Optical Characteristics, $I_F = 350\text{mA}$, $T_j = 25^\circ\text{C}$, RH30%

Parameter	Symbol	Min	Typ	Max	Unit
Forward Voltage ^[1]	V_F	2.75	3.2	3.5	V
Luminous Flux ^{[2] [1]}	Φ_V	109	137	150	lm
Color Coordinate ^{[3] [1]}	X		0.33		-
	Y		0.33		-
Viewing Angle ^[4]	$2\theta_{1/2}$		120		deg.
Optical Efficiency	η_{op}		128.1		lm/W
Thermal resistance ^[5]	$R_{th JS}$		5.5	6.5	$^\circ\text{C} / \text{W}$
Temperature coefficient of V_F $-40^\circ\text{C} \leq T \leq 125^\circ\text{C}$	TC_V		8		mV/ $^\circ\text{C}$

Notes :

(1) Tolerance : $V_F : \pm 0.1\text{V}$, $LF : \pm 7\%$, $x,y : \pm 0.005$

(2) The luminous flux was measured at the peak of the spatial pattern which may not be aligned with the mechanical axis of the LED package.

(3) Correlated Color is derived from the CIE 1931 Chromaticity diagram.

(4) $\Theta_{1/2}$ is the off-axis where the luminous intensity is 1/2 of the peak intensity

(5) Thermal resistance = $R_{th JA}$: Junction/ambient , $R_{th JS}$: Junction/solder point

PCB design for improved heat dissipation : area of 520 mm² per LED, Metal core PCB

※ Not applicable for reverse operation

Performance Characteristics

Table 2. Absolute Maximum Ratings

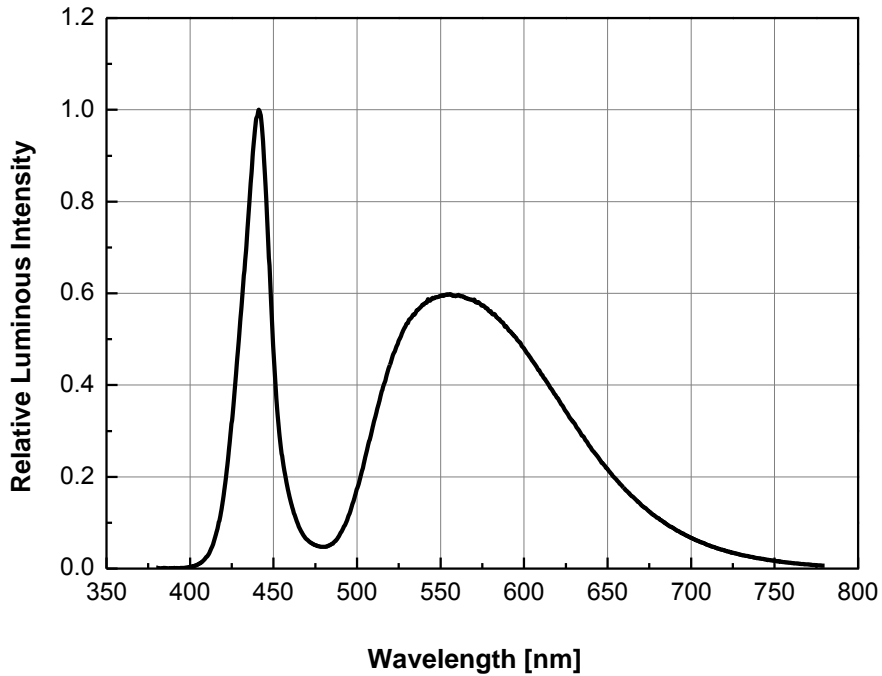
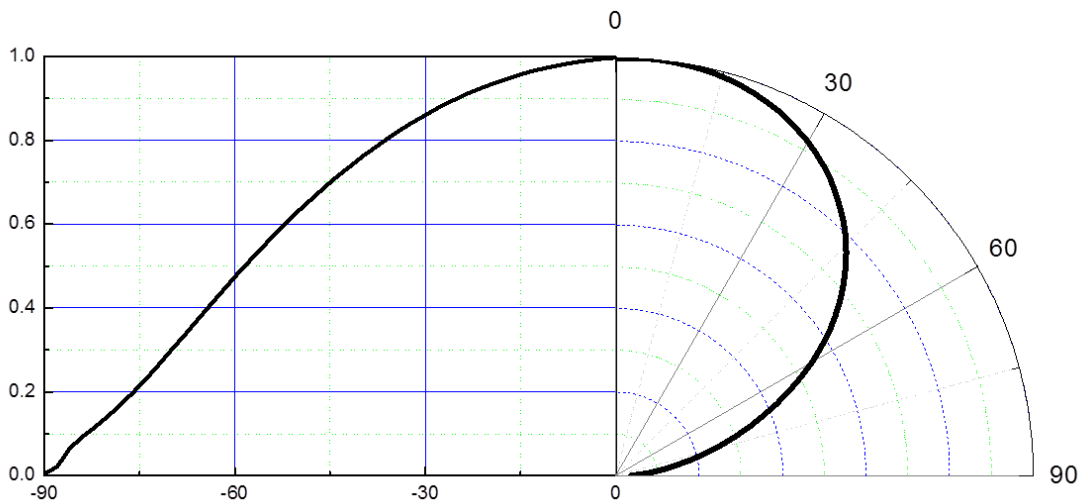
Parameter	Symbol	Value	Unit
Power Dissipation ($T_a=25^\circ\text{C}$)	P_d	3.5	W
Forward Current ($T_a=25^\circ\text{C}$)	I_F	1000	mA
Operating Temperature	T_{opr}	-40 ~ +125	$^\circ\text{C}$
Storage Temperature	T_{stg}	-40 ~ +125	$^\circ\text{C}$
Junction Temperature	T_j	150	$^\circ\text{C}$
Soldering Temperature	T_{sld}	Reflow Soldering : 260 $^\circ\text{C}$ for 10sec. Hand Soldering : 315 $^\circ\text{C}$ for 4sec.	
ESD ($R=1.5\text{k}\Omega$, $C=100\text{pF}$) ⁽¹⁾		ESD Class 3B (JESD22-A114-E)	kV

Notes :

(1) A ESD Protection device is included for protection.

- LED's properties might be different from suggested values like above and below tables if operation condition will be exceeded our parameter range. Care is to be taken that power dissipation does not exceed the absolute maximum rating of the product.
- All measurements were made under the standardized environment of Seoul Semiconductor.

Relative Spectral Distribution

Fig 1. Color Spectrum, $I_F = 350\text{mA}$, $T_j = 25^\circ\text{C}$, RH30%

Fig 2. Characteristic diagrams, $I_F = 350\text{mA}$, $T_j = 25^\circ\text{C}$, RH30%


Forward Current Characteristics

Fig 3. Forward Voltage vs. Forward Current , $T_j = 25^\circ\text{C}$

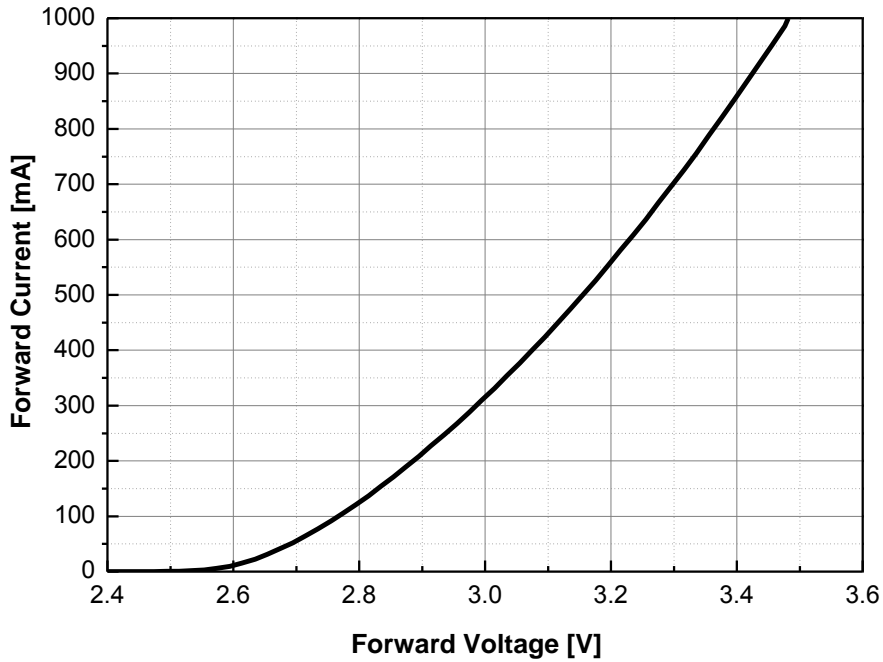
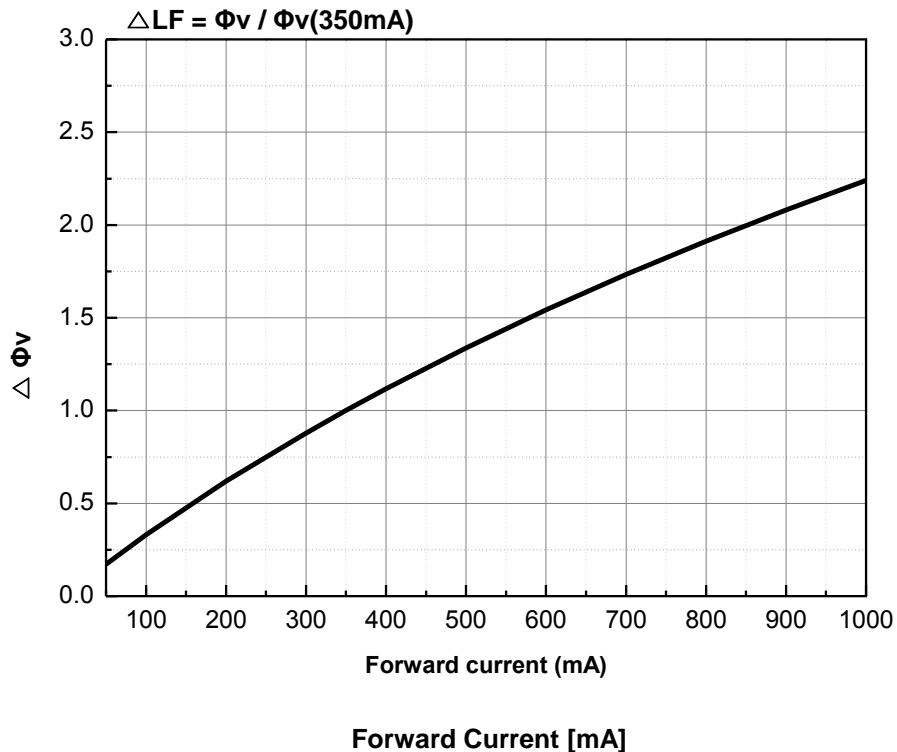
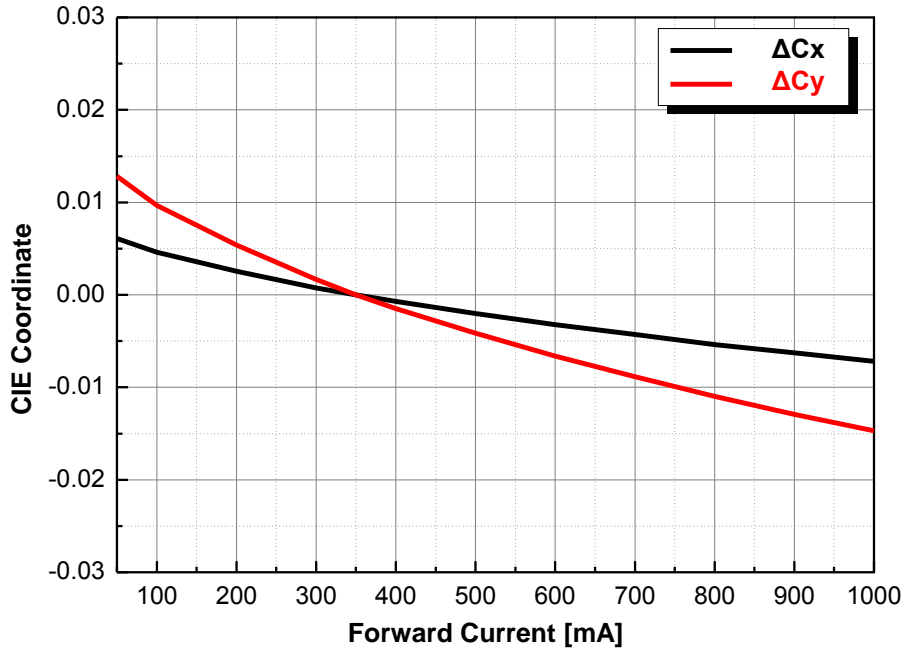


Fig 4. Forward Current vs. Relative Luminous flux, $T_j = 25^\circ\text{C}$

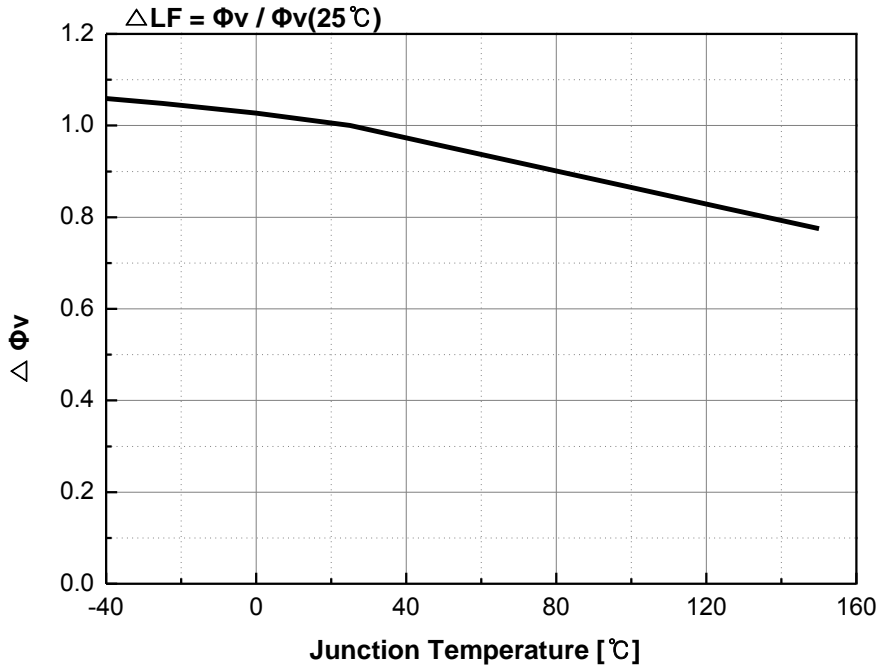
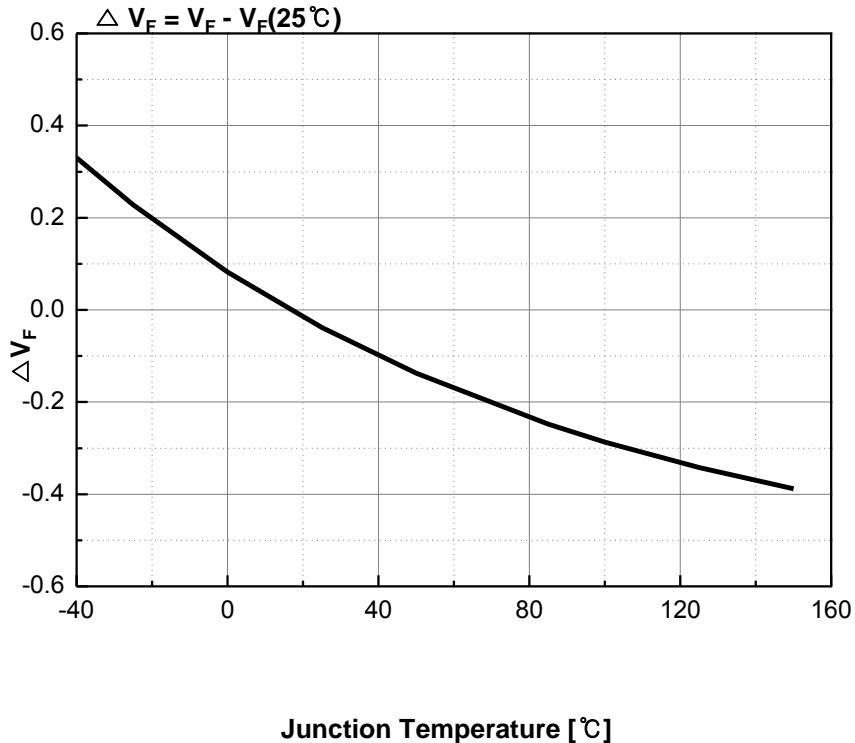


Forward Current Characteristics

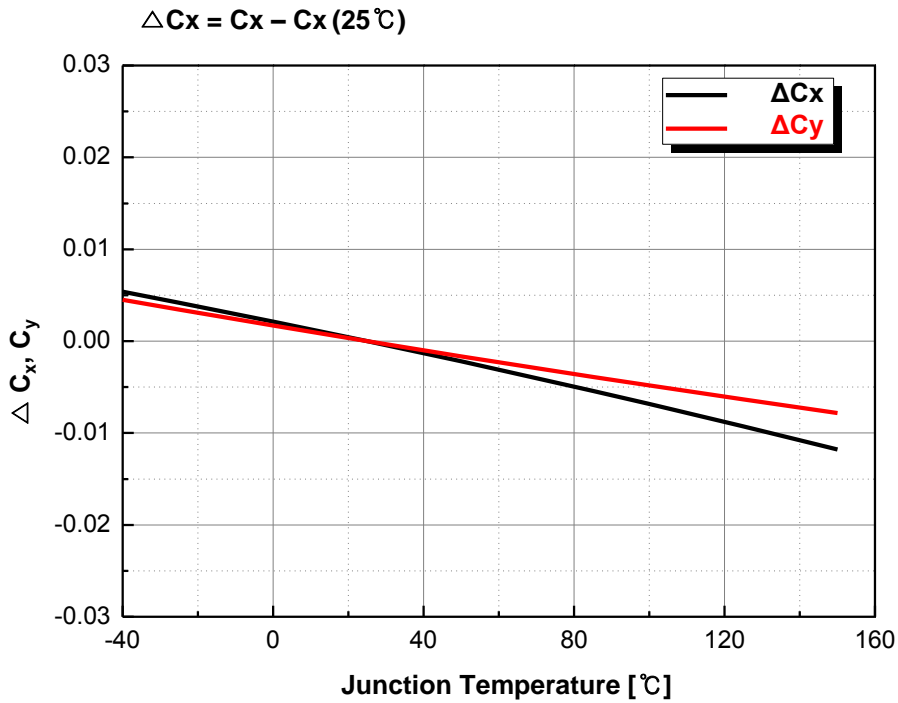
Fig 5. Forward Current vs. CIE X, Y Shift , $T_j = 25^\circ\text{C}$



Junction Temperature Characteristics

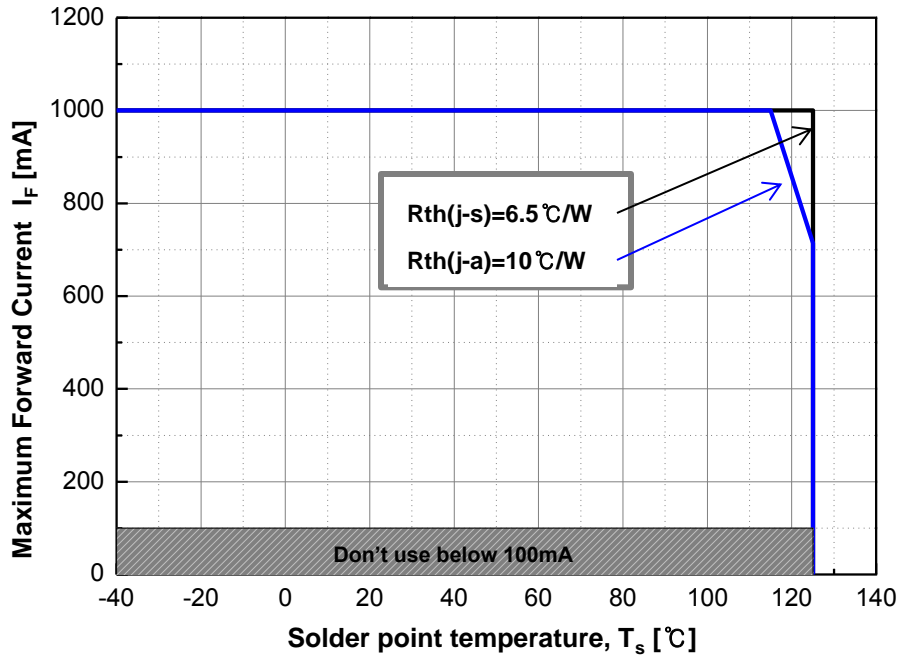
Fig 6. Relative Light Output vs. Junction Temperature, $I_F = 350\text{mA}$

Fig 7. Junction Temperature vs. Forward Voltage Shift, $I_F = 350\text{mA}$


Junction Temperature Characteristics

Fig 8. Chromaticity Coordinate vs. Junction Temperature, $I_f = 350\text{mA}$


Ambient Temperature Characteristics

Fig 9. Maximum Forward Current vs. Solder point temperature, $T_j(\text{max.})=150^\circ\text{C}$



Color Bin Structure

Part Number	Luminous Flux (lm) @ I _F = 350mA			Color Chromaticity Coordinate @ I _F = 350mA			Forward Voltage (V _F)		
	Bin Code	Min.	Max.	Bin Code	Min.	Max.	Bin Code	Min.	Max.
SZ5-P0-W0-00	V1	118.5	130	Refer to page.13			G	2.75	3.00
	V2	130	140				H	3.00	3.25
	V3	140	150				I	3.25	3.50
	W1	150	160						

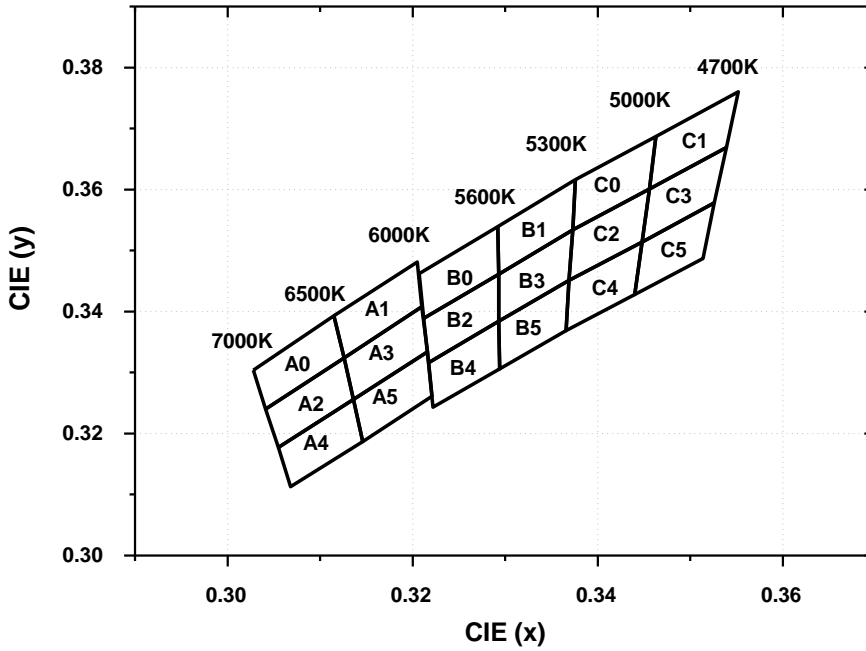
Available ranks

***Notes :**

(1) All measurements were made under the standardized environment of Seoul Semiconductor
In order to ensure availability, single color rank will not be orderable.

Color Bin Structure

CIE Chromaticity Diagram, $I_F = 250\text{mA}$, $T_j = 25^\circ\text{C}$,



A0		A1		A2		A3		A4	
CIE x	CIE y	CIE x	CIE y	CIE x	CIE y	CIE x	CIE y	CIE x	CIE y
0.3028	0.3304	0.3115	0.3393	0.3041	0.3240	0.3126	0.3324	0.3055	0.3177
0.3041	0.3240	0.3126	0.3324	0.3055	0.3177	0.3136	0.3256	0.3068	0.3113
0.3126	0.3324	0.3210	0.3408	0.3136	0.3256	0.3216	0.3334	0.3146	0.3187
0.3115	0.3393	0.3205	0.3481	0.3126	0.3324	0.3210	0.3408	0.3136	0.3256
A5		B0		B1		B2		B3	
CIE x	CIE y	CIE x	CIE y	CIE x	CIE y	CIE x	CIE y	CIE x	CIE y
0.3136	0.3256	0.3207	0.3462	0.3292	0.3539	0.3212	0.3389	0.3293	0.3461
0.3146	0.3187	0.3212	0.3389	0.3293	0.3461	0.3217	0.3316	0.3293	0.3384
0.3221	0.3261	0.3293	0.3461	0.3373	0.3534	0.3293	0.3384	0.3369	0.3451
0.3216	0.3334	0.3292	0.3539	0.3376	0.3616	0.3293	0.3461	0.3373	0.3534
B4		B5		C0		C1		C2	
CIE x	CIE y	CIE x	CIE y	CIE x	CIE y	CIE x	CIE y	CIE x	CIE y
0.3217	0.3316	0.3293	0.3384	0.3376	0.3616	0.3463	0.3687	0.3373	0.3534
0.3222	0.3243	0.3294	0.3306	0.3373	0.3534	0.3456	0.3601	0.3369	0.3451
0.3294	0.3306	0.3366	0.3369	0.3456	0.3601	0.3539	0.3669	0.3448	0.3514
0.3293	0.3384	0.3369	0.3451	0.3463	0.3687	0.3552	0.3760	0.3456	0.3601
C3		C4		C5					
CIE x	CIE y	CIE x	CIE y	CIE x	CIE y				
0.3456	0.3601	0.3369	0.3451	0.3448	0.3514				
0.3448	0.3514	0.3366	0.3369	0.3440	0.3428				
0.3526	0.3578	0.3440	0.3428	0.3514	0.3487				
0.3539	0.3669	0.3448	0.3514	0.3526	0.3578				

Available ranks

***Notes :**

•Measurement Uncertainty of the Color Coordinates : ± 0.005

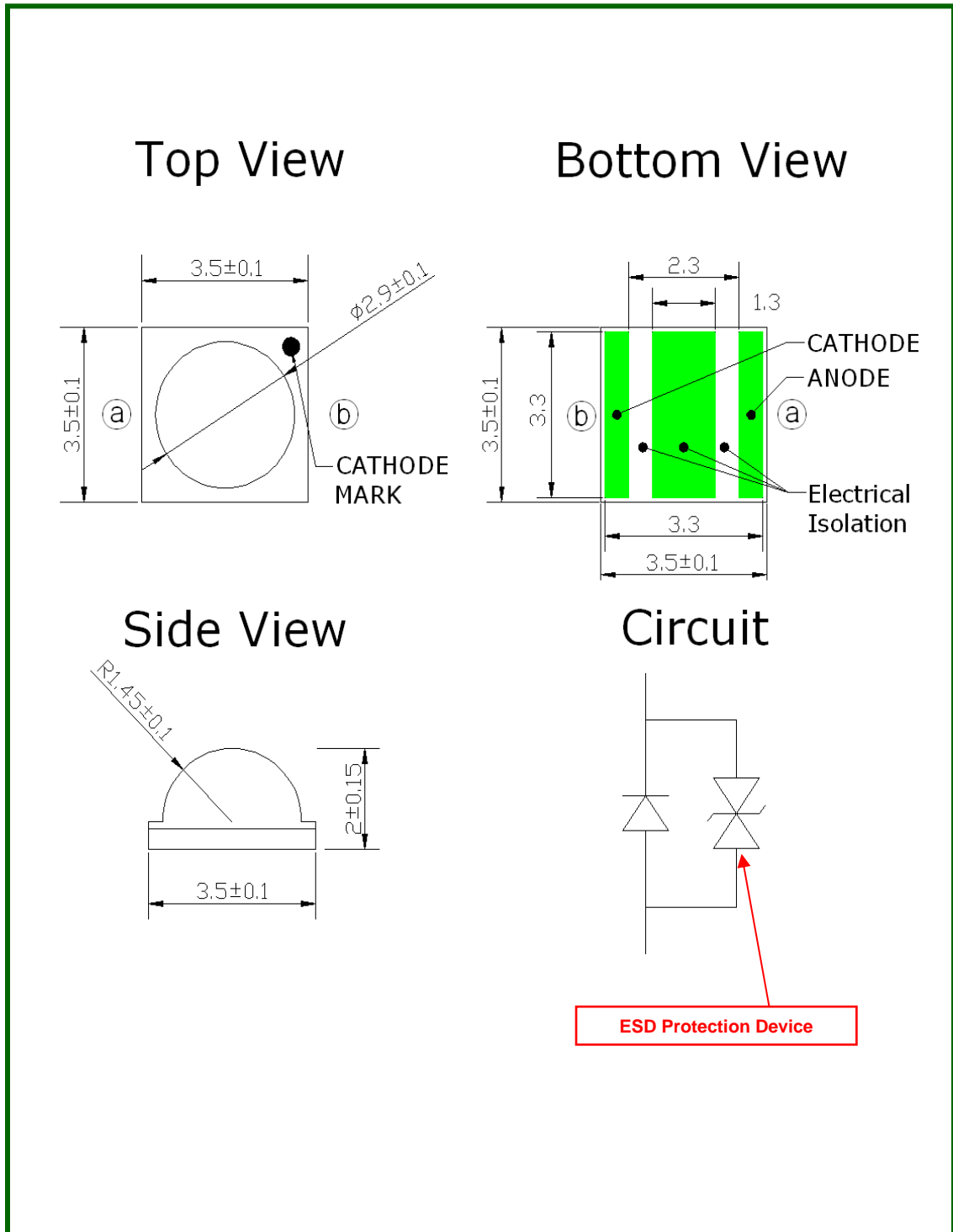
Reliability Test

Test Item	Standard Test Method	Test Condition	Duration / Cycle	Number Of Test
External Visual	JESD22 B-101	Visual inspection	-	77
D.P.A	AEC-Q101-004	Random Sample H3TRB,HAST,TC	-	5
ESD	JESD22 A-114	Human-body mode, R=1.5k Ω , C = 100pF	3 times Negative/ Positive	30
Physical Dimension	JESD22 B-100	Verify physical dimensions against device mechanical drawing	3 times	30
Temperature cycling	JESD22 A-104	Tc= -40 ~ 125°C, 30 min. dwell, 5 min transfer, 1000 cycles	1000hrs	77
Power Temperature Cycle	JESD22 A-105	Ta=-40°C~125°C, If =700mA, 20 min dwell / 20 min transition (1 hour cycle), 2 min ON / 2 min OFF	1000hrs	77
High Humidity High Temp. Operating Life	JESD22 A-101	85°C/85% RH, @ 1000mA 1hr ON/1hr OFF	1000hrs	77
High Temperature Operating Life	JESD22 A-108C	Ta= 100°C, If =1000mA	1000hrs	77
Solderability	JESD22B-102	Bake : 150 °C Dipping terminal to 245 °C	-	10
Resistance solder heat	JESD22 A-111	Preconditioned at MSL 2 Bake : 125 °C, Soak : 60 °C, 60 % R.H. Full body immersion to 260 °C.	-	10
Thermal Resistance	JESD24	Measure TR to assure specification compliance	-	10
Wire bond strength	MIL-STD-750	Pull test to assure specification compliance	-	10
Bond shear	AEC-Q101-003	Shear test to assure specification compliance	-	10
Die shear	MIL-STD-750	Shear test to assure specification compliance	-	10
Low Temperature Operating Life	SSC (Internal standard)	Ta=-40°C, IF=1000mA	1000hrs	77

Criteria for Judging the Damage

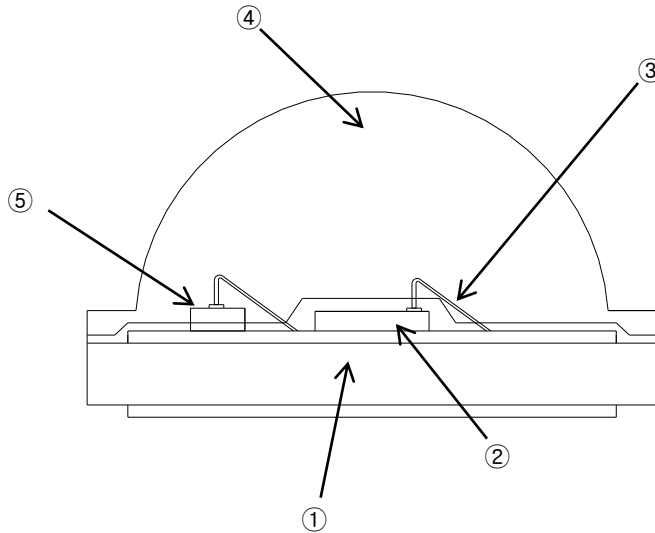
Item	Symbol	Condition	Criteria for Judgment	
			MIN	MAX
Forward Voltage	V_F	$I_F=350mA$	-	Initial \times 1.1
Luminous Intensity	I_V	$I_F=350mA$	Initial \times 0.8	-

Mechanical Dimensions / Material Structure



- (1) All dimensions are in millimeters.
- (2) Scale : none
- (3) Undefined tolerance is ± 0.1 mm

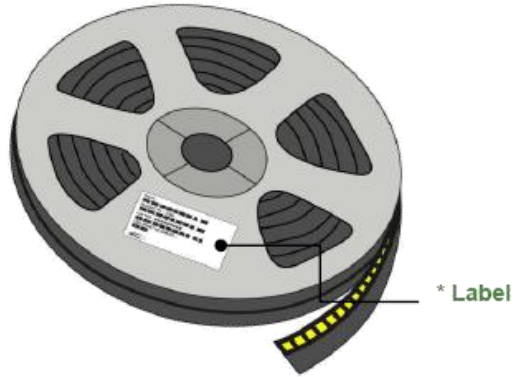
Mechanical Dimensions / Material Structure



Parts No.	Name	Description	Materials
①	Substrate	Metal Ceramic	Copper Alloy (Gold Plated)
②	Chip Source	Blue LED	GaN on Sapphire
③	Wire	Metal	Gold Wire
④	Encapsulation	Silicone	+Phosphor
⑤	ESD Protection Device	Si	-

Reel Packaging

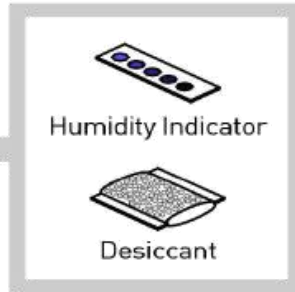
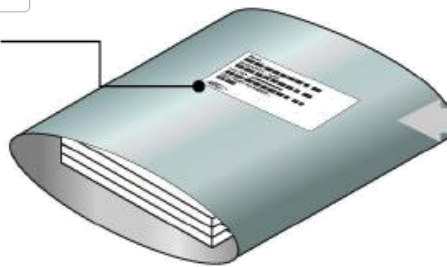
Reel



Aluminum Bag



* Label



Humidity Indicator

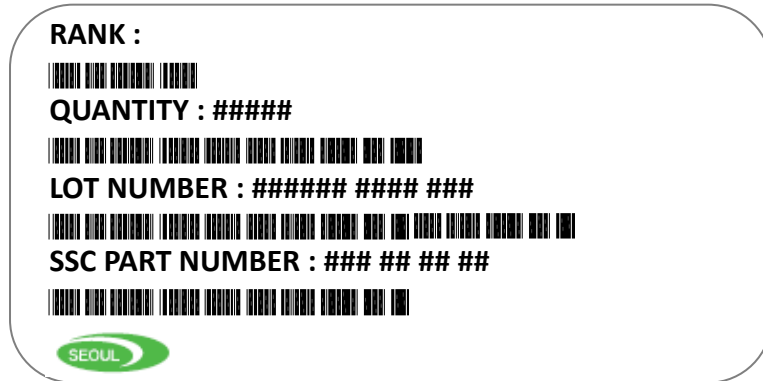
Desiccant

Outer Box



* Label

Product Nomenclature

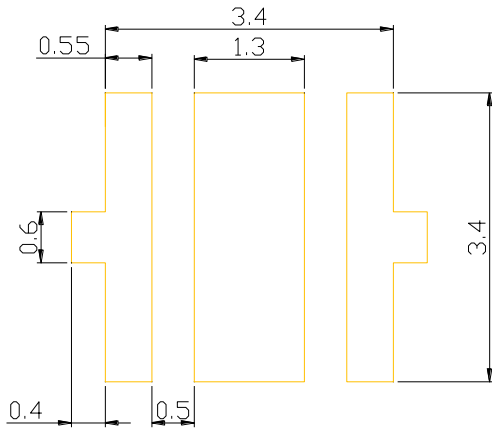
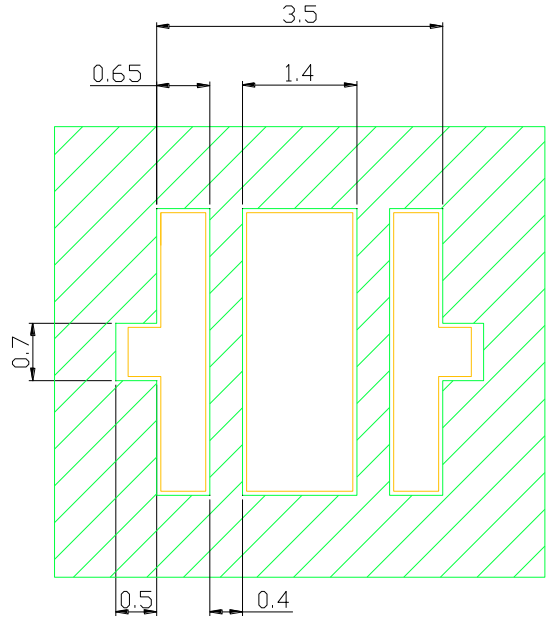
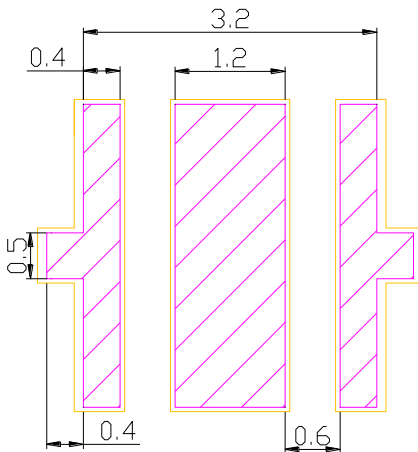

Table 5. Part Numbering System : X₁X₂X₃X₄X₅X₆X₇X₈

Part Number Code	Description	Part Number	Value
X ₁	Company	S	Company
X ₂	Z-Power LED series number	Z	Z-Power LED series
X ₃ X ₄	Color Specification	W0	Pure white
X ₅	PKG series	5	Z5 series
X ₆	Lens Type	A	Dome Type
X ₇	PCB Type	0	Emitter Type
X ₈	Revision No.	B	B Version

Table 6. Lot Numbering System : Y₁Y₁Y₂Y₃Y₄Y₅Y₅Y₅ - Y₆Y₆Y₆ - Y₇Y₇Y₇ - Y₈Y₈Y₈Y₈Y₈

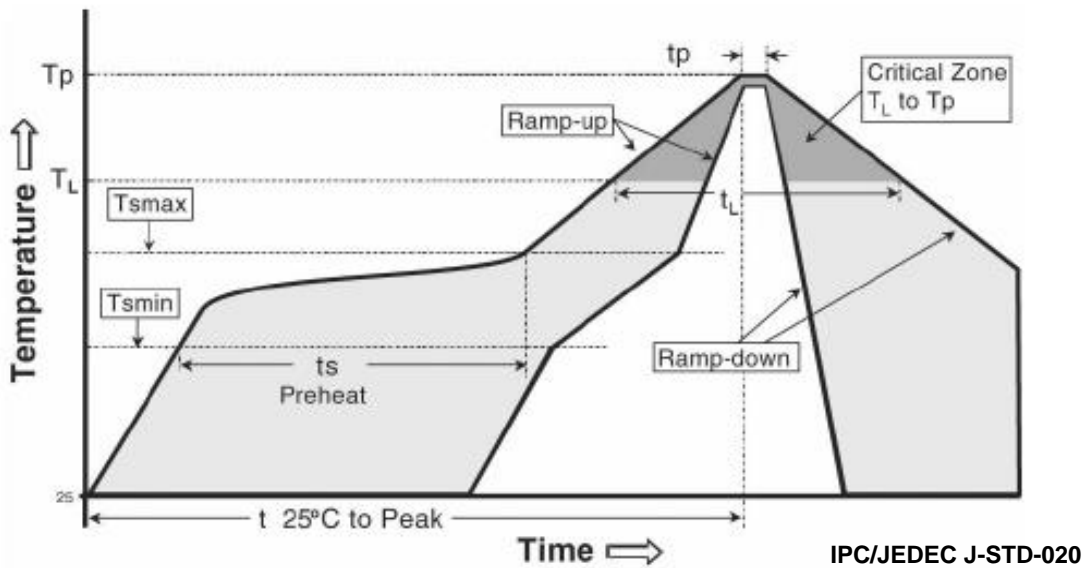
Lot Number Code	Description
Y ₁	Year
Y ₂	Month
Y ₃	Day
Y ₄	Production area
Y ₅	Mass order
Y ₆	Taping number
Y ₇	Reel number
Y ₈	Internal management number

Recommended Solder Pad

Recommended PCB Solder Pad

Solder Resist

Solder Resist
Recommended Stencil Pattern

Solder Stencil
Notes :

- (1) All dimensions are in millimeters.
- (2) Scale : none
- (3) This drawing without tolerances are for reference only.
- (4) Undefined tolerance is $\pm 0.1\text{mm}$.

Reflow Soldering Characteristics


Table 7.

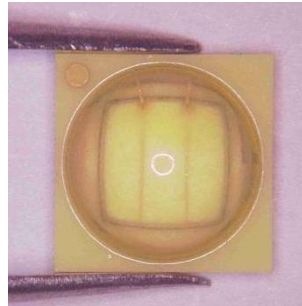
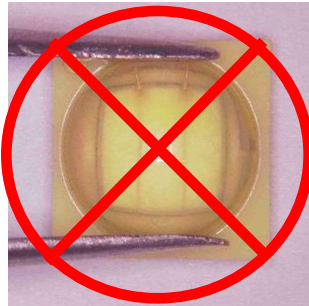
Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Average ramp-up rate (T _{max} to T _p)	3° C/second max.	3° C/second max.
Preheat - Temperature Min (T _{min}) - Temperature Max (T _{max}) - Time (T _{min} to T _{max}) (t _s)	100 °C 150 °C 60-120 seconds	150 °C 200 °C 60-180 seconds
Time maintained above: - Temperature (T _L) - Time (t _L)	183 °C 60-150 seconds	217 °C 60-150 seconds
Peak Temperature (T _p)	215 °C	260 °C
Time within 5°C of actual Peak Temperature (t _p) ²	10-30 seconds	20-40 seconds
Ramp-down Rate	6 °C/second max.	6 °C/second max.
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.

Caution

- (1) Reflow soldering is recommended not to be done more than two times. In the case of more than 24 hours passed soldering after first, LEDs will be damaged.
- (2) Repairs should not be done after the LEDs have been soldered. When repair is unavoidable, suitable tools must be used.
- (3) Die slug is to be soldered.
- (4) When soldering, do not put stress on the LEDs during heating.
- (5) After soldering, do not warp the circuit board.

Handling of Silicone Resin for LEDs

- (1) During processing, mechanical stress on the surface should be minimized as much as possible. Sharp objects of all types should not be used to pierce the sealing compound.



- (2) In general, LEDs should only be handled from the side. By the way, this also applies to LEDs without a silicone sealant, since the surface can also become scratched.
- (3) When populating boards in SMT production, there are basically no restrictions regarding the form of the pick and place nozzle, except that mechanical pressure on the surface of the resin must be prevented. This is assured by choosing a pick and place nozzle which is larger than the LED's reflector area.
- (4) Silicone differs from materials conventionally used for the manufacturing of LEDs. These conditions must be considered during the handling of such devices. Compared to standard encapsulants, silicone is generally softer, and the surface is more likely to attract dust. As mentioned previously, the increased sensitivity to dust requires special care during processing. In cases where a minimal level of dirt and dust particles cannot be guaranteed, a suitable cleaning solution must be applied to the surface after the soldering of components.
- (5) Seoul Semiconductor suggests using isopropyl alcohol for cleaning. In case other solvents are used, it must be assured that these solvents do not dissolve the package or resin. Ultrasonic cleaning is not recommended. Ultrasonic cleaning may cause damage to the LED.
- (6) Please do not mold this product into another resin (epoxy, urethane, etc) and do not handle this product with acid or sulfur material in sealed space.
- (7) Avoid leaving fingerprints on silicone resin parts.

Precaution for Use

(1) Storage

To avoid the moisture penetration, we recommend storing Z5 Series LEDs in a dry box with a desiccant. The recommended storage temperature range is 5 °C to 30 °C and a maximum humidity of RH50%.

(2) Use Precaution after Opening the Packaging

Use proper SMT techniques when the LED is to be soldered dipped as separation of the lens may affect the light output efficiency.

Pay attention to the following:

a. Recommend conditions after opening the package

- Sealing

- Temperature : 5 ~ 30 °C Humidity : less than RH60%

b. If the package has been opened more than 1 year (MSL_2) or the color of the desiccant changes, components should be dried for 10-24hr at 65±5 °C

(3) Do not apply mechanical force or excess vibration during the cooling process to normal temperature after soldering.

(4) Do not rapidly cool device after soldering.

(5) Components should not be mounted on warped (non coplanar) portion of PCB.

(6) Radioactive exposure is not considered for the products listed here in.

(7) Gallium arsenide is used in some of the products listed in this publication. These products are dangerous if they are burned or shredded in the process of disposal. It is also dangerous to drink the liquid or inhale the gas generated by such products when chemically disposed of.

(8) This device should not be used in any type of fluid such as water, oil, organic solvent and etc. When washing is required, IPA (Isopropyl Alcohol) should be used.

(9) When the LEDs are in operation the maximum current should be decided after measuring the package temperature.

(10) LEDs must be stored in a clean environment. We recommend LEDs store in nitrogen-filled container.

(11) The appearance and specifications of the product may be modified for improvement without notice.

(12) Long time exposure of sunlight or occasional UV exposure will cause lens discoloration.

Precaution for Use

(13) VOCs (Volatile organic compounds) emitted from materials used in the construction of fixtures can penetrate silicone encapsulants of LEDs and discolor when exposed to heat and photonic energy. The result can be a significant loss of light output from the fixture. Knowledge of the properties of the materials selected to be used in the construction of fixtures can help prevent these issues.

(14) The slug is electrically isolated.

(15) Attaching LEDs, do not use adhesives that outgas organic vapor.

(16) The driving circuit must be designed to allow forward voltage only when it is ON or OFF. If the reverse voltage is applied to LED, migration can be generated resulting in LED damage.

(17) LEDs are sensitive to Electro-Static Discharge (ESD) and Electrical Over Stress (EOS). Below is a list of suggestions that Seoul Semiconductor purposes to minimize these effects.

a. ESD (Electro Static Discharge)

Electrostatic discharge (ESD) is defined as the release of static electricity when two objects come into contact. While most ESD events are considered harmless, it can be an expensive problem in many industrial environments during production and storage. The damage from ESD to LEDs may cause the product to demonstrate unusual characteristics such as:

- Increase in reverse leakage current lowered turn-on voltage
- Abnormal emissions from the LED at low current

The following recommendations are suggested to help minimize the potential for an ESD event. One or more recommended work area suggestions:

- Ionizing fan setup
- ESD table/shelf mat made of conductive materials
- ESD safe storage containers

One or more personnel suggestion options:

- Antistatic wrist-strap
- Antistatic material shoes
- Antistatic clothes

Environmental controls:

- Humidity control (ESD gets worse in a dry environment)

Precaution for Use

b. EOS (Electrical Over Stress)

Electrical Over-Stress (EOS) is defined as damage that may occur when an electronic device is subjected to a current or voltage that is beyond the maximum specification limits of the device.

The effects from an EOS event can be noticed through product performance like:

- Changes to the performance of the LED package
(If the damage is around the bond pad area and since the package is completely encapsulated the package may turn on but flicker show severe performance degradation.)
- Changes to the light output of the luminaire from component failure
- Components on the board not operating at determined drive power

Failure of performance from entire fixture due to changes in circuit voltage and current across total circuit causing trickle down failures. It is impossible to predict the failure mode of every LED exposed to electrical overstress as the failure modes have been investigated to vary, but there are some common signs that will indicate an EOS event has occurred:

- Damaged may be noticed to the bond wires (appearing similar to a blown fuse)
- Damage to the bond pads located on the emission surface of the LED package
(shadowing can be noticed around the bond pads while viewing through a microscope)
- Anomalies noticed in the encapsulation and phosphor around the bond wires
- This damage usually appears due to the thermal stress produced during the EOS event

c. To help minimize the damage from an EOS event Seoul Semiconductor recommends utilizing:

- A surge protection circuit
- An appropriately rated over voltage protection device
- A current limiting device



Company Information

Published by

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Company Information

Seoul Semiconductor (www.SeoulSemicon.com) manufactures and packages a wide selection of light emitting diodes (LEDs) for the automotive, general illumination/lighting, Home appliance, signage and back lighting markets. The company is the world's fifth largest LED supplier, holding more than 10,000 patents globally, while offering a wide range of LED technology and production capacity in areas such as "nPola", "Acrich", the world's first commercially produced AC LED, and "Acrich MJT - Multi-Junction Technology" a proprietary family of high-voltage LEDs.

The company's broad product portfolio includes a wide array of package and device choices such as Acrich and Acirch2, high-brightness LEDs, mid-power LEDs, side-view LEDs, and through-hole type LEDs as well as custom modules, displays, and sensors.

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