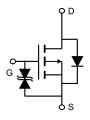




P-Channel Trench MOSFET

Description

The RMA7P20ED1 uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge and operation with gate voltages as low as 2.5V. This device is suitable for use as a load switch



Schematic diagram

General Features

• $V_{DS} = -20V, I_{D} = -0.7A$

 $R_{DS(ON)} < 0.60\Omega$ @ V_{GS} =-2.5V

 $R_{DS(ON)} < 0.42\Omega$ @ V_{GS} =-4.5V

- Lead free product is acquired
- Surface mount package
- Halogen-free

Top View Bottom View G S S

DFN1006-3

Application

Load switch

Package Marking And Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
1605	RMA7P20ED1	DFN1006-3	Ø180mm	8 mm	

Absolute Maximum Ratings (T_A=25 ℃ unless otherwise noted)

Parameter	Symbol	Limit	Unit		
Drain-Source Voltage	V _{DS}	-20	V		
Gate-Source Voltage	V _G s	±8	V		
Continuous Drain Current (T. =150°C)	T _c =25℃	1	-0.7	- A	
Continuous Drain Current (T _J =150℃)	T _c =70 ℃	I _D	-0.55	A	
Drain Current-Pulsed (Note 1)	I _{DM}	-2	А		
Maximum Power Dissipation	P _D	0.9	W		
Operating Junction and Storage Temperature Ra	nge	T_{J}, T_{STG}	-50 To 150	°C	

Thermal Characteristic

Thermal Resistance, Junction-to-Ambient (Note 2)	R _{0JA}	100	°C/W

Electrical Characteristics (T_A=25 ℃ unless otherwise noted)

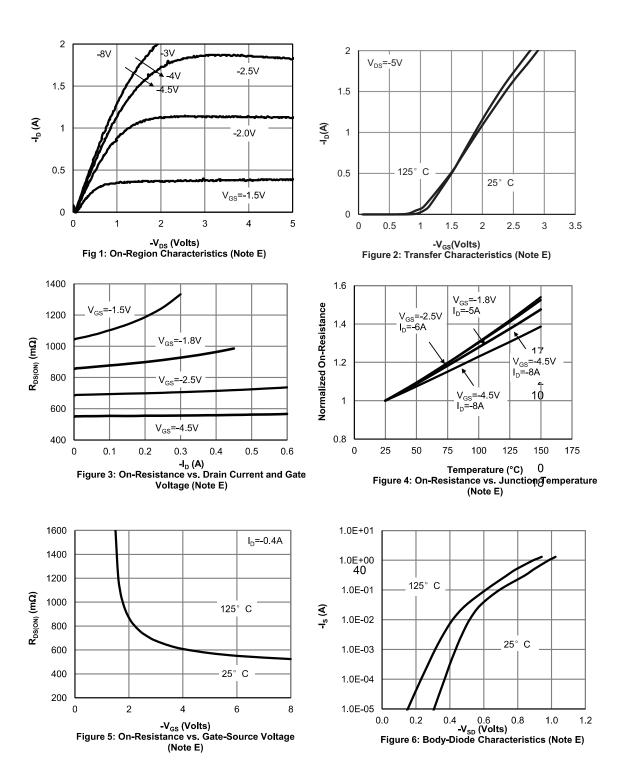
Parameter	Symbol	Condition	Min	Тур	Max	Unit		
Off Characteristics								
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =-250μA		-	-	V		
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-20V,V _{GS} =0V	-	-	-1	μA		
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±8V,V _{DS} =0V	-	-	±100	nA		
On Characteristics (Note 3)			•					
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} ,I _D =-250μA	-0.45	-0.6	-1.0	V		
Paris Os and Os Olda Paristana	5	V _{GS} =-4.5V, I _D =-0.5A		360	420			
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =-2.5V, I _D =-0.3A		400	500	mΩ		
Forward Transconductance	g FS	V _{DS} =-5V,I _D =-0.3A						
Dynamic Characteristics (Note4)	1							
Input Capacitance	C _{Iss}	V _{DS} =-4V,V _{GS} =0V, F=1.0MHz		52	-	PF		
Output Capacitance	Coss			12	-	PF		
Reverse Transfer Capacitance	C _{rss}	- F-1.0IVID2	-	8.2	-	PF		
Switching Characteristics (Note 4)			•					
Turn-on Delay Time	t _{d(on)}		-	6	-	nS		
Turn-on Rise Time	t _r	V_{DD} =-4V, I_{D} =-0.3A ,	-	5	-	nS		
Turn-Off Delay Time	t _{d(off)}	R_L =-1.2 Ω , V_{GEN} =-4.5 V , R_g =1 Ω	-	23	-	nS		
Turn-Off Fall Time	t _f		-	8	-	nS		
Total Gate Charge	Qg		-	8.0	-	nC		
Gate-Source Charge	Q _{gs}	V _{DS} =-4V,I _D =-0.5A,V _{GS} =-4.5V		0.16	-	nC		
Gate-Drain Charge	Q_{gd}		-	0.2	-	nC		
Drain-Source Diode Characteristics				'				
Diode Forward Voltage (Note 3)	V _{SD}	V _{GS} =0V,I _S =-0.5A	-	-	-1.2	V		
Diode Forward Current (Note 2)	Is		_	-	0.7	Α		

Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- 2. Surface Mounted on FR4 Board, t ≤ 10 sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production

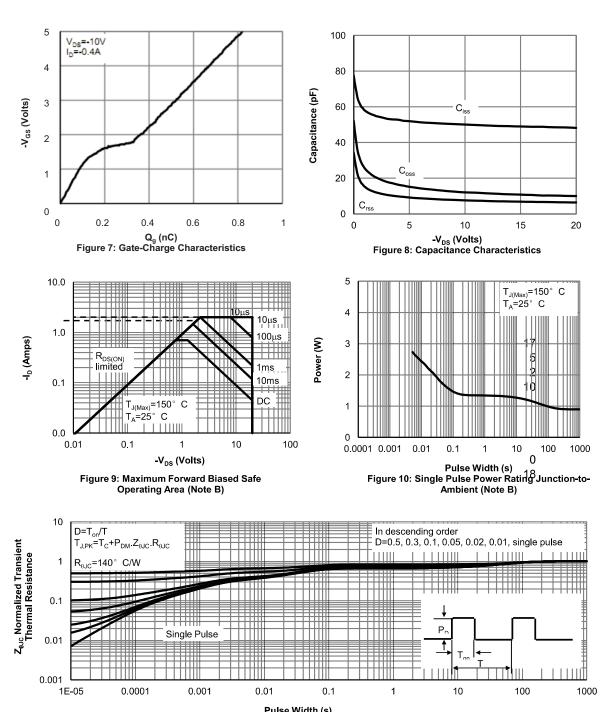


RATING AND CHARACTERISTICS CURVES (RMA7P20ED1)





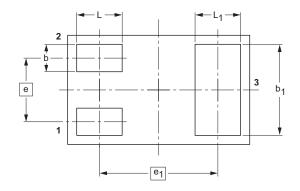
RATING AND CHARACTERISTICS CURVES (RMA7P20ED1)

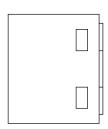


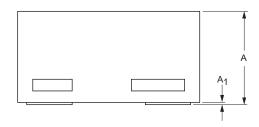
Pulse Width (s)
Figure 11: Normalized Maximum Transient Thermal Impedance (Note B)

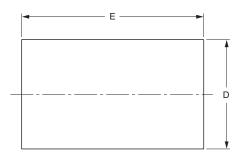


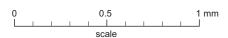
DFN1006-3 Package Information











DIMENSIONS (mm are the original dimensions)

	,			•		,				
UNIT	A ⁽¹⁾	A ₁ max.	b	b ₁	D	E	е	e ₁	L	L ₁
mm	0.50 0.46	0.03	0.20 0.12	0.55 0.47	0.62 0.55	1.02 0.95	0.35	0.65	0.30 0.22	0.30 0.22



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