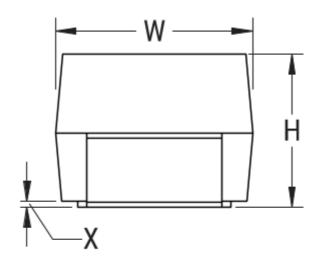
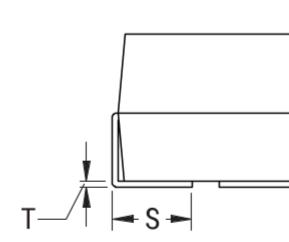
T520B686M004ATE040

T520, Tantalum, Polymer Tantalum, 68 uF, 20%, 4 VDC, SMD, Polymer, Molded, Low ESR, Non-Combustible, 40 mOhms, 3528, Height Max = 2mm

CATHODE (-) END VIEW

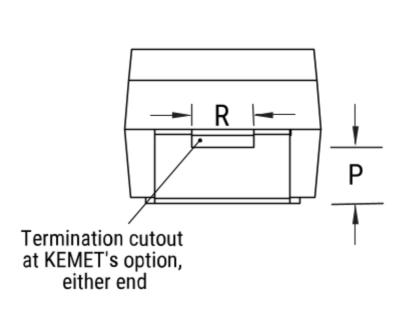
SID

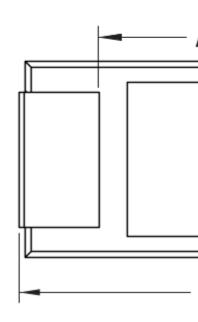




ANODE (+) END VIEW

BOTTO





Click here for the 3D model.

Dimensions

Footprint 3528

L	3.5mm +/-0.2mm
W	2.8mm +/-0.2mm
Н	1.9mm +/-0.1mm

Dimensions

T 0.13mm REF

S 0.8mm +/-0.3mm

F 2.2mm +/-0.1mm

1.9mm MIN A

В 0.4mm +/-0.15mm

P 0.5mm REF R 1mm REF

X 0.1mm +/-0.1mm

Packaging Specifications

Packaging T&R, 178mm

Packaging Quantity 2000

General Information

Series T520

Dielectric Polymer Tantalum

Style SMD Chip

SMD, Polymer, Molded, Low ESR, Non-Combustible Description

Low ESR **Features**

Yes **RoHS** Termination Tin AEC-Q200 No

Component Weight 94.85 mg Shelf Life 52 Weeks

MSL 3

Specifications

Capacitance 68 uF

Capacitance

20% Tolerance

Voltage DC 4 VDC (105C) Temperature Range -55/+105°C

Rated Temperature 105°C

Life 2000 Hrs (105C)

60C, 90% RH, 500 Hours, No Load Humidity

Dissipation Factor 8% 120Hz 25C

Failure Rate N/A

Resistance 40 mOhms (100kHz 25C)

1800 mA (rms, 100kHz 45C), 1260 mA (rms, 85C), 450 mA (rms, Ripple Current

105C)

Leakage Current 27.2 uA (5min 25°C)

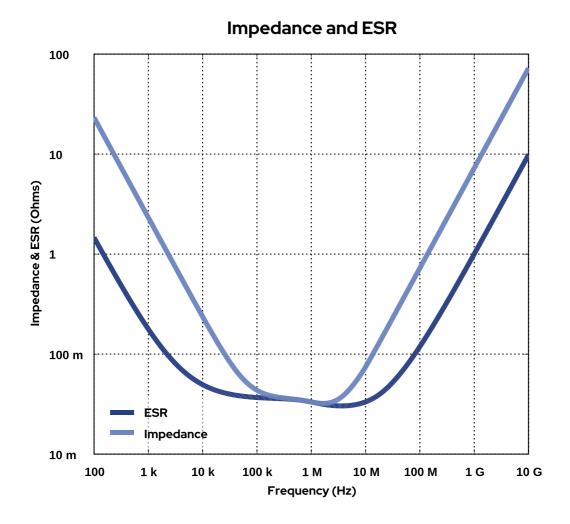
Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or

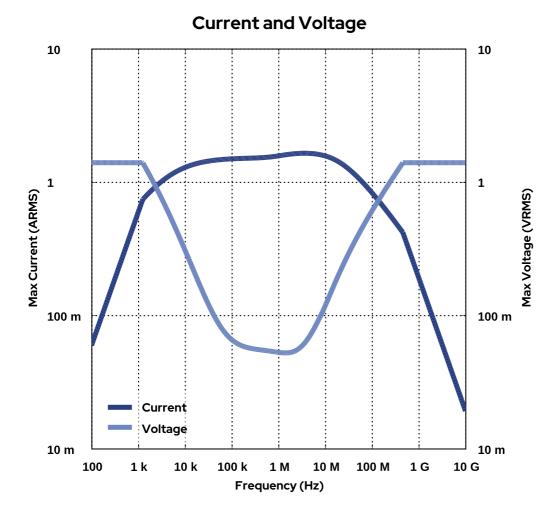
results obtained.

Generated 5/18/2023 - cdee8226-821f-42d1-a5c3-959d52f632b1 © 2006 - 2023 KEMET Generated 5/18/2023 - cdee8226-821f-42d1-a5c3-959d52f632b1 © 2006 - 2023 KEMET

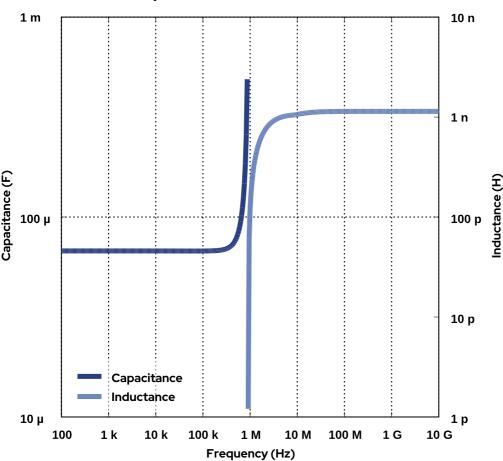
Simulations

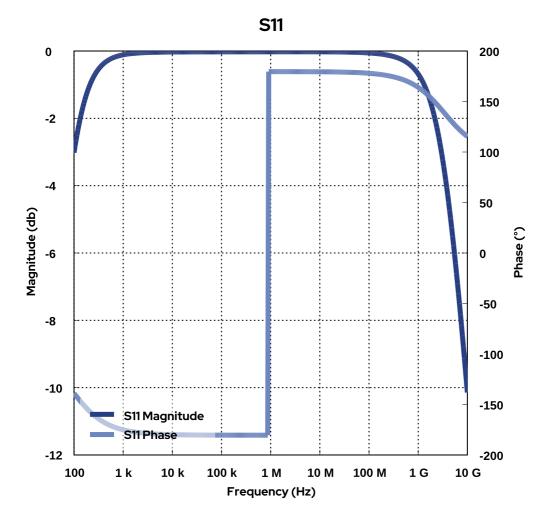
For the complete simulation environment please visit **K-SIM**.

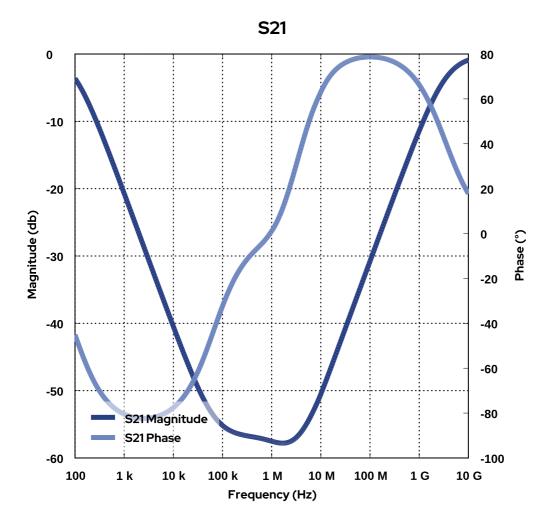




Capacitance and Inductance







These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.
- The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are

- calculated for each frequency and are not combined with voltages generated at any other harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.