

Click here for the 3D model.

| Dimensions |  |
| :--- | :--- |
| Chip Size | 2225 |
| L | $5.6 \mathrm{~mm}+/-0.4 \mathrm{~mm}$ |
| W | $6.4 \mathrm{~mm}+/-0.4 \mathrm{~mm}$ |
| T | $1.4 \mathrm{~mm}+/-0.15 \mathrm{~mm}$ |
| B | $0.6 \mathrm{~mm}+/-0.35 \mathrm{~mm}$ |


| Packaging Specifications |  |
| :--- | :--- |
| Packaging | T\&R, 180mm, Plastic Tape |
| Packaging Quantity | 1000 |

C2225C560JKGALTU
Aliases (C2225C560JKGAL7800)
SMD Comm COG HV, Ceramic, 56 pF, 5\%, 10000 VDC, COG, SMD, MLCC, Ultra-Stable, Low Loss, Class I, 2225

| General Information |  |
| :--- | :--- |
| Series | SMD Comm COG HV |
| Style | SMD Chip |
| Description | SMD, MLCC, Ultra-Stable, Low Loss, Class I |
| Features | Ultra-Stable, Low Loss, Class I |
| RoHS | No |
| Prop 65 | http://www.p65warnings.ca.gov. |
| Termination | Lead (SnPb) |
| Marking | No |
| AEC-Q200 | No |
| Component | 300 mg |
| Weight | 78 Weeks |
| Shelf Life | 1 |
| MSL |  |


| Specifications |  |
| :--- | :--- |
| Capacitance | 56 pF |
| Measurement Condition | 1 MHz 1.0 Vrms |
| Capacitance Tolerance | $5 \%$ |
| Voltage DC | 10000 VDC |
| Dielectric Withstanding Voltage | 12000 VDC |
| Temperature Range | $-55 /+125^{\circ} \mathrm{C}$ |
| Temperature Coefficient | COG |
| Capacitance Change with Reference to | $30 \mathrm{ppm} / \mathrm{C}, 1 \mathrm{MegaHz}$ |
| $+25^{\circ} \mathrm{C}$ and O VDC Applied (TCC) | 1.0 Vrms |
| Dissipation Factor | $0.1 \% 1 \mathrm{MHz} 1.0 \mathrm{Vrms}$ |
| Aging Rate | $\mathrm{O} \% \mathrm{Loss} / \mathrm{Decade}$ |
| Insulation Resistance | Hour |

