

PCN Number:	20160607001	PCN Date:	06/17/2016
Title:	Datasheet for DS92LV2421/DS92LV2422		
Customer Contact:	PCN Manager	Dept:	Quality Services
Change Type:			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design
<input type="checkbox"/>	Assembly Process	<input checked="" type="checkbox"/>	Data Sheet
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change
<input type="checkbox"/>	Mechanical Specification	<input type="checkbox"/>	Test Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Materials
<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

Notification Details

Description of Change:

Texas Instruments Incorporated is announcing an information only notification. The product datasheet(s) is updated as seen in the change revision history below:



DS92LV2421, DS92LV2422

SNLS321C – MAY 2010 – REVISED MAY 2016

www.ti.com

Changes from Revision B (April 2013) to Revision C

Page

• Added ESD Ratings table, Feature Description section, Device Functional Modes, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section	1
• Updated thermal characteristic values based on latest simulation data	11
• Changed deserializer LVCMOS DC and supply current specification test conditions based on latest production tests	12
• Changed I _{OL} test condition for V _{OL} at V _{DDIO} = 3.3 V to 3 mA	12
• Changed max value of Deserializer V _{OL} to 0.45 V	12
• Changed test condition parameter for V _{OL} Serial Control Characteristic	13
• Changed RPU = 10 kΩ condition for the Serial Control Bus Characteristics of t _R and t _F	13
• Added notes for serializer and deserializer switching characteristics verified by characterization	14
• Added corresponding pins for deserializer t _{CLH} and t _{CHL} parameter	15
• Added test condition to t _{DD} deserializer parameter	15
• Changed corrected units for deserializer lock time and delay parameter	15
• Added serial stream and video control signal filter waveform to Feature Description	23
• Changed "NA" and "Disable" term in Table 4 and Table 5 to "Off"	28
• Changed output states to correct values based on OSS_SEL and PDB configuration in Table 6	29
• Added details for Deserializer Map Select strap pin configuration	33
• Added clarification on the state of deserializer outputs during BIST mode operation.....	33
• Added statement to set input to low when entering BIST mode with DS90C241 or DS90UR241	33
• Added note that ID[X] cannot be tied to VSS, as only four device addresses are supported	35
• Added RID tolerance and tablenote that RID ≠ 0 Ω to set ID[X]	35
• Changed statement that CONFIG settings can also be programmed via register	37

Device Family	Change From:	Change To:
DS92LV2421/DS92LV2422	SNLS321B	SNLS321C

These changes may be reviewed at the datasheet links provided.

<http://www.ti.com/product/ds92lv2421>

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

No anticipated impact. This is a specification change announcement only. There are no changes to the actual device.

Changes to product identification resulting from this PCN:			
None.			
Product Affected:			
DS92LV2421SQ/NOPB	DS92LV2421SQX/NOPB	DS92LV2422SQE/NOPB	
DS92LV2421SQE/NOPB	DS92LV2422SQ/NOPB	DS92LV2422SQX/NOPB	

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

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