DELIVERY SPECIFICAT	<u>IVERY SPECIFICATION</u>					
То	No	n-Control	led Copy			
CUSTOMER'S PRODUCT NAME	TDK'S PROD	OUCT NAME				
	Dipped Ra FA-Series High Tempe 【Halogen-f	dial Lead Type erature Application ree, RoHS compliar				
Please return this specification to TDK rep If orders are placed without returned speciaccepted by your side.	ification, please a		t specification is			
	DATE:	YEAR MC	NTH DAY			
TDK Corporation Sales Electronic Components Sales & Marketing Group	Engineering Electronic Compor Ceramic Capacitor	nents Business Corr rs Business Group	ıpany			
APPROVED Person in charge	APPROVED	CHECKED	Person in charge			

#### SCOPE

This delivery specification shall be applied to Multilayer ceramic chip capacitors to be delivered to

#### **PRODUCTION PLACES**

Production places defined in this specification shall be TDK Xiamen Co., (China).

#### PRODUCT NAME

The name of the product to be defined in this specifications shall be <u>FAOO $\triangle \triangle \Box \Box \Box \times \times \times \odot * * * *$ </u>.

#### **REFERENCE STANDARD**

JIS	C 5101-1	Fixed capacitors for use in electronic equipment-Part 1 :
		Generic specification
	C 0806-2	Packaging of components for automatic handing-Part 2 :
		Packaging of components with unidirectional leads on continuous tapes
JEITA	RCR-2335 C	Safety application guide for fixed ceramic capacitors for use in electronic equipment

#### CONTENTS

- 1. CODE CONSTRUCTION
- 2. COMBINATION OF RATED CAPACITANCE AND TOLERANCE
- 3. OPERATING TEMPERATURE RANGE
- 4. STORING CONDITION AND TERM
- 5. INDUSTRIAL WASTE DISPOSAL
- 6. PERFORMANCE
- 7. INDICATION
- 8. INSIDE STRUCTURE AND MATERIAL
- 9. PACKAGING
- 10. CAUTION
- 11. TAPE PACKAGING SPECIFICATION

#### <EXPLANATORY NOTE>

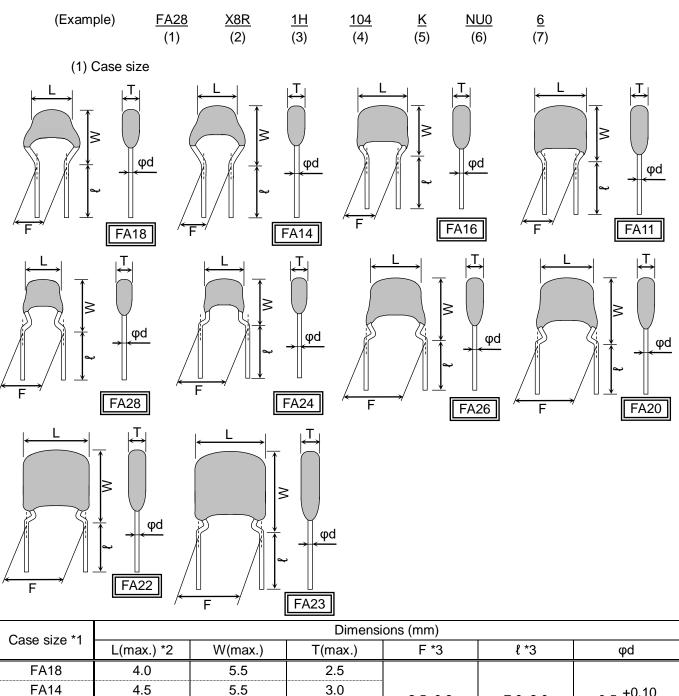
When the mistrust in the spec arises, this specification is given priority. And it will be confirmed by written spec change after conference of both posts involved.

This specification warrants the quality of the ceramic chip capacitor. Capacitors should be evaluated or confirmed a state of mounted on your product.

If the use of the capacitors goes beyond the bounds of this specification, we can not afford to guarantee.

Division	Date	SPEC. No.
Ceramic Capacitors Business Group	Apr., 2020	C2020-FA150

### **1. CODE CONSTRUCTION**



FA14	4.5	5.5	3.0	2.5±0.8	7.0±2.0	0.5 +0.10
FA16	5.5	6.0	3.5	2.5±0.0	7.0±2.0	0.5 -0.03
FA11	5.5	7.0	4.0			
FA28	4.0	5.5	2.5			
FA24	4.5	5.5	3.0			0.5 <sup>+0.10</sup> -0.03
FA26	5.5	6.0	3.5	5.0±1.0	7.0±2.0	
FA20	5.5	7.0	4.0	5.0±1.0	7.0±2.0	0.5 -0.03
FA22	7.5	8.5	4.5			
FA23	8.5	11.0	5.5			

\*1 FA denotes forming lead.

The first digit refers to a distance between leads (1:2.5mm, 2:5.0mm), the second digit is for TDK internal code. \*2 The FA18, FA14, FA28 and FA24 types represent dimensions 1 mm below the top of the body.

Other types represent the dimensions of the central part of the body.

\*3 Dimension F and *l* is applied to bulk packaging.

The measurement point of F dimensions is 1.5 to 2.0mm below the kink. Refer to Appendix 2 and 3 for dimension of taping packaging.

- (2) Temperature Characteristics (Details are shown in para 6 No.7,8)
- (3) Rated Voltage

Symbol	Rated Voltage
2 J	DC 630 V
2 W	DC 450 V
2 E	DC 250 V
2 A	DC 100 V
1 H	DC 50 V
1 E	DC 25 V

(4) Rated Capacitance Stated in three digits and in units of pico farads (pF). The first and second digits identify the first and second significant figures of the capacitance, the third digit identifies the multiplier.

(Example)	Symbol	Rated Capacitance
	104	100,000pF

(5) Capacitance tolerance	Symbol	Tolerance Cap		Capacitance(C)
	D	±0.5 p	σF	C=10pF
	J	±5 %	%	Over 10pE
	К	±10 °	%	Over 10pF
		-		

(6) Internal code

(7) Packaging

Symbol	Applied voltage of Life
NU0	Rated voltage ×2 (*1)
RU0	Rated voltage ×1
*1 0E D	tod valtage 1 F

1 2E : Rated voltage×1.5 2W : Rated voltage×1.2 2J : Rated voltage×1.2

Symbol	Packaging
0	Bulk
6	Ammo Pack

# 2. COMBINATION OF RATED CAPACITANCE AND TOLERANCE

Class	Temperature Characteristics	Capacitance	tolerance	Rated capacitance(C)
		C=10	D (±0.5 pF)	10
4	NDO	10 <c≦100< td=""><td>J (± 5 %)</td><td>E- 6 series</td></c≦100<>	J (± 5 %)	E- 6 series
1	NP0	100 <c≦10,000< td=""><td>J (± 5 %)</td><td>E-12 series</td></c≦10,000<>	J (± 5 %)	E-12 series
		10,000 <c< td=""><td>J (± 5 %)</td><td>E- 6 series</td></c<>	J (± 5 %)	E- 6 series
2	X8R	C≦10	K (±10 %)	E- 6 series

#### Capacitance Step in E series

E series		Capacitance Step										
E- 6	1.0 1.5 2.2 3.3 4.7				.0 1.5 2.2 3.3				6	.8		
E-12	1.0	1.2	1.5	1.8	2.2	2.7	3.3	3.9	4.7	5.6	6.8	8.2

## **3. OPERATING TEMPERATURE RANGE**

T.C.	Min. operating	Max. operating	Reference
	Temperature	Temperature	Temperature
NP0 X8R	-55°C	150°C	25°C

## 4. STORING CONDITION AND TERM

Storing temperature	Storing humidity	Storing term
5~40°C	20~70%RH	Within 6 months upon receipt.

## 5. INDUSTRIAL WASTE DISPOSAL

Dispose this product as industrial waste in accordance with the industrial Waste Law.

# 7. PERFORMANCE

No.	It	em	table 1 Performance		Test	or inspect	ion m	ethod
1	External Appearance		No defects which may affect performance.	By visual checking.				
2	Indication	Appearance	Meet a requirement per para 8.	solvent		t Solvent temp.		Dipping time
		Resistance to solvent	Shall be visible.	Isopro alcoh	opyl ol	20~25°	С	30±5s.
3	Voltage Proof	Between termination	No insulation breakdown or other damage.	Class	Rated	l voltage	App	oly voltage
				1	100V :	and under	3×1	rated voltage
				·	Ove	er 100V	1.5 ×	rated voltage
				2	RV	≦100V	2.5 ×	rated voltage
					Ove	er 100V	1.5 ×	rated voltage
				Above DC voltage shall be applied for 1~5s.				
				Charge / discharge current shall not exce 50mA.				
		Between termination coating	No insulation breakdown or other damage.			ed voltage. Iall ball me		)
4	coating Insulation Resistance		10,000MΩ or 500 MΩ • µF min. whichever smaller.	<ul> <li>≪450V DC and under≫ Apply rated voltage.</li> <li>≪630V DC≫ Apply DC500V.</li> <li>Applying time : 60sec.</li> </ul>				
5	Capacitanc	e	Within the specified tolerance.	Class 1				
				Ratec	i tance	Measu freque	•	Measuring voltage
				1,000 and u	pF	1MHz±		0.5~5
				Over 1,000	pF	1kHz±1	0%	Vrms.
				Class 2				
				Ratec	t itance	Measu freque		Measuring voltage
				10µF and u		1kHz±1		1.0±0.2 Vrms.
				For info	rmation ng volta			•

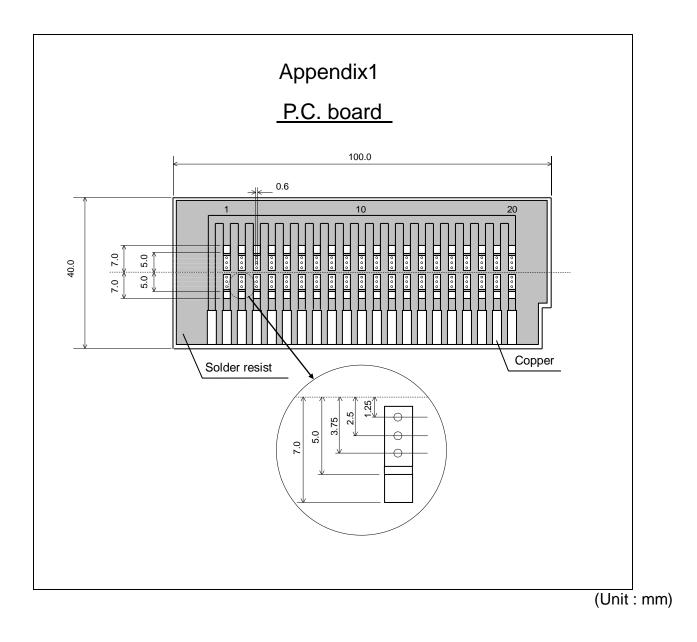
(conti	nued)								
No.		em	Per	formand	e			t or inspection method	
6	Q (Class 1)		Capacitance	9	Q		See No.5 in this table for measuring condition.		
			30pF and ove	er 1	,000 min.				
			Under 30pF		)+20×C min.	For	informat	ion which product has which	
			C : Rated			Diss	ipation I	Factor, please contact with our entative.	
	Dissipatio	n Factor				- 5010	3 Tepies		
	(Class 2)		T.C.		D.F.				
			X8R	0.0	3 max.				
					_				
7	Temperate Character of Capacit	istics	Tempe	erature C (ppm/°		base	Temperature Coefficient shall be calculated based on values at 25°C and 85°C temperature.		
	(Class 1)	lance	ND	<u>(ppin/ (</u> 20 : 0	,	tern	berature		
					0 ± 30	Measuring temperature be -10°C and -25°C		emperature below 20°C shall	
			Capacitance drift Within ±0.2% or ±0.05pF, whichever larger.		be -		u -25 C		
8	Temperature							shall be measured by the	
	Character of Capacit		Capacitance Change(%)			steps shown in the following table, after thermal equilibrium is obtained for each			
	(Class 2)	lance	No voltage applied			step		indition is obtained for each	
	· · /					be calcu	lated ref. STEP3 reading.		
					-	Step	Temperature(°C)		
				X8R: ±	±15		1	Reference temp. ±2	
							2	Min. operating temp. ±2	
						_	3	Reference temp. ±2	
							4	Max. operating temp. ±2	
9	Lead Strength				such as lead	to le Pulli	ad draw	the parts, apply pulling force ring direction gradually. rigth : 10N e : 10±1s.	
	Bending Strength		No mechanical d breakage and loc		such as lead	axis weig posi This and Ben	vertical, hting ar tion. operation repeat t ding fort	the capacitors to keep the bend it 90 degrees with d put it back to the original on shall be done for 2~3s. he following times. th : 5N e : 2 times	

(cont	inued)					
No.	,	Item Performance			Test or inspection method	
10	Mechanical Shock	External appearance	No mechanical damage.			With following conditions.
		Capacitance	Chara	cteristics	Change from the	Waveform : Half-sine Applied force : 100G max. Velocity change : 12.3ft/s.
			Class	NP0	value before test ±2.5% or ±0.25pF,	Duration : 6 msec. Shocks : 18shocks in each 3 mutually
			Class	X8R	whichever larger. ±7.5 %	perpendicular axes. Solder the capacitors on a P.C.Board
			2	NOK	±7.5 %	shown in Appendix1 before testing. The FA23 type fixes the capacitors with
		Q Class1	Meet the	e initial sp	Dec.	coating as follows.
		D.F. Class2	Meet the initial spec.		ec.	
11	Vibration	External appearance	No mechanical damage.		amage.	Vibrate the capacitor with following conditions.
		Capacitance	Chara	cteristics	Change from the value before test	Applied force : 5G max. Frequency : 10-2,000-10Hz
			Class 1	NP0	±2.5% or ±0.25pF, whichever larger.	Duration : 20 min. Cycle : 12cycles in each 3 mutually perpendicular directions.
			Class 2	X8R	±7.5 %	Solder the capacitors on a P.C.Board shown in Appendix1 before testing. The
		Q Class1	Meet the	e initial sp	Dec.	FA23 type fixes the capacitors with coating as follows.
		D.F. Class2	Meet the	e initial sp	Dec.	
12	Solderability		Leads shall be covered by new solder more than 75% of its surface.			Completely soak both terminations in solder at 245±5°C for 2±0.5s.
						Solder : Sn-3.0Ag-0.5Cu(Pb-free) Flux : Isopropyl alcohol(JIS K 8839) Rosin(JIS K 5902) 25% solid solution. Dipping : By 1.5~2.0mm from the root of lead.

No.	ntinued)	em		Perf	ormance		Test or inspection method		
13	Resistance	External	No defec		n may affect	Completely soak both terminations in			
	to solder	appearance	performa	ince.	•	solder at 260±5°C for 10±1s.			
	heat	Capacitance	Charac Class 1	teristics NP0	Change from the value before test ±2.5 % or ±0.25pF whichever larger.		Pb-free) S K 8839) om the root		
			Class 2	X8R	±7.5 %		Leave the capacitors in ambient conditio for the following time before measurement.		
		Q Class1	Meet the	initial sp	pec.	measur			
		D.F. Class2	Meet the			Class1 : 6~24h Class2 : 24±2h			
		Insulation Resistance	Meet the initial spec. No insulation breakdown or other damage.			_			
		Voltage proof							
14	Heat shock	External appearance	No mechanical damage.			Solder the capacitors on a P.C.Board shown in Appendix1 before testing.			
		Capacitance	Chara	cteristics	Change from the value before test		the capacitors in the capacitors in the capacitors in the second se	he condition	
			0		±2.5 % or	Step	Temp.(°C)	Time(min.)	
			Class 1	NP0	±0.25pF whichever larger.	1	Min. operating Temp.±3	30 ± 3	
			Class 2	X8R	±7.5 %	2	Max. operating Temp.±2	30 ± 3	
						Test cyc Transit	cle : 1,000cycles time : Less than 1n	nin.	
		Q Class1	Meet the				he capacitors in an		
		D.F Class2	Meet the			for the f	ollowing time befor ement.	re	
		Insulation Resistance	Meet the				1 : 6~24h 2 : 24±2h		
		Voltage proof	No insula damage.		akdown or other	– Class2 : 24±2h			

lo.		tem		Perf	ormance	Test or inspection method	
5	Moisture Resistance	External appearance Capacitance	No mech	anical d	amage.	Solder the capacitors on a P.C.Board shown in Appendix1 before testing.	
		Capacitarioe	Chara	cteristics	Change from the value before test	Apply the rated voltage at temperature 85±2°C and 85%RH for 1,000 +48,0h.	
			Class 1	NP0	±7.5% or ±0.75pF whichever larger.	Charge/discharge current shall not exceed 50mA.	
			Class 2	X8R	±12.5 %	Leave the capacitors in ambient condition for the following time before measurement.	
		Q				Class1 : 6~24h Class2 : 24±2h	
		Class1	Capa	acitance	Q	Veltage conditioning (Only Class?)	
				and ove		Voltage conditioning : (Only Class2) Voltage treat the capacitor under testing	
					100+10/3×C	temperature and voltage for 1hour.	
			Und	er 30pF	min.	Leave the capacitors in ambient	
			C : Rated capacitance (pF)			condition for 24±2h before measurement.	
		D.F. Class2	200% of initial spec max.			Use this measurement for initial value.	
		Insulation Resistance	500MΩ or 25MΩ $\cdot$ µF min. whichever smaller.				
6	Life	External appearance	No mechanical damage.			Solder the capacitors on a P.C.Board shown in Appendix1 before testing.	
		Capacitance	Characteristics Change from the value before test			Below the voltage shall be applied at maximum operating temperature $\pm 2^{\circ}$ C for 1,000 +48,0h.	
			Class		±3% or	Applied voltage	
			1	NP0	±0.3pF whichever larger.	Rated voltage x2	
						Rated voltage x1.5	
			Class 2	X8R	±15 %	Rated voltage x1.2	
						Rated voltage x1	
		Q				For information which products has	
		Class1	Cap	acitance	e Q	which applied voltage, please contact with our sales representative.	
				and ove			
				and ove		Charge/discharge current shall not exceed 50mA.	
				ler 30pF	min.		
			Under 10pF 200+10×C min.		min.	Leave the capacitors in ambient condition for the following time before measurement.	
					capacitance (pF)	Class1 : 6~24h Class2 : 24±2h	
		D.F. Class2	200% of initial spec max.			Voltage conditioning : (Only Class2) Voltage treat the capacitor under testing temperature and voltage for 1hour. Leave the capacitors in ambient condition for 24±2h before measurement. Use this measurement for initial value.	

\* As for the initial measurement of capacitors (Class2) on number 8, 10, 11, 13, and 14, leave capacitors at 150 -10,0°C for 1h and measure the value after leaving capacitors for 24±2h in ambient condition.



- 1. Material : Glass Epoxy(As per JIS C6484 GE4)
- 2. Thickness : 1.6mm

Cop

Copper(Thickness:0.035mm) Solder resist

# 7. INDICATION

# 7.1 Indication (Example)

T.C.	FA18 FA14 FA28 FA24	FA16 FA11 FA26 FA20	FA22 FA23
NP0	(1) -> 333	$(1) \xrightarrow{104J} (2)$ $(3) \xrightarrow{104J} (2)$	$(1) \xrightarrow{154} (2)$ $(3) \xrightarrow{15K} (4)$
X8R	(1) - + 474	$(1) \xrightarrow{335K} (2)$	

## 7.2 Meaning of indication

No.	Item	Detail			
(1)	Rated Capacitance	Indicate in three digits.			
(2)	Capacitance tolerance	Indicates the symbol.			
(3)	Rated voltage	For DC50V, indicate a bar under the rated capacitance.			
(4)	Manufacturer	Indicates " TDK ".			

# 8. INSIDE STRUCTURE AND MATERIAI

. 11131	DE SIRUCI	URE					
No.	NAME	No.	NAME	MATERIAL			
				Class 1	Class 2		
		1-1	Dielectric	CaZrO₃	BaTiO₃		
	Multilayer	1-2	Electrode	Ν	Ji		
1	Ceramic Chip	1-3		C	Cu		
	Capacitors	1-4	Termination	Ν	Ji		
		1-5		Sn			
2	2 Coating			Ероху			
				[Halogen-free]			
3	Solder for joint			Lead free solder			
4	l	_ead wi	re	Tin plated copper	covers steel wire		

## 9. PACKAGING

Packaging shall be done to protect the components from the damage during Transportation and storing, and a label which has the following information shall be attached.

- 1) Inspection No. \*
- 2) TDK P/N
- 3) Quantity

\* Composition of Inspection No.

Example 
$$\underline{X}$$
  $\underline{0}$   $\underline{A}$  -  $\underline{OO}$  -  $\underline{OOO}$   
(a) (b) (c) (d) (e)

- a) Inspection factory codeb) Last digit of year
- c) Month and A for January and B for February and so on. (Skip I)
- d) Inspection Date of the month.
- e) Serial No. of the day

#### 1) Total number of components in a plastic bag.

Туре	Qty.(pcs.)
FA18, FA28	
FA14, FA24	
FA16, FA26	500
FA11, FA20	
FA22	
FA23	200
	1 1 10 10

2) Tape packaging is as per TDK tape packaging specification.

# **10. CAUTION**

	AUTION						
No.	Process	Condition					
1	Operating Condition (Storage,Use,	<ul> <li>1-1. Storage, Use</li> <li>1) The capacitor must be stored in an ambient temperature of 5~40°C with a relative humidity of 20~70%. The products should be used within 6 months upon receipt.</li> </ul>					
	Transportation)	<ol> <li>The capacitors must be operated and stored in an environment free of dew condensation and these gases such as Hydrogen Sulphide, Hydrogen Sulphate Chlorine, Ammonia and sulfur.</li> </ol>					
		3) Avoid storing in sun light and wet with dew.					
		<ol> <li>Do not use capacitors under high humidity and high and low atmospheric pressure which may affect capacitors reliability.</li> </ol>					
		5) Capacitors should be tested for the solderability when they are stored for long time.					
		<ul> <li>1-2. Handling in transportation</li> <li>1) In case of the transportation of the capacitors, the performance of the capacitors may be deteriorated depending on the transportation condition. (Refer to JEITA RCR-2335C 9.2 Handling in transportation)</li> </ul>					
2	Circuit design	2-1. Operating temperature Operating temperature should be followed strictly within this specification, especially be careful with the maximum temperature.					
		1) Do not use capacitor above the maximum allowable operating temperature.					
		2) Surface temperature including self heating should be below maximum operating temperature.					
		(Due to dielectric loss, capacitor will heat itself when AC is applied. Especially at high frequencies around its SRF, the heat might be so extreme that it may damage itself or the product mounted on. Please design the circuit so that the maximum temperature of the capacitor including the self heating to be below the maximum allowable operating temperature. Temperature rise shall be below 20°C.)					
		<ol> <li>The electrical characteristics of the capacitors will vary depending on the temperature. The capacitors should be selected and designed in taking the temperature into consideration.</li> </ol>					
		<ul> <li>2-2. Operating voltage</li> <li>1) Operating voltage across the terminals should be below the rated voltage. When AC and DC are super imposed, V0-P must be below the rated voltage.</li> </ul>					
		AC or pulse with overshooting, $V_{P-P}$ must be below the rated voltage. (1) and (2)					
		(5) When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.					
		Voltage (1) DC voltage (2) DC + AC voltage (3) AC voltage					
		Positional Measurement (Rated voltage)					
		Voltage (4) Pulse voltage (A) (5) Pulse voltage (B)					
		Positional Measurement (Rated voltage)					
		L					

No.	Process		Conditio	on
2	Circuit design	reliability 3) The effect	of the capacitor may be reduced.	ing on applied DC and AC voltages. The
		considera 2-3. Frequency 1) When the may vibra	ation. capacitors (Class 2) are used in ate themselves and generate aud	AC and/or pulse voltages, the capacitors lible sound.
3	Designing P.C.board	the capacitor of	ds are inserted into different pitch r outer resin to result in cracking, apacitor layout is as following.	n holes, it may induce excessive stress in and it may degrade the quality.
		Ν	ot recommended	Recommended
			crack	
4	Lead wire insertion	cause lea Please ac		
			Not recommended	Recommended
		Clinching	crack	
		stress in t quality. When the	the capacitor or outer resin to res	t pitch holes, it may induce excessive sult in cracking, and it may degrade the hrough hole on the pc board, please body would not receive excessive force.

۱o.	Process	Cc	ndition			
5	Soldering	5-1. Flux selection Although highly-activated flux gives bet activity may also degrade the insulatior To avoid such degradation, it is recomn				
		<ol> <li>It is recommended to use a mildly activative use acidic flux is not recommended.</li> </ol>	ted rosin flux (less than 0.1wt% chlorine). Do no			
		2) Excessive flux must be avoided. Please provide proper amount of flux.				
		3) When water-soluble flux is used, enough	n washing is necessary.			
		5-2. Recommended soldering profile by vario	us methods			
		Flow soldering soldering Preheating	Manual soldering (Solder iron)			
		$0   \underbrace{60 \sim 120 \text{ sec.}}_{\text{Within 5 sec.}}   \underbrace{\text{Over 60 sec.}}_{\text{Within 5 sec.}}  $	0 Preheating 3 sec.(As short as possible)			
		5-3. Avoiding thermal shock 1) Preheating condition				
		Soldering	Temp.(°C)			
		Wave soldering	ΔT≦150			
		Manual soldering	<u>ΔT≦190</u>			
		<ol> <li>Cooling condition Natural cooling using air is recommend cleaning, the temperature difference(Δ<sup>-</sup></li> </ol>	ed. If the chips are dipped into a solvent for Γ) must be less than 100°C.			
		5-4. Amount of solder In sufficient solder may detach the capa See bellow for example of solder amou				
		Adequate				
		Insufficient solder	Low robustness may cause contact failure or capacitor comes off the P.C.board.			

No.	Process	Condition	
5	Soldering	5-5. Solder repair by solder iron Tip temperature of solder iron varies by its type, P.C.board size. Higher the tip temperature, quick the operation is, but crack the capacitor. Following condition is recommended.	
		(Recommended solder iron condition)	
		Temp. (°C) Wattage (W) Shape (mm)	Time (sec.)
		350 MAX. 20 MAX. φ3.0 MAX.	3 MAX.
6	Cleaning Coating and	<ol> <li>If an unsuitable cleaning fluid is used, flux residue or some to capacitor surface to deteriorate especially the insulation</li> <li>If cleaning condition is not suitable, it may damage the capa</li> <li>Insufficient washing         <ol> <li>Terminal electrodes may corrode by Halogen in the flux.</li> <li>Halogen in the flux may adhere on the surface of tinsulation resistance.</li> <li>Water soluble flux has higher tendency to have above and (2).</li> </ol> </li> <li>Excessive washing         <ol> <li>Excessive washing way damage the coating material deteriorate it.</li> <li>When ultrasonic cleaning is used, excessively high ultra affect the adhesion between the ceramic dielectric and To avoid this, following is the recommended condition.</li> <li>Power : 20W/ℓ max.</li> <li>Frequency : 40kHz max.</li> <li>Washing time : 5 minutes max.</li> </ol> </li> <li>If the cleaning fluid is contaminated, density of Halogen in bring the same result as insufficient cleaning.</li> <li>When the P.C.board is coated, please verify the quality influe</li> </ol>	resistance. acitor. capacitor, and lower the e mentioned problems (1) of coated capacitor and asonic energy output can the terminal electrodes.
7	Coating and molding of the P.C.board		·

No.	Process	Condition
8	Lead wire bending	During lead wire bending process, mechanical stress often concentrates in one part of capacitor body and it may damage the ceramic and the coating. Refer to following for bending the lead wire.
		the lead bending would not affect the capacitor body.
9	Handling of loose capacitor	If dropped the capacitor may crack. Once dropped do not use it. Especially, the large case sized capacitor is tendency to have cracks easily, so please handle with care.
10	Capacitance aging	The capacitors (Class 2) have aging in the capacitance. They may not be used in precision time constant circuit. In case of the time constant circuit, the evaluation should be done well.
11	Estimated life and estimated failure rate of capacitors	The estimated life and the estimated failure rate depend on the temperature and the voltage. This can be calculated by the equation described in JEITA RCR-2335C Annex F(Informative) Calculation of the estimated lifetime and the estimated failure rate (Temperature acceleration : 3rd powered low, Voltage acceleration : 10degC law) The failure rate can be decreased by reducing the temperature and the voltage but they will not be guaranteed.

No.	Process	Condition
12	Caution during operation of equipment	<ol> <li>A capacitor shall not be touched directly with bare hands during operation in order to avoid electric shock.</li> <li>Electric energy held by the capacitor may be discharged through the human body when touched with a bare hand.</li> </ol>
		Even when the equipment is off, a capacitor may stay charged. The capacitor should be handled after being completely discharged using a resistor.
		2) The terminals of a capacitor shall not be short-circuited by any accidental contact with a conductive object. A capacitor shall not be exposed to a conductive liquid such as an acid or alkali solution. A conductive object or liquid, such as acid and alkali, between the terminals may lead to the breakdown of a capacitor due to short circuit
		<ol> <li>Confirm that the environment to which the equipment will be exposed during transportation and operation meets the specified conditions. Do not to use the equipment in the following environments.</li> </ol>
		(1) Environment where a capacitor is spattered with water or oil
		<ul> <li>(2) Environment where a capacitor is exposed to direct sunlight</li> <li>(3) Environment where a capacitor is exposed to Ozone, ultraviolet rays or radiation</li> <li>(4) Environment where a capacitor exposed to corrosive gas(e.g. hydrogen sulfide,</li> </ul>
		sulfur dioxide, chlorine. ammonia gas etc.) (5) Environment where a capacitor exposed to vibration or mechanical shock exceeding the specified limits. (6) Atmosphere change with causes condensation
		(b) Almosphere change with causes condensation
13	Others	The product listed in this specification is intended for use in automotive applications under- normal operation and usage conditions.
		The product is not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality requires a more stringent level of safety or reliability, or whose failure, malfunction or defect could cause serious damage to society, person or property. Please understand that we are not responsible for any damage or liability caused by use of the products in any of the applications below or for any other use exceeding the range or conditions set forth in this specification sheet. If you intend to use the products in the applications listed below or if you have special requirements exceeding the range or conditions set forth in this specification, please contact us.
		<ul> <li>(1) Aerospace/Aviation equipment</li> <li>(2) Transportation equipment (electric trains, ships etc.)</li> <li>(3) Medical equipment (Excepting Pharmaceutical Affairs Law classification Class1,2)</li> <li>(4) Power-generation control equipment</li> <li>(5) Atomic energy-related equipment</li> <li>(6) Seabed equipment</li> </ul>
		(7) Transportation control equipment
		(8) Public information-processing equipment
		<ul><li>(9) Military equipment</li><li>(10) Electric heating apparatus, burning equipment</li><li>(11) Disaster prevention/crime prevention equipment</li></ul>
		<ul><li>(12) Safety equipment</li><li>(13) Other applications that are not considered general-purpose applications</li></ul>
		When designing your equipment even for general-purpose applications, you are kindly requested to take into consideration securing protection circuit/device or providing backup circuits in your equipment. In addition, although the product listed in this specification is intended for use in automotive applications as described above, it is not prohibited to use in general electronic equipment, whose performance and/or quality doesn't require a more stringent level of safety or reliability, or whose failure, malfunction or defect could not cause serious
		damage to society, person or property. Therefore, the description of this caution will be applied, when the products are used in general electronic equipment under a normal operation and usage conditions.

## **11.TAPE PACKAGING SPECIFICATION**

#### **1. DIMENSION OF TAPING**

Dimensions of FA1\* type shall be according to Appendix 2. Dimensions of FA2\* type shall be according to Appendix 3.

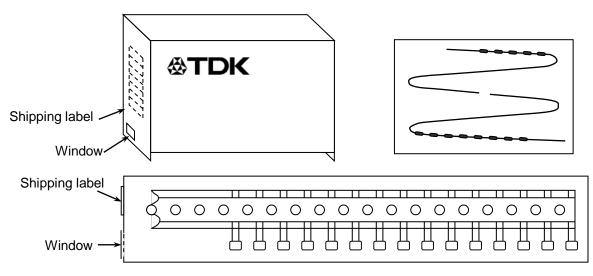
### 2. QUANTITY

Туре	Parts quantity/box (pcs.)
FA18, FA28	
FA14, FA24	2,000
FA16, FA26	
FA11, FA20	1,500
FA22, FA23	1,000

#### 3. PERFORMANCE SPECIFICATIONS

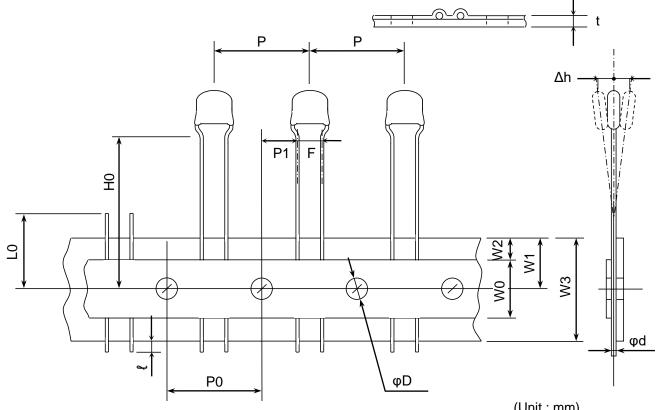
- 3-1. The missing of components shall be within consecutive 3pcs.
- 3-2. Empty part for min 3pcs shall be provided at the beginning and the end of taping.
- 3-3. Shipping label must be attached at the side of carton.
- 3-4. When pull the carrier tape for left side with keeping the head of capacitors to the direction of the above figure, adhesive tape shall be upper side.
- 3-5. Folded tape shall contain 25pcs. of components.

#### 4. PACKAGING SPECIFICATION (Ammo pack)



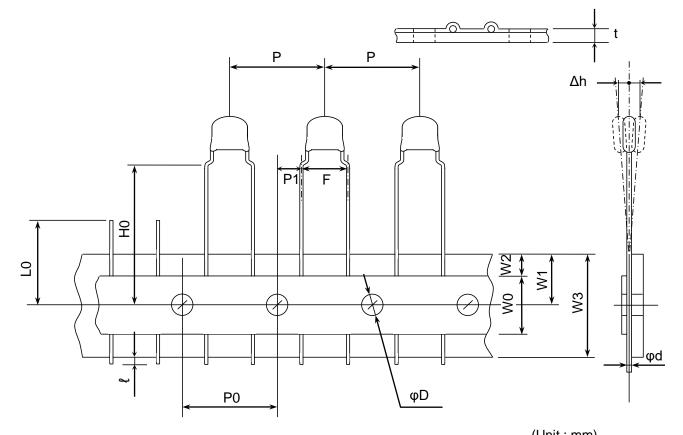
- 4-1. Head of the capacitors shall face the window.
- 4-2. In case of FA22 and FA23 series, a stainless round steel is put in a hole of tape. Please remove a stainless round steel at the time of use.

# Taping dimensions (FA18,FA14,FA16,FA11)



Note	Dimensions	Symbol
	(12.7)	Р
	(12.7)	P0
	( 5.1)	P1
	12.0±1.0	W0
	9.0±0.5	W1
Adhesive tape shall not stick out from carrier tape.	3.0 max.	W2
	18.0+1.0,-0.5	W3
	16.0±0.8	H0
	1.0 max.	ł
	0.6±0.2	t
	11.0 max.	LO
The measurement point is 1.5 to 2.0mm below the kink.	2.5+0.5,-0.2	F
	φ0.5+0.1,-0.03	φd
	(φ4.0)	φD
	(±2)	Δh

# Taping dimensions (FA28,FA24,FA26,FA20,FA22,FA23)



Note	Dimensions	Symbol
	(12.7)	Р
	(12.7)	P0
	( 3.85)	P1
	12.0±1.0	W0
	9.0±0.5	W1
Adhesive tape shall not stick out from carrier tape.	3.0 max.	W2
	18.0+1.0,-0.5	W3
	16.0±0.8	H0
	1.0 max.	ł
	0.6±0.2	t
	11.0 max.	LO
The measurement point is 1.5 to 2.0mm below the kink.	5.0+0.8,-0.2	F
	φ0.5+0.1,-0.03	φd
	(φ4.0)	φD
	(±2)	Δh