

Product Change Notice (PCN)

Subject: Design Change for the ISL6208CHRZ-T and ISL6028CHRZ-TS2705

Publication Date: 6/10/2015

Effective Date: 9/10/2015

Revision Description:

Initial Release

Description of Change:

This notice is to advise our customers of a minor silicon design revision for the ISL6208CHRZ-T and ISL6208CHRZ-TS2705 products only.

Reason for Change:

Product enhancement through a minor design change to reduce the possibility of shoot-through during high duty cycle PWM pulses when driving co-pak FETs with dedicated high-side FET gate driver return path.

Impact on fit, form, function, quality & reliability:

The change will have no impact on the form, fit, function, quality, reliability and environmental compliance of the devices.

Product Identification:

There will be no change in the external marking of the packaged parts or to the product data sheet electrical specification. Product affected by this change is identifiable via Intersil's internal traceability system.

Qualification status: Complete, see attached

Sample availability: 6/10/2015

Device material declaration: Available upon request

Questions or requests pertaining to this change notice, including additional data or samples, must be sent to Intersil within 30 days of the publication date.

For additional information regarding this notice, please contact your regional change coordinator (below)			
Americas: PCN-US@INTERSIL.COM	Europe: PCN-EU@INTERSIL.COM	Japan: PCN-JP@INTERSIL.COM	Asia Pac: PCN-APAC@INTERSIL.COM

Appendix A – Product Verification/Validation (see attached)

Appendix A – Product Verification/Validation

Bench Verification of the ISL6208C Shoot-through Fix Validation		
Test	Complete (y/n)	Shoot-through (y/n)
1 PWM = Tri-Low-High-Tri	yes	no
2 PWM = High-Low-High	yes	no
3 D = 0.1, 0.5, 0.9	yes	no
4 Increase PWM width to see first pulse	yes	no
5 D > 0.9	yes	no