

# 8A, 100V - 200V Ultra Fast Surface Mount Rectifier

#### **FEATURES**

- AEC-Q101 qualified
- Planar technology
- Low power loss, high efficiency
- Ideal for automated placement
- Moisture sensitivity level: level 1, per J-STD-020
- RoHS Compliant
- Halogen-free

#### **APPLICATIONS**

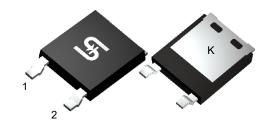
- High frequency switching
- DC/DC
- Snubber

#### **MECHANICAL DATA**

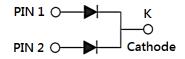
- Case: ThinDPAK
- Molding compound meets UL 94V-0 flammability rating
- Terminal: Matte tin plated leads, solderable per J-STD-002
- Meet JESD 201 class 2 whisker test
- Polarity: As marked
- Weight: 0.193g (approximately)

KEY PARAMETERS			
PARAMETER	VALUE	UNIT	
l <sub>F</sub>	8	Α	
$V_{RRM}$	100 - 200	V	
I <sub>FSM</sub>	130	Α	
T <sub>J MAX</sub>	175 °C		
Package	ThinDPAK		
Configuration	Common cathode		





**ThinDPAK** 



PARAMETER		SYMBOL	PUAD8BCH	PUAD8DCH	UNIT
Marking code on the device			UAD8BC	UAD8DC	
Repetitive peak reverse voltage		$V_{RRM}$	100	200	V
Reverse voltage, total rms value		V <sub>R(RMS)</sub>	70	140	V
Forward current per device		I <sub>F</sub>	8		Α
Surge peak forward current single half	t = 8.3ms	1	130 270		A
sine-wave superimposed on rated load per diode	t = 1.0ms	I <sub>FSM</sub>			
Junction temperature		TJ	-55 to +175		°C
Storage temperature		Tstg	-55 to +175		°C

THERMAL PERFORMANCE			
PARAMETER	SYMBOL	TYP	UNIT
Junction-to-lead thermal resistance	R <sub>OJL</sub>	3.5	°C/W
Junction-to-ambient thermal resistance	Reja	11.8	°C/W
Junction-to-case thermal resistance	Rejc	2.0	°C/W

Thermal Performance Note: Mounted on heat sink with 2" x 3" x 0.25" Al-Plate

ELECTRICAL SPECIFICATIONS (T <sub>A</sub> = 25°C unless otherwise noted)					
PARAMETER	CONDITIONS	SYMBOL	TYP	MAX	UNIT
Forward voltage per diode <sup>(1)</sup>	I <sub>F</sub> = 2A, T <sub>J</sub> = 25°C	Vf	0.80		V
	I <sub>F</sub> = 2A, T <sub>J</sub> = 125°C		0.64		V
	I <sub>F</sub> = 4A, T <sub>J</sub> = 25°C		0.86	0.92	V
	I <sub>F</sub> = 4A, T <sub>J</sub> = 125°C		0.71	-	V
Reverse current @ rated V <sub>R</sub>	T <sub>J</sub> = 25°C	- I <sub>R</sub>	-	2	μA
per diode <sup>(2)</sup>	T <sub>J</sub> = 125°C		2	-	μΑ
Junction capacitance per diode	1MHz, V <sub>R</sub> = 4.0V	Сл	78	-	pF
Daversa resource times	I <sub>F</sub> = 0.5A, I <sub>R</sub> = 1.0A, I <sub>rr</sub> = 0.25A	4	-	25	ns
Reverse recovery time	$I_F = 1.0A$ , $di/dt = 50A/\mu s$ , $V_R = 30V$	t <sub>rr</sub>	24	-	
Reverse recovery current		I <sub>RM</sub>	2.7	-	Α
Reverse recovery charge	I <sub>F</sub> = 4.0A, di/dt = 200A/μs, V <sub>R</sub> = 100V	Qrr	37	-	nC
Reverse recovery time		t <sub>rr</sub>	19	-	ns

## Notes:

- 1. Pulse test with PW = 0.3ms
- 2. Pulse test with PW = 30ms

ORDERING INFORMATION			
ORDERING CODE(1)	PACKAGE	PACKING	
PUAD8xCH	ThinDPAK	4,500 / Tape & Reel	

#### Notes:

1. "x" defines voltage from 100V(PUAD8BCH) to 200V(PUAD8DCH)



## **CHARACTERISTICS CURVES**

(T<sub>A</sub> = 25°C unless otherwise noted)

**Fig.1 Forward Current Derating Curve** 

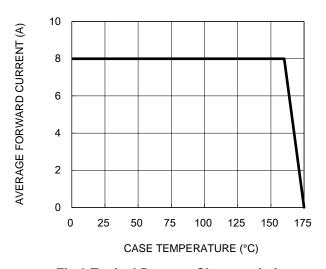


Fig.3 Typical Reverse Characteristics

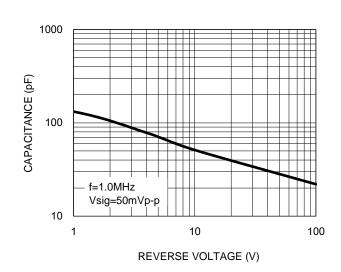
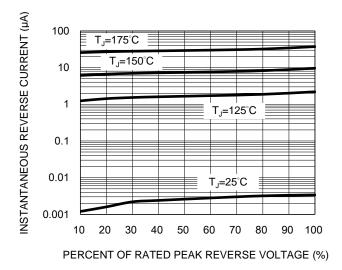


Fig.2 Typical Junction Capacitance

**Fig.4 Typical Forward Characteristics** 



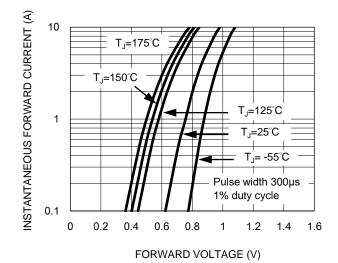
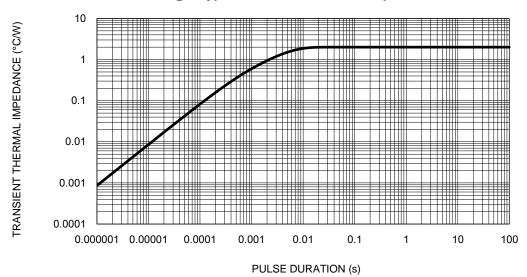


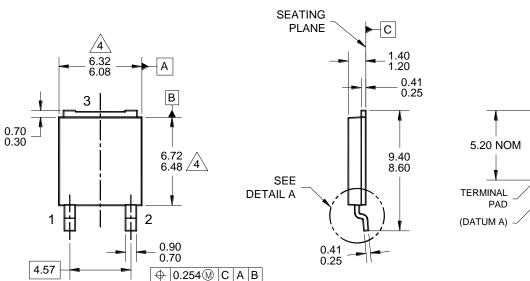
Fig.5 Typical Transient Thermal Impedance

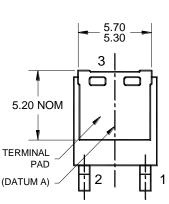


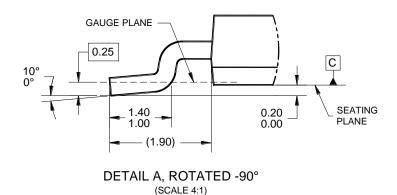


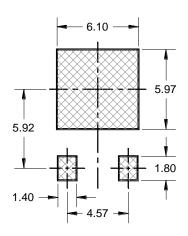
## **PACKAGE OUTLINE DIMENSIONS**

#### **ThinDPAK**

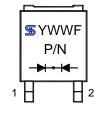








SUGGESTED PAD LAYOUT



#### MARKING DIAGRAM

YWW = DATE CODE F = FACTORY CODE

P/N = MARKING CODE

NOTES: UNLESS OTHERWISE SPECIFIED

- 1. ALL DIMENSIONS ARE IN MILLIMETERS.
- 2. DIMENSIONING AND TOLERANCING PER ASME Y14.5M-1994.
- 3. PACKAGE OUTLINE REFERENCE: JEDEC TO-252, VARIATION AE, ISSUE F.
- MOLDED PLASTIC BODY DIMENSIONS DO NOT INCLUDE MOLD FLASH, PROTRUSION, OR GATE BURRS.
  - 5. DWG NO. REF: HQ2SD07-TDPAK-065 REV A.



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