

C1206T104K5RACTU

Aliases (C1206T104K5RAC7800)

SMD COTS X7R, Ceramic, 0.1 uF, 10%, 50 VDC, X7R, SMD, MLCC, COTS, Temperature Stable, Class II, 1206



Click here for the 3D model.

Dimensions	
Chip Size	1206
L	3.2mm +/-0.2mm
W	1.6mm +/-0.2mm
Т	0.78mm +/-0.10mm
В	0.5mm +/-0.25mm

Packaging Specifications	
Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	4000

SMD COTS X7R
SMD Chip
SMD, MLCC, COTS, Temperature Stable, Class $$ II
Temperature Stable, Class II
Yes
Tin
No
Testing per MIL-PRF-55681 PDA 8%
No
17 mg
78 Weeks
1

Specifications	
Capacitance	0.1 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	50 VDC
Dielectric Withstanding Voltage	125 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	2.5% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	10 GOhms

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