## a YAGEO company



Click here for the 3D model.

| General Information |  |
| :--- | :--- |
| Series | ESD SMD Auto COG |
| Style | SMD Chip |, | Sescription | SMD, MLCC, Temperature Stable, Electro Static |
| :--- | :--- |
| Features | Temperature Stable, Automotive Grade |
| RoHS | Yes |
| Termination | Tin |
| Marking | No |
| Qualifications | Yes |
| AEC-Q200 | 78 me |
| Component | 1 |
| Weight |  |
| Shelf Life |  |
| MSL |  |


| Dimensions |  |
| :--- | :--- |
| Chip Size | 1206 |
| L | $3.2 \mathrm{~mm}+/-0.2 \mathrm{~mm}$ |
| W | $1.6 \mathrm{~mm}+/-0.2 \mathrm{~mm}$ |
| T | $1.6 \mathrm{~mm}+/-0.20 \mathrm{~mm}$ |
| B | $0.5 \mathrm{~mm}+/-0.25 \mathrm{~mm}$ |
|  |  |
| Packaging Specifications | T\&R, 330mm, Plastic Tape |
| Packaging | 8000 |
| Packaging Quantity |  |


| Specifications |  |
| :--- | :--- |
| Capacitance | 0.047 uF |
| Measurement Condition | 1 kHz 1.0 Vrms |
| Capacitance Tolerance | $1 \%$ |
| Voltage DC | 63 VDC |
| ESD Level per AEC-Q200 | $25,000 \mathrm{~V} \mathrm{ESD}$ <br> Level |
| Dielectric Withstanding Voltage | 157.5 VDC |
| Temperature Range | $-55 /+125^{\circ} \mathrm{C}$ |
| Temperature Coefficient | COG |
| Capacitance Change with Reference to $+25^{\circ} \mathrm{C}$ | $30 \mathrm{ppm} / \mathrm{C}, 1 \mathrm{kHz}$ |
| and O VDC Applied (TCC) | 1.0 Vrms |
| Dissipation Factor | $0.1 \% 1 \mathrm{kHz} 1.0 \mathrm{Vrms}$ |
| Aging Rate | $0 \% \mathrm{Loss} / \mathrm{Decade}$ |
| Insulation Resistance | Hour |
|  | 21.2766 GOhms |

