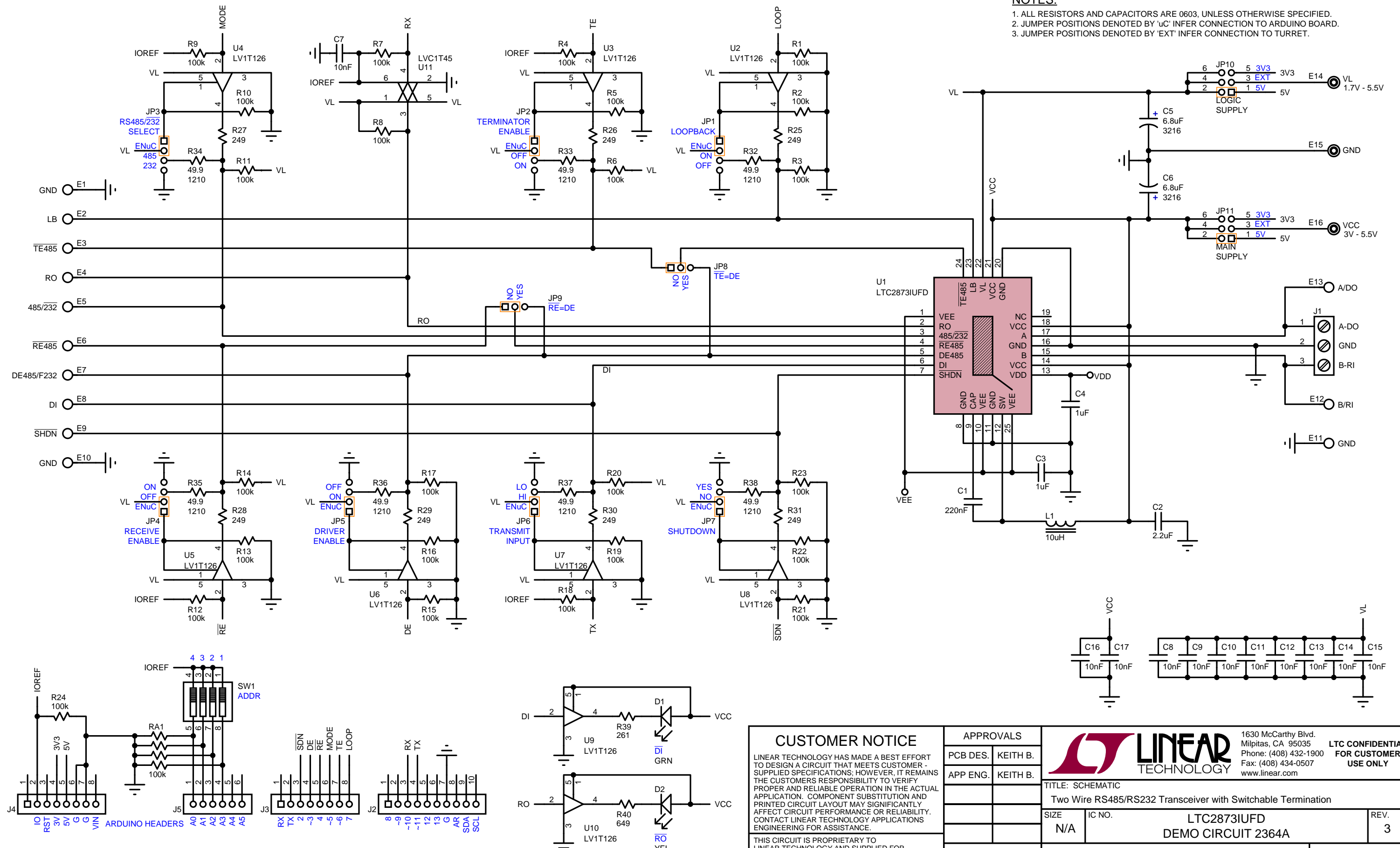


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	3	2ND PROTOTYPE	KEITH B.	1-22-16

- NOTES:**
- ALL RESISTORS AND CAPACITORS ARE 0603, UNLESS OTHERWISE SPECIFIED.
 - JUMPER POSITIONS DENOTED BY 'UC' INFER CONNECTION TO ARDUINO BOARD.
 - JUMPER POSITIONS DENOTED BY 'EXT' INFER CONNECTION TO TURRET.



CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER - SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMERS RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	KEITH B.
APP ENG.	KEITH B.

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TITLE: SCHEMATIC

Two Wire RS485/RS232 Transceiver with Switchable Termination

SIZE	N/A	IC NO.	LTC2873IUFD	REV.	3
SCALE = NONE		DATE:	Wednesday, January 20, 2016	SHEET 1 OF 1	