

Product Change Notification

PCN# 20190520001



- PCN for ARMADA 388 SOM

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Embedded Edge Computing

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Background

Dear customer,

SolidRun Ltd. is discontinuing production of its ARMADA 388 Rev-2.0 SOM products and following the introduction of the ARMADA 388 Rev-2.1 SOM, recommends to migrate to that platform, keeping the same form factor.

You are receiving this notification, because you have purchased one or more of SolidRun Ltd. products affected by the discontinuation, during the past twenty-four (24) months.



Affected SKUs

The affected SKUs are all of the SKUs for ARMADA 388 Rev-2.0. Please see detailed information below on the affected SKUs or customized SKU for specific customers based on the following SKUs.

SKU	Description	New SKU	New Description
SRM6828S32D0 1GE000V20C0	SOM ARMADA A388 32Mb SPI 1GB DDR No eMMC Com. Temp R2.0	SRM6828S32D01GE 000V21C0	SOM A388 32MB SPI 1GB DDR 8GB eMMC Com. Temp R2.1
SRM6828S32D0 1GE000V20I0	SOM ARMADA A388 32Mb SPI 1GB DDR No eMMC Ind. Temp R2.0	SRM6828S32D01GE 000V21I0	SOM ARMADA A388 32Mb SPI 1GB DDR No eMMC Ind. Temp R2.1
SRM6828S32D0 1GE008V20C0	SOM ARMADA A388 32Mb SPI 1GB DDR 8GB eMMC Com. Temp R2.0	SRM6828S32D01GE 008V21C0	SOM ARMADA A388 32Mb SPI 1GB DDR 8GB eMMC Com. Temp R2.1
SRM6828S32D0 1GE008V20I0	SOM ARMADA A388 32Mb SPI 1GB DDR 8GB eMMC Ind. Temp R2.0	SRM6828S32D01GE 008V21I0	SOM ARMADA A388 32Mb SPI 1GB DDR 8GB eMMC Ind. Temp R2.1
SRM6828S32D0 2GE000V20C0	SOM ARMADA A388 32Mb SPI 2GB DDR No eMMC Com. Temp R2.0	SRM6828S32D02GE 000V21C0	SOM ARMADA A388 32Mb SPI 2GB DDR No eMMC Com. Temp R2.1
SRM6828S32D0 2GE000V20I0	SOM ARMADA A388 32Mb SPI 2GB DDR No eMMC Ind. Temp R2.0	SRM6828S32D02GE 000V21I0	SOM ARMADA A388 32Mb SPI 2GB DDR No eMMC Ind. Temp R2.1
SRM6828S32D0 2GE008V20C0	SOM ARMADA A388 32Mb SPI 2GB DDR 8GB eMMC Com. Temp R2.0	SRM6828S32D02GE 008V21C0	SOM ARMADA A388 32Mb SPI 2GB DDR 8GB eMMC Com. Temp R2.1
SRM6828S32D0 2GE008V20I0	SOM ARMADA A388 32Mb SPI 2GB DDR 8GB eMMC Ind. Temp R2.0	SRM6828S32D02GE 008V21I0	SOM ARMADA A388 32Mb SPI 2GB DDR 8GB eMMC Ind. Temp R2.1

Description of Changes

Due to the discontinuation of some of the power management devices used for production of the ARMADA 388 Rev-2.0 SOM, and due to Marvell move from ARMADA 388 A0 revision to B0, we are introducing an updated ARMADA 388 Rev-2.1 SOM which features equivalent power management devices from a different vendor and either ARMADA 388 A0 or B0 revisions.

Electrical

ARMADA 388 Rev-2.0 SOM power management devices from Marvell were replaced with Richtek based power management providing equivalent performance levels. Except for this change there are no changes in the electrical interface of the updated ARMADA 388 Rev-2.1 SOM.

Internal to the SOM, the only change is the introduction of a 2Kbit I2C EEPROM present on I2C0 bus at address 0x53

There is no power consumption differences between the ARMADA 388 Rev-2.0 SOM and the updated ARMADA 388 Rev-2.1 SOM.

Mechanical

There are no mechanical differences between the ARMADA 388 Rev-2.0 SOM and the updated ARMADA 388 Rev-2.1 SOM. Backward mechanical compatibility was preserved.

Software

Customers using u-boot 2018.03 or newer version will not need to make software changes when switching from the ARMADA 388 Rev-2.0 SOM to the updated ARMADA 388 Rev-2.1 SOM.

Customers using older u-boot releases such as 2018.01 or others, should visit SolidRun Developer Center at <https://developer.solid-run.com/knowledge-base/armada-38x-som-u-boot-odt-update/> for further information and instructions on required u-boot changes.

With regards to A0/B0 chip revision change, the change bears no functional impact. The only software visible difference is the value in the Armada 388 revision register that marks the chip revision.

The following software packages are known to be affected by the chip A0/B0 revision register value change:

- Marvell released Linux kernel based on version 3.10 fails to initialize the on chip Ethernet device when seeing the unexpected B0 chip revision.
- Marvell released U-Boot based on version 2013.01 prints an error message at boot time on Armada 388 revision B0. This error message has no functional effect and can be ignored.

No other software packages are known to change behavior when updating the Armada 388 revision to B0.

Transition

SolidRun recommends its customers to transition to the ARMADA 388 Rev-2.1 SOM.

The online store is updated with the new part numbers and customers can order samples:

https://www.solid-run.com/product-category/som-com/?filter_product-family=marvell&filter_cpu-type=arm-a9 (SOMs)

or here: https://www.solid-run.com/product-category/sbcs-fanless-pcs/?filter_product-family=marvell&filter_som-com-family=marvell-a388 (SBCs)

Contact

Please contact SolidRun if you have any questions.

For commercial and sales inquiries please email to: sales@solid-run.com

For technical questions please email to: support@solid-run.com