

Release Notes

${\rm CY3280\text{-}BSM\ Universal\ CapSense}^{\rm @}\ {\rm Simple\ Button\ Module\ Kit}$

Release Date: October 18, 2011

Thank you for your interest in the CY3280-BSM Universal CapSense[®] Simple Button Module Kit. This document lists installation requirements, limitations, and known bugs with release of the kit.

System Requirements and Recommendations

Hardware/Operating System Requirements	Minimum	Recommended
Processor speed	1 GHz	2 GHz
RAM	1 GB	2 GB
Free hard drive space	800 MB	1 GB
Screen resolution	1024×768	1280×1024
CD/DVD drive	✓	✓
USB	Full Speed	2.0 Hi-Speed
Windows XP (SP2 or higher), Vista, or Windows 7	✓	✓
Software Prerequisites	Minimum	Recommended
Microsoft Internet Explorer	7	8
Adobe Reader (for PDF documents)	6	9+
Windows Installer	3.1	3.1
.NET Framework	2.0 SP1	2.0 SP1
PSoC Programmer	3.12	3.13.1
PSoC Designer	5.1	5.1 SP2.1

Installation

To install, insert the kit DVD into your PC's DVD-ROM drive. If the installer does not start automatically, start it manually by executing *cyautorun.exe* in the DVD's root directory. Follow the installation instructions on screen.

Note Go to http://www.cypress.com/go/psocprogrammer for the latest version of PSoC Programmer. If you have a previous installation of PSoC Designer and PSoC Programmer, you must uninstall the same before reinstalling. To uninstall the software go to **Start > Control Panel > Add or Remove Programs** and click on the **Remove** button adjacent to the particular software. Follow the instruction to uninstall.

Updates

Go to http://www.cypress.com/go/cy3280-bsm for the latest software downloads and documents.



Limitations and Known Bugs

- In the code examples, CapSense sensors are tuned for 3 mm overlay; as a result, they can be overly sensitive when used without an overlay.
- Debugging of PSoC Designer 'Chip-Level Design' for the CY3280-20x34 UCC is not supported in Revision** of the CY3280-20x34 CapSense Universal Controller kit...

Documentation

Additional kit documentation is available in the Documentation folder of the DVD.

To access PSoC Designer documentation, go to **PSoC Designer > Help > Documentation.** These documents can also be accessed from the installation directory.

Technical Support

For assistance, go to http://www.cypress.com/go/support or contact our customer support at +1 (800) 541-4736 Ext. 8 (in the USA), or +1 (408) 943-2600 Ext. 8 (International).

Silicon Errata

You can access the latest versions of the silicon errata by visiting http://www.cypress.com/psoc and navigating to Errata.

Additional Information

- For more information about PSoC Designer functionality and releases, review the user guide and release notes on the PSoC Designer web page: http://www.cypress.com/go/psocdesigner
- For more information about PSoC Programmer, supported hardware, and COM layer, visit the PSoC Programmer web page: http://www.cypress.com/go/psocprogrammer
- For a list of trainings on PSoC Designer, visit http://www.cypress.com/psoctraining



Cypress Semiconductor 198 Champion Ct. San Jose, CA 95134-1709 USA Tel: 408.943.2600

Fax: 408.943.4730

Application Support Hotline: 425.787.4814

www.cypress.com

© Cypress Semiconductor Corporation, 2009-2011. The information contained herein is subject to change without notice. Cypress Semiconductor Corporation assumes no responsibility for the use of any circuitry other than circuitry embodied in a Cypress product. Nor does it convey or imply any license under patent or other rights. Cypress products are not warranted nor intended to be used for medical, life support, life saving, critical control or safety applications, unless pursuant to an express written agreement with Cypress. Furthermore, Cypress does not authorize its products for use as critical components in life-support systems where a malfunction or failure may reasonably be expected to result in significant injury to the user. The inclusion of Cypress products in life-support systems application implies that the manufacturer assumes all risk of such use and in doing so indemnifies Cypress against all charges.

Programmable System-on-Chip™ and PSoC Designer™ are trademarks and PSoC® and CapSense® are registered trademarks of Cypress Semiconductor Corp. All other trademarks or registered trademarks referenced herein are property of the respective corporations.

This Source Code (software and/or firmware) is owned by Cypress Semiconductor Corporation (Cypress) and is protected by and subject to worldwide patent protection (United States and foreign), United States copyright laws and international treaty provisions. Cypress hereby grants to licensee a personal, non-exclusive, non-transferable license to copy, use, modify, create derivative works of, and compile the Cypress Source Code and derivative works for the sole purpose of creating custom software and or firmware in support of licensee product to be used only in conjunction with a Cypress integrated circuit as specified in the applicable agreement. Any reproduction, modification, translation, compilation, or representation of this Source Code except as specified above is prohibited without the express written permission of Cypress.

Disclaimer: CYPRESS MAKES NO WARRANTY OF ANY KIND, EXPRESS OR IMPLIED, WITH REGARD TO THIS MATERIAL, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. Cypress reserves the right to make changes without further notice to the materials described herein. Cypress does not assume any liability arising out of the application or use of any product or circuit described herein. Cypress does not authorize its products for use as critical components in life-support systems where a malfunction or failure may reasonably be expected to result in significant injury to the user. The inclusion of Cypress' product in a life-support systems application implies that the manufacturer assumes all risk of such use and in doing so indemnifies Cypress against all charges.

Use may be limited by and subject to the applicable Cypress software license agreement.