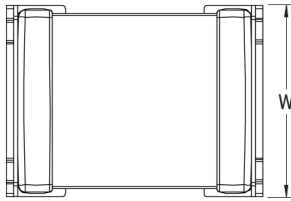
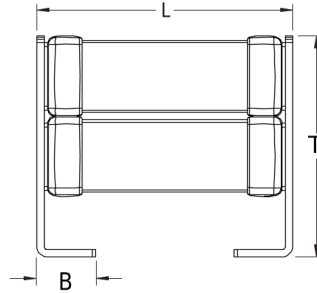


TOP VIEW  
Single or Double Chip Stack



SIDE VIEW  
Double Chip Stack



Click [here](#) for the 3D model.

### Dimensions

L	3.5mm +/-0.3mm
W	2.6mm +/-0.3mm
T	6.15mm +/-0.15mm
B	0.8mm +/-0.15mm

### Packaging Specifications

Packaging	T&R, 180mm, Plastic Tape
Packaging Quantity	300

### General Information

Series	KPS SMD Comm X7R SnPb
Style	Stacked Chip
Description	SMD, MLCC, Stacked, Double Chip, Temperature Stable
Features	Temperature Stable
RoHS	No
Prop 65	<b>⚠ WARNING:</b> Cancer and reproductive harm - <a href="http://www.p65warnings.ca.gov">http://www.p65warnings.ca.gov</a> .
SCIP Number	795b247e-78a9-405c-8728-223c2d9782a2
Termination	Lead (SnPb)
AEC-Q200	No
Component Weight	290 mg
Chip Size	1210-2
Shelf Life	78 Weeks
MSL	1

### Specifications

Capacitance	10 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	20%
Voltage DC	25 VDC
Dielectric Withstanding Voltage	62.5 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	10% 1 kHz 1.0Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	50 MOhms