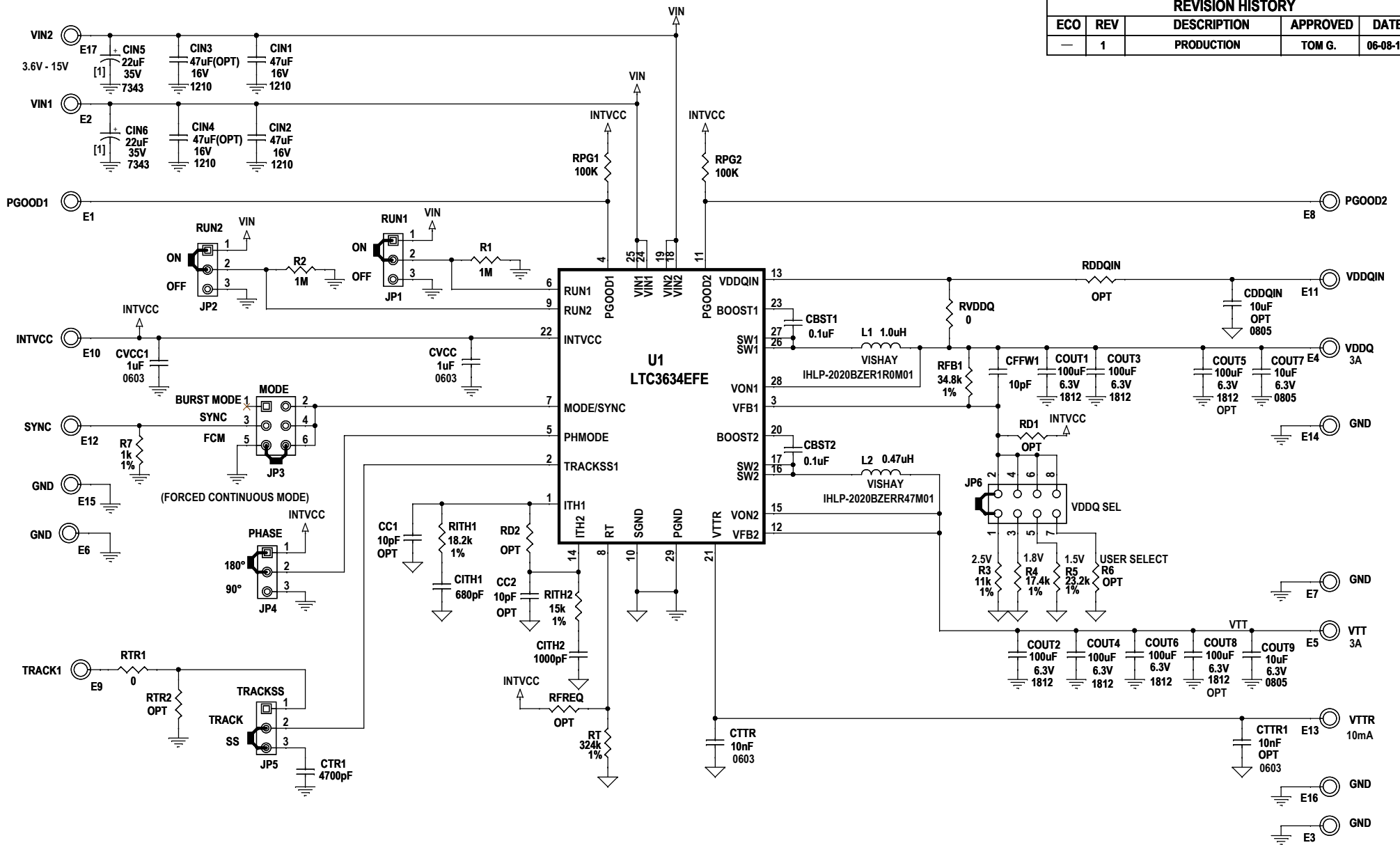


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	PRODUCTION	TOM G.	06-08-11



NOTES: UNLESS OTHERWISE SPECIFIED

- [1] CIN5 and CIN6 ARE INSERTED ON THE DC1839A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE USE OF LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, CIN5 AND CIN6 ARE NOT NEEDED.
- 2. ALL RESISTOR AND CAPACITOR CASE SIZE ARE 0402.

CUSTOMER NOTICE		APPROVALS		LINEAR TECHNOLOGY	
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	JW	1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only	
		APP ENG.	TOM G.		
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		TITLE: SCHEMATIC		DUAL DDR MONOLITHIC SYNCHRONOUS STEP-DOWN REGULATOR	
		SCALE = NONE	DATE: Friday, July 01, 2011	SIZE N/A	IC NO. LTC3634EFE DEMO CIRCUIT 1839A
		SHEET 1 OF 1			