

DATA SHEET

**ELECTROSTATIC DISCHARGE
PROTECTION DEVICES**

INDUSTRIAL / CONSUMER

UBD8C18L01

RoHS compliant & Halogen free



Product specification— March 27, 2021 V.2



ElectroStatic Discharged Protection Devices (ESD) Data Sheet

Description

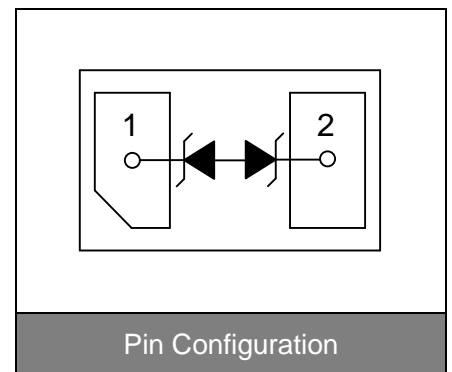
The UBD8C18L01 includes back-to-back TVS diodes fabricated in a proprietary silicon avalanche technology to provide protection for electronic equipment that may experience destructive electrostatic discharge (ESD). This robust diodes can safely absorb repetitive ESD strikes up to the maximum level specified in the IEC61000-4-2 international standard without performance degradation.

The back-to-back configuration provides symmetrical ESD protection for data lines when AC signals are present.



Features

- IEC61000-4-2 ESD 15KV Air, 8KV contact compliance
- SOD882 surface mount package
- Working voltage: 18V
- Low leakage current
- Low operating and clamping voltages
- Lead Free/RoHS compliant
- Flammability rating UL 94V-0
- Meets MSL level 1, per J-STD-020
- Marking: B18



Applications

- RF Signal ESD Protection
- NFC Antenna Protection
- MP3/ PMP
- Portable Medical
- Set Top Boxes
- Smart Phones
- External Storage
- Ultrabooks, Notebooks
- Tablets, eReaders

Maximum Ratings

Rating	Symbol	Value	Unit
ESD voltage (Contact discharge)	V_{ESD}	± 8	kV
ESD voltage (Air discharge)		± 15	
Storage & operating temperature range	T_{STG}, T_J	-55~+150	°C

Electrical Characteristics (T_J=25°C)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	V _{RWM}				18	V
Reverse breakdown voltage	V _{BR}	I _{BR} =1mA	19			V
Reverse leakage current	I _R	V _R =5V			1	μA
Clamping voltage (tp=8/20μs)	V _C	I _{PP} =1A			30	V
Clamping voltage (tp=8/20μs)	V _C	I _{PP} =2A			35	V
Peak pulse current (tp=8/20μs)	I _{PP}				2	A
Off state junction capacitance	C _J	0Vdc,f=1MHz		0.4	0.6	pF

Typical Characteristics Curves

Figure 1. Capacitance vs. Bias Voltage

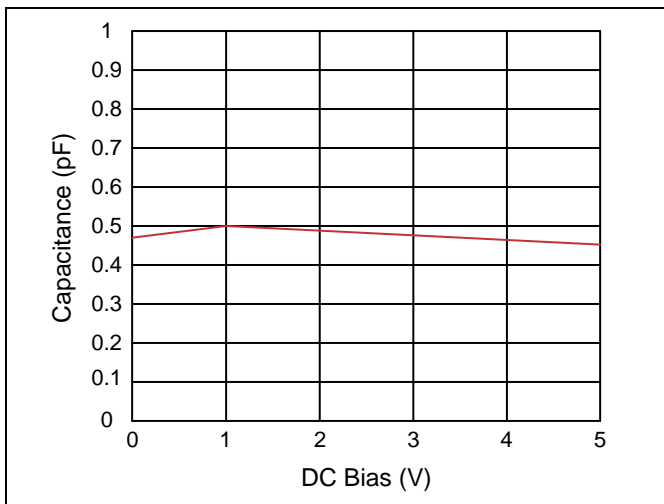


Figure 2. Insertion Loss (S21) I/O to GND

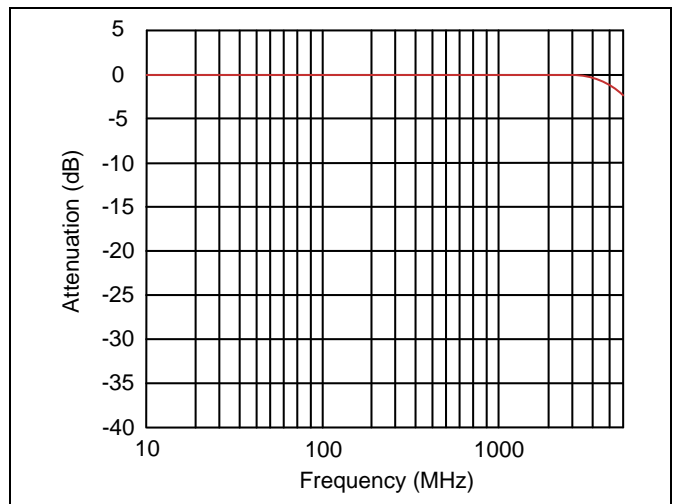


Figure 3. Pulse Waveform

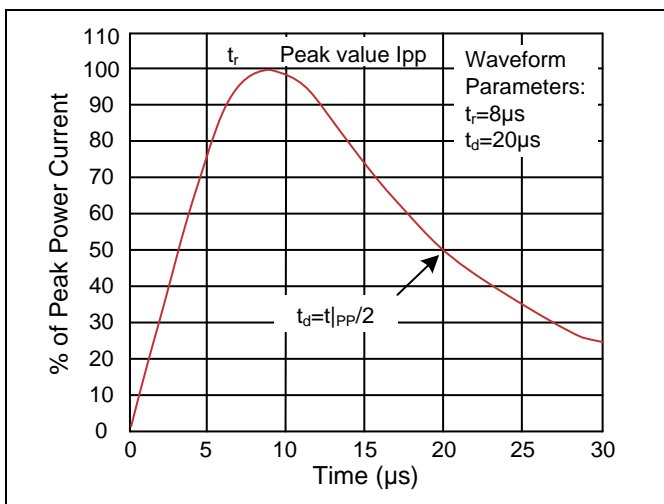
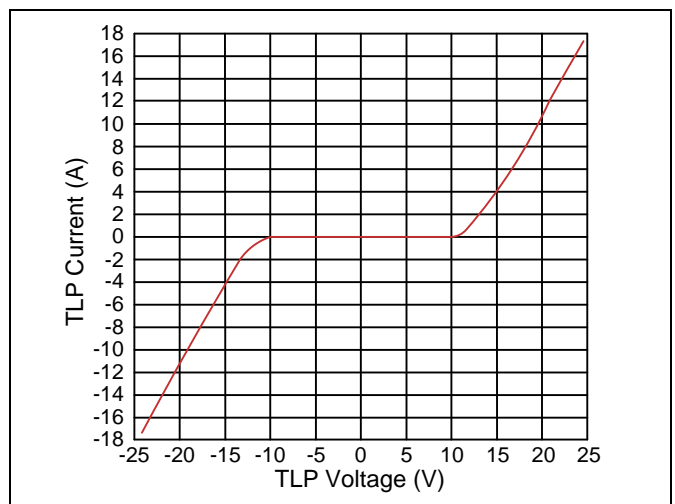
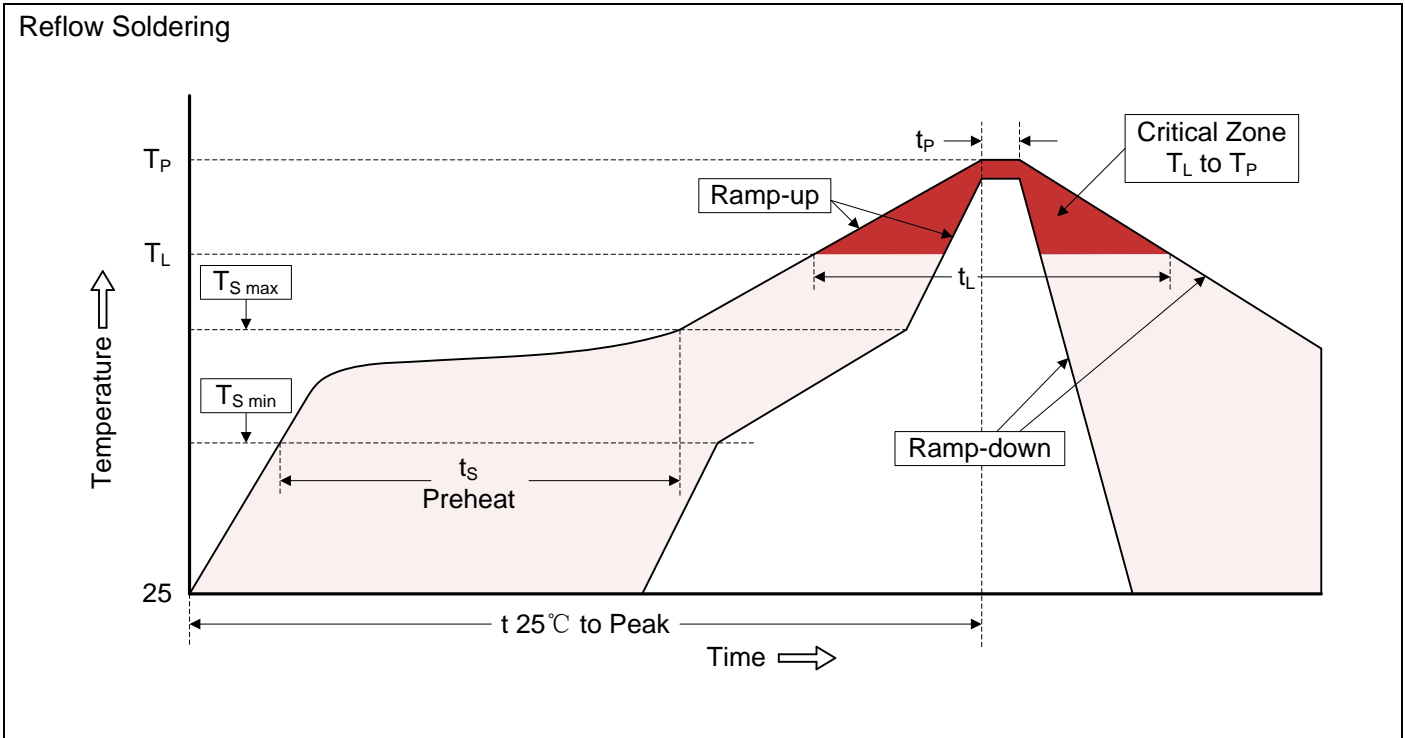


Figure 4. Transmission Line Pulsing (TLP) Plot



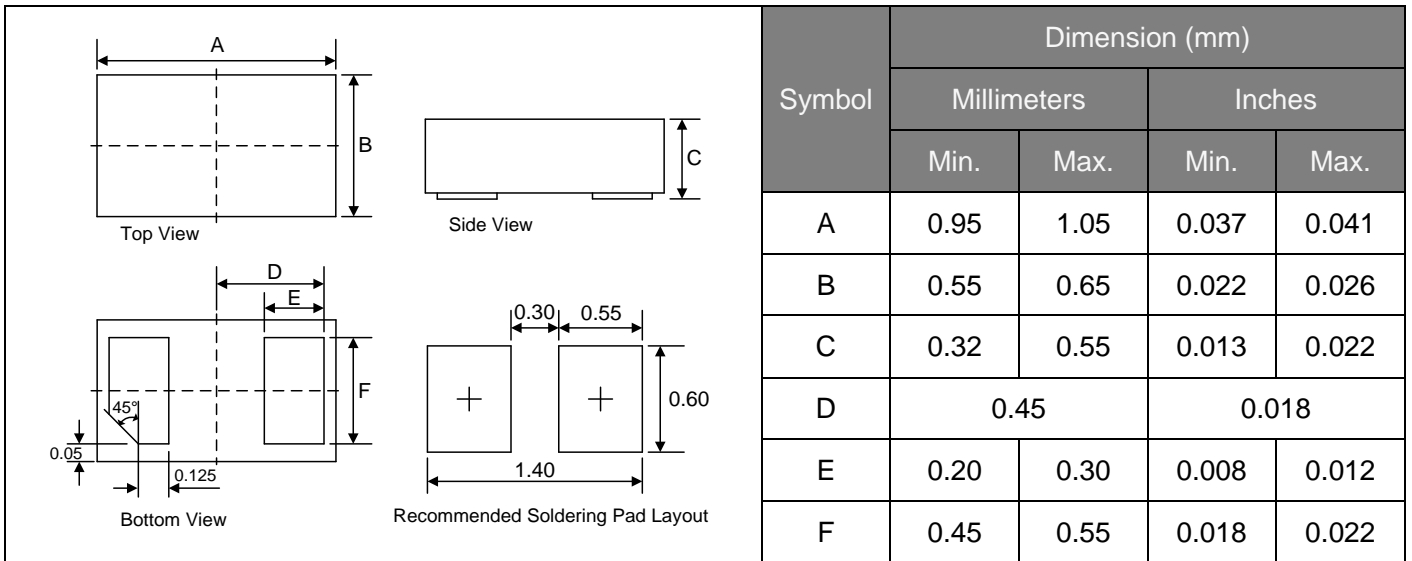
Recommended Soldering Conditions



Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate (T_L to T_P)	3°C/second max.
Preheat -Temperature Min ($T_{S\ min}$) -Temperature Max ($T_{S\ max}$) -Time (min to max) (t_s)	150°C 200°C 60-180 seconds
$T_{S\ max}$ to T_L -Ramp-up Rate	3°C/second max.
Time maintained above: -Temperature (T_L) -Time (t_L)	217°C 60-150 seconds
Peak Temperature (T_P)	260°C
Time within 5°C of actual Peak Temperature (t_P)	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

Dimensions (SOD882)



Packaging

