# FRED Pt<sup>®</sup> Ultrafast Rectifier, 30 A



PRODUCT SUMMARY								
Package	TO-247AD 2L							
I <sub>F(AV)</sub>	30 A							
V <sub>R</sub>	600 V							
V <sub>F</sub> at I <sub>F</sub>	1.15 V							
t <sub>rr</sub> typ.	30 ns							
T <sub>J</sub> max.	175 °C							
Diode variation	Single die							

### **FEATURES**

- Low forward voltage drop
- · Ultrafast recovery time
- 175 °C operating junction temperature
- Designed and qualified according to commercial qualification



- AEC-Q101 qualified, meets JESD 201 class 1 HALOGEN FREE whisker test
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

### DESCRIPTION

Ultralow V<sub>F</sub>, soft-switching ultrafast rectifiers optimized for Discontinuous (Critical) Mode (DCM) Power Factor Correction (PFC).

The minimized conduction loss, optimized stored charge and low recovery current minimized the switching losses and reduce over dissipation in the switching element and snubbers.

The device is also intended for use as a freewheeling diode in power supplies and other power switching applications.

### **APPLICATIONS**

AC/DC SMPS 70 W to 400 W e.g. laptop and printer AC adapters, desktop PC, TV and monitor, games units, and DVD AC/DC power supplies.

ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS					
Repetitive peak reverse voltage	V <sub>RRM</sub>		600	V					
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>C</sub> = 127 °C	30	٨					
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_C$ = 25 °C, $t_p$ = 8.3 ms; half sine wave	250	A					
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C					

<b>ELECTRICAL SPECIFICATIONS</b> ( $T_J$ = 25 °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	I <sub>R</sub> = 100 μA	600	-	-				
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 30 A	-	1.4	2	V			
		I <sub>F</sub> = 30 A, T <sub>J</sub> = 150 °C	-	1.15	1.35				
Reverse leakage current		$V_{R} = V_{R}$ rated	-	0.2	30				
neverse leakage current	I <sub>R</sub>	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	-	250	μA			
Junction capacitance	CT	V <sub>R</sub> = 600 V	-	20	-	pF			
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	-	8.0	-	nH			

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RoHS COMPLIANT



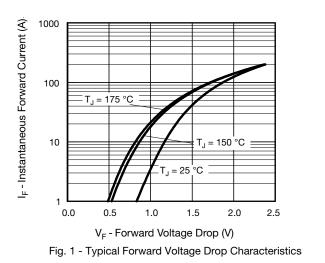


<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>J</sub> = 25 $^{\circ}$ C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS				
		$I_F = 1 \text{ A}, dI_F/dt = 50$	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$			-				
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	45	-	A nC			
		T <sub>J</sub> = 125 °C		-	100	-				
Pook receivery ourrent	I <sub>RRM</sub>	T <sub>J</sub> = 25 °C	I <sub>F</sub> = 30 A dI <sub>F</sub> /dt = 200 A/μs V <sub>B</sub> = 200 V	-	5.6	-				
Peak recovery current		T <sub>J</sub> = 125 °C		-	10	-				
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 25 °C	•R = 200 •	-	127	-				
		T <sub>J</sub> = 125 °C		-	580	-				

THERMAL - MECHANICAL SPECIFICATIONS										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C				
Thermal resistance, junction to case	R <sub>thJC</sub>		-	0.7	1.1	°C/W				
Thermal resistance, junction to ambient per leg	R <sub>thJA</sub>	Typical socket mount	-	-	70					
Thermal resistance, case to heat sink	R <sub>thCS</sub>	Mounting surface, flat, smooth, and greased	-	0.5	-					
Weight			-	2.0	-	g				
Weight			-	0.07	-	oz.				
Mounting torque			1.2 (10)	-	2.4 (20)	kgf · cm (lbf · in)				
Marking device		Case style: TO-247AD 2L		EPU3006LH						

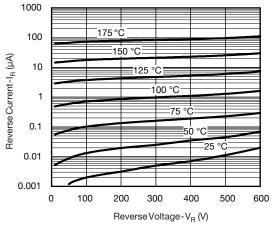
## VS-EPU3006LHN3

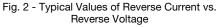
## **Vishay Semiconductors**



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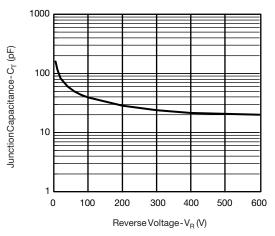
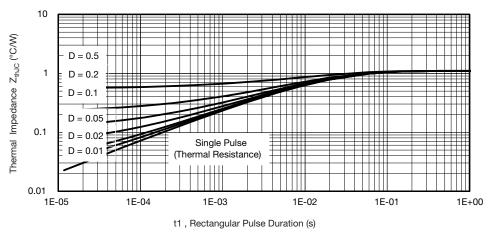
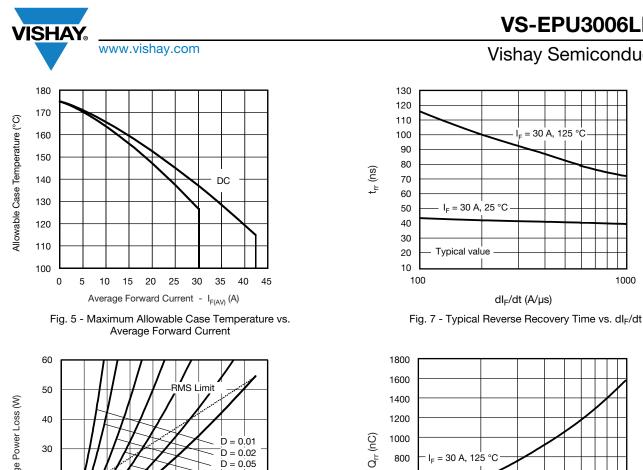


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage







D = 0.1

D = 0.2 D = 0.5

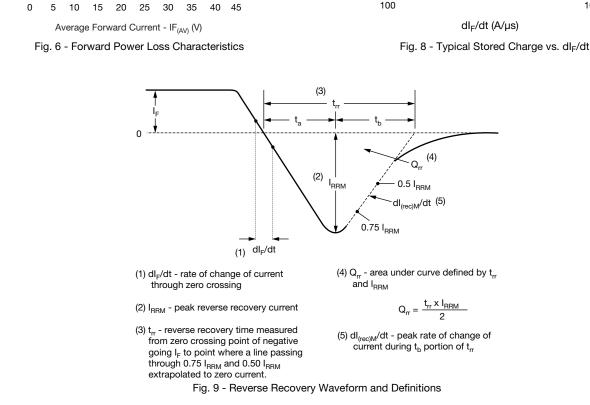
DC

## **Vishay Semiconductors**

1000

Typical value

1000



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600

400

200

0

100

I<sub>F</sub> = 30 A, 25 °C

Average Power Loss (W)

20

10

0



### **ORDERING INFORMATION TABLE**

Device code	VS-	E	Ρ	U	30	06	L	н	N3
	1	2	3	4	5	6	7	8	9
	1 - Vishay Semiconductors product								
	2 - E = single diode 2-pin								
	3 - P = TO-247								
	4 -	U =	ultrafas	t recove	ery time				
	5 -	Cur	rent cod	le (30 =	30 A)				
	6 -	Volt	age coo	le (06 =	600 V)				
	7 - L = long lead								
	8 - H = AEC-Q101 qualified								
	9 -			ntal digit: en-free,		ompliar	nt, and t	otally le	ad (Pb)

ORDERING INFORMATION (Example)								
PREFERRED P/N QUANTITY PER TUBE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION								
VS-EPU3006LHN3	25	500	Antistatic plastic tube					

LINKS TO RELATED DOCUMENTS							
Dimensions TO-247AD 2L www.vishay.com/doc?95536							
Part marking information	TO-247AD 2L	www.vishay.com/doc?95648					



**TO-247AD 2L** 

#### **DIMENSIONS** in millimeters and inches



Section C - C, D - D

(b, b2)

(4)

View	<u>/ B</u>

SYMBOL	MILLIN	MILLIMETERS		INCHES		NOTES	SYMBOL	MILLIMETERS		INCHES		NOTES
STIVIDUL	MIN.	MAX.	MIN.	MAX.	NOTES		STMDUL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.65	5.31	0.183	0.209			E	15.29	15.87	0.602	0.625	3
A1	2.21	2.59	0.087	0.102			E1	13.46	-	0.53	-	
A2	1.50	2.49	0.059	0.098			е	5.46	BSC	0.215	5 BSC	
b	0.99	1.40	0.039	0.055			ØК	0.2	254	0.0	010	
b1	0.99	1.35	0.039	0.053			L	19.81	20.32	0.780	0.800	
b2	1.65	2.39	0.065	0.094			L1	3.71	4.29	0.146	0.169	
b3	1.65	2.34	0.065	0.092			ØР	3.56	3.66	0.14	0.144	
С	0.38	0.89	0.015	0.035			Ø P1	-	6.98	-	0.275	
c1	0.38	0.84	0.015	0.033			Q	5.31	5.69	0.209	0.224	
D	19.71	20.70	0.776	0.815	3		R	4.52	5.49	0.178	0.216	
D1	13.08	-	0.515	-	4		S	5.51	BSC	0.217	' BSC	
D2	0.51	1.35	0.020	0.053				•		•		•

#### Notes

<sup>(1)</sup> Dimensioning and tolerancing per ASME Y14.5M-1994

(2) Contour of slot optional

(3) Dimension D and E do not include mold flash. These dimensions are measured at the outermost extremes of the plastic body

(4) Thermal pad contour optional with dimensions D1 and E1

(5) Lead finish uncontrolled in L1

<sup>(6)</sup> Ø P to have a maximum draft angle of 1.5 to the top of the part with a maximum hole diameter of 3.91 mm (0.154")

<sup>(7)</sup> Outline conforms to JEDEC<sup>®</sup> outline TO-247 with exception of dimension A min., D, E min., Q min., S, and note 4

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