

CHIP COIL (CHIP INDUCTORS) LQW18AN□□□□1ZD Murata Standard Reference Specification 【AEC-Q200】

1. Scope

This reference specification applies to LQW18AN_1ZD series, Chip coil (Chip Inductors) for automotive Electronics based on AEC-Q200 except for Power train and Safety.

2. Part Numbering

(ex) LQ W 18 A N 2N2 D 1 Z D

Product ID Structure Dimension (L×W) and Characteristics Category Characteristics Category Category

3. Rating

Operating Temperature Range
 Storage Temperature Range.
 -55°C to +125°C
 -55°C to +125°C

Customer Part Number	MURATA Part Number	(Ref	er to below omment)	Q (*1) (min.)	DC Resistance (*1) (Ω max.)	Self Resonant Frequency (*1) (MHz min.)	Rated Current (mA)	ESD Rank 6: 25kV			
	LQW18AN2N2D1ZD	2.2	D:±0.5nH	25	0.018	18000	1400				
	LQW18AN3N9C1ZD		C:±0.2nH			44000	4000				
	LQW18AN3N9D1ZD	3.9	D:±0.5nH		0.032	11000	1000				
	LQW18AN5N6D1ZD	5.6	D:±0.5nH			10000					
	LQW18AN6N8C1ZD	6.8	C:±0.2nH		0.045		900				
	LQW18AN6N8D1ZD	0.0	D:±0.5nH	38		7000					
	LQW18AN8N2D1ZD	8.2	D:±0.5nH	30		5000					
	LQW18AN10NG1ZD	10			0.058		750	6			
	LQW18AN10NJ1ZD	10	G:±2%								
	LQW18AN12NG1ZD	12	J:±5%		0.071						
	LQW18AN12NJ1ZD	12			0.071						
	LQW18AN15NJ1ZD	15	J:±5%			4500					
	LQW18AN18NG1ZD	18			0.085	3500	700				
	LQW18AN18NJ1ZD	10					3500				
	LQW18AN22NG1ZD	22 G:±2% J:±5%	G:±2%	77	G:±2%	G:±2%	42 0.099	0.099	3200	640	
	LQW18AN22NJ1ZD		J:±5%		42	0.099	3200	040			
	LQW18AN27NG1ZD		27		27		0.116	2800	590		
	LQW18AN27NJ1ZD	۷1				0.110	2000	290			
	LQW18AN33NJ1ZD	33	J:±5%		0.132	2500	550				

(*1) Standard Testing Conditions

《Unless otherwise specified》

Temperature : Ordinary Temperature / 15°C to 35°C

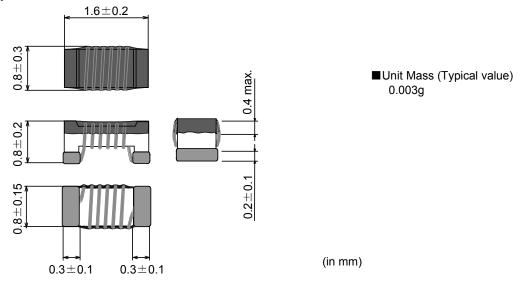
Humidity : Ordinary Humidity / 25%(RH) to 85%(RH)

《In case of doubt》

Temperature : 20°C±2°C

Humidity : 60%(RH) to 70%(RH)
Atmospheric Pressure : 86kPa to 106 kPa

4. Appearance and Dimensions



5. Electrical Performance

No.	Item	Specification	Test Method
5.1	Inductance	Inductance shall meet item 3.	Measuring Equipment: KEYSIGHT 4991A or equivalent Measuring Frequency: <inductance> 100MHz</inductance>
			Position coil under test as shown in below and contact coil with each terminal by adding weight.
5.2	Q	Q shall meet item 3.	
			6.97mm
			Measuring Method : See the endnote. <electrical :<="" p="" performance=""></electrical>
			Measuring Method of Inductance/Q>
5.3	DC Resistance	DC Resistance shall meet item 3.	Measuring Equipment : Digital multi meter
5.4	Self Resonant Frequency(S.R.F)	S.R.F shall meet item 3.	Measuring Equipment : KEYSIGHT 5230A or equivalent
5.5	Rated Current	Self temperature rise shall be limited to 20°C max. Inductance Change : within ±10%	The rated current is applied.



6. Q200 Requirement

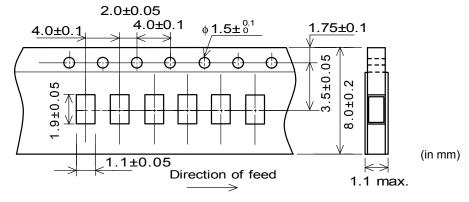
6.1.Performance (based on Table 5 for Magnetics(Inductors / Transformer) AEC-Q200 Rev.D issued June 1. 2010

		D issued June 1. 2010			
	AEC-Q200		Murata Specification / Deviation		
No					
3	High Temperature	1000hours at 125 deg C Set for 24hours at room	Meet Table A a Table A		
	Exposure	temperature, then measured.	Table A	Appearance	No damage
	·			Inductance change (at 100MHz)	Within ±5%
4	Temperature Cycling	1000cycles -40 deg C to +125 deg C Set for 24hours at room temperature,then measured.	Meet Table A a	after testing.	
7	Biased Humidity	1000hours at 85 deg C, 85%RH unpowered.	Meet Table A a	after testing.	
8	Operational Life	Apply Rated Current 125 deg C 1000hours Set for 24hours at room temperature, then measured	Meet Table A a	after testing.	
9	External Visual	Visual inspection	No abnormaliti	es	
10	Physical Dimension	Meet ITEM 4 (Style and Dimensions)	No defects		
12	Resistance to Solvents	Per MIL-STD-202 Method 215	Not Applicable		
13	Mechanical Shock	Per MIL-STD-202 Method 213 Condition C: 100g's(0.98N), 6ms, Half sine, 12.3ft/s	Meet Table A a	after testing.	
14	Vibration	5g's(0.049N) for 20 minutes, 12cycles each of 3 orientations Test from 10-2000Hz.	Meet Table A a	after testing.	
	Resistance to Soldering Heat	No-heating Solder temperature 260C+/-5 deg C Immersion time 10s	Pre-heating : 1 Meet Table A a	150C +/-10 deg C, 60s tafter testing.	o 90s
17	ESD	Per AEC-Q200-002	ESD Rank : Ro Meet Table A a	efer to Item 3. Rating. after testing.	
18	Solderbility	Per J-STD-002	Method b : No 95% of the terr (Except expose	minations is to be solder	red.
19	Electrical Characterization	Measured : Inductance	No defects		
20	Flammability	Per UL-94	Not Applicable		

	AEC-Q200		Murata Specification / Doviation		
No	Stress	Test Method	Murata Specification / Deviation		
21	21 Board Flex Epoxy-PCB(1.6mm)		Meet Table B after testing.		
		Deflection 2mm(min) Holding time 60s	<u>Table B</u>	Appearance	No damage
			DC resistance change Within ±10%		Within ±10%
22	Terminal Strength	l	Murata Deviation Request : 10N/5s No defect		

7. Specification of Packaging

7.1 Appearance and Dimensions of paper tape (8mm-wide)



7.2 Specification of Taping

- (1) Packing quantity (standard quantity)
 - 4,000 pcs. / reel
- (2) Packing Method

Products shall be packed in the cavity of the base tape and sealed by top tape and bottom tape.

(3) Sprocket hole

The sprocket holes are to the right as the tape is pulled toward the user.

(4) Spliced point

Base tape and Top tape has no spliced point.

(5) Missing components number

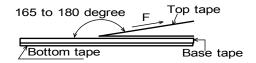
Missing components number within 0.1 % of the number per reel or 1 pc., whichever is greater, and are not continuous. The Specified quantity per reel is kept.

7.3 Pull Strength

Top tape	5N min.
Bottom tape	ON HIII.

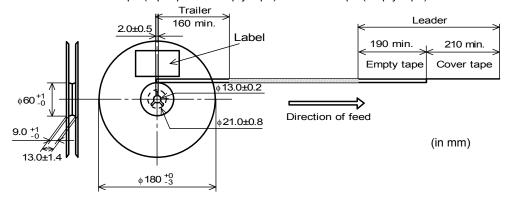
7.4 Peeling off force of cover tape

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Speed of Peeling off	300mm/min
Peeling off force	0.1N to 0.6N (minimum value is typical)



7.5 Dimensions of Leader-tape, Trailer and Reel

There shall be leader-tape (top tape and empty tape) and trailer-tape (empty tape) as follows.



7.6 Marking for reel

Customer part number, MURATA part number, Inspection number(*1), RoHS marking(*2), Quantity etc · · ·

- - (1) Factory Code

(2) Date First digit : Year / Last digit of year

Second digit : Month / Jan. to Sep. \rightarrow 1 to 9, Oct. to Dec. \rightarrow O, N, D

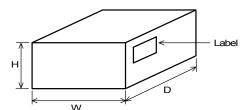
Third, Fourth digit : Day

- (3) Serial No.
- *2) <Expression of RoHS marking> ROHS $-\underline{Y}(\underline{\triangle})$ (1) (2)
 - (1) RoHS regulation conformity
 - (2) MURATA classification number

7.7 Marking for Outside package (corrugated paper box)

Customer name, Purchasing order number, Customer part number, MURATA part number, RoHS Marking (*2) , Quantity, etc \cdots

7.8 Specification of Outer Case



Outer Case Dimensions (mm)			Standard Reel Quantity
W	D	Н	in Outer Case (Reel)
186	186	93	5

 Above Outer Case size is typical. It depends on a quantity of an order.

8. A Caution

8.1 Limitation of Applications

Please contact us before using our products for the applications listed below which require especially high reliability for the prevention of defects which might directly cause damage to the third party's life, body or property.

- (1) Aircraft equipment
- (6) Transportation equipment (trains, ships, etc.)
- (2) Aerospace equipment
- (7) Traffic signal equipment
- (3) Undersea equipment
- (8) Disaster prevention / crime prevention equipment
- (4) Power plant control equipment
- (9) Data-processing equipment
- (5) Medical equipment
- (10) Applications of similar complexity and /or reliability requirements to the applications listed in the above

8.2 Caution(Rating)

Do not exceed maximum rated current of the product. Thermal stress may be transmitted to the product and short / open circuit of the product or falling off the product may be occurred.

8.3 Fail-safe

Be sure to provide an appropriate fail-safe function on your product to prevent a second damage that may be caused by the abnormal function or the failure of our product.



9. Notice

Products can only be soldered with reflow.

This product is designed for solder mounting.

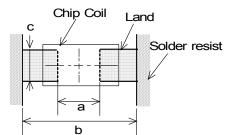
Please consult us in advance for applying other mounting method such as conductive adhesive.

9.1 Land pattern designing

Recommended land patterns for reflow soldering are as follows:

These have been designed for Electric characteristics and solderability.

Please follow the recommended patterns. Otherwise, their performance which includes electrical performance or solderability may be affected, or result to "position shift" in soldering process.



Α	0.6 to 0.8
В	1.9 to 2.0
С	0.7 to 1.0
	(in mm)

9.2 Flux, Solder

· Use rosin-based flux.

Includes middle activator equivalent to 0.06(wt)% to 0.1(wt)% Chlorine.

Don't use highly acidic flux with halide content exceeding 0.2(wt)% (chlorine conversion value).

Don't use water-soluble flux.

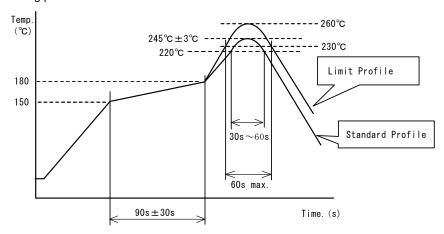
- · Use Sn-3.0Ag-0.5Cu solder.
- Standard thickness of solder paste : 100μ m to 150μ m.

9.3 Reflow soldering conditions

 Pre-heating should be in such a way that the temperature difference between solder and product surface is limited to 150°C max. Cooling into solvent after soldering also should be in such a way that the temperature difference is limited to 100°C max.

Insufficient pre-heating may cause cracks on the product, resulting in the deterioration of products quality.

- Standard soldering profile and the limit soldering profile is as follows.
 The excessive limit soldering conditions may cause leaching of the electrode and / or resulting in the deterioration of product quality.
- · Reflow soldering profile



	Standard Profile	Limit Profile	
Pre-heating	150°C~180°C 、90s±30s		
Heating	above 220°C, 30s~60s	above 230°C, 60s max.	
Peak temperature	245°C±3°C	260°C,10s	
Cycle of reflow	2 times	2 times	



9.4 Reworking with soldering iron

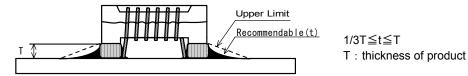
The following conditions must be strictly followed when using a soldering iron.

Pre-heating	150°C,1 min
Tip temperature	350°C max.
Soldering iron output	80W max.
Tip diameter	φ3mm max.
Soldering time	3(+1,-0)s
Time	2 times

Note: Do not directly touch the products with the tip of the soldering iron in order to prevent the crack on the products due to the thermal shock.

9.5 Solder Volume

- Solder shall be used not to be exceed the upper limits as shown below.
- Accordingly increasing the solder volume, the mechanical stress to Chip is also increased.
 Exceeding solder volume may cause the failure of mechanical or electrical performance.

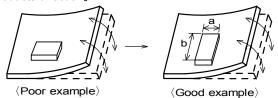


9.6 Product's location

The following shall be considered when designing and laying out P.C.B.'s.

(1) P.C.B. shall be designed so that products are not subject to the mechanical stress due to warping the board.

[Products direction]

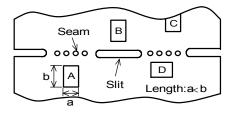


Products shall be located in the sideways direction (Length : a < b) to the mechanical stress.

(2) Components location on P.C.B. separation.

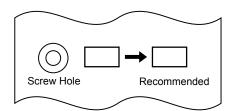
It is effective to implement the following measures, to reduce stress in separating the board. It is best to implement all of the following three measures; however, implement as many measures as possible to reduce stress.

Contents of Measures	Stress Level
(1) Turn the mounting direction of the component parallel to the board separation surface.	A > D*1
(2) Add slits in the board separation part.	A > B
(3) Keep the mounting position of the component away from the board separation surface.	A > C



*1 A > D is valid when stress is added vertically to the perforation as with Hand Separation.
If a Cutting Disc is used, stress will be diagonal to the PCB, therefore A > D is invalid.

(3) Mounting Components Near Screw Holes
When a component is mounted near a screw hole,
it may be affected by the board deflection that occurs
during the tightening of the screw. Mount the component
in a position as far away from the screw holes as possible.





9.7 Cleaning Conditions

Products shall be cleaned on the following conditions.

- (1) Cleaning temperature shall be limited to 60°C max. (40°C max for IPA)
- (2) Ultrasonic cleaning shall comply with the following conditions with avoiding the resonance phenomenon at the mounted products and P.C.B.

Power: 20 W / I max. Frequency: 28kHz to 40kHz Time: 5 min max.

- (3) Cleaner
 - 1. Alcohol type cleaner Isopropyl alcohol (IPA)
 - 2. Aqueous agent

PINE ALPHA ST-100S

- (4) There shall be no residual flux and residual cleaner after cleaning.
 - In the case of using aqueous agent, products shall be dried completely after rinse with de-ionized water in order to remove the cleaner.
- (5) Other cleaning Please contact us.

9.8 Resin coating

The inductance value may change due to high cure-stress of resin to be used for coating/molding products.

An open circuit issue may occur by mechanical stress caused by the resin, amount/cured shape of resin, or operating condition etc. Some resin contains some impurities or chloride possible to generate chlorine by hydrolysis under some operating condition may cause corrosion of wire of coil, leading to open circuit.

So, please pay your careful attention when you select resin in case of coating/molding the products with the resin. Prior to use the coating resin, please make sure no reliability issue is observed by evaluating products mounted on your board.

9.9 Caution for use

- Sharp material such as a pair of tweezers or other material such as bristles of cleaning brush , shall not be touched to the winding portion to prevent the breaking of wire.
- · Mechanical shock should not be applied to the products mounted on the board to prevent the breaking of the core.

9.10 Notice of product handling at mounting

In some mounting machines, when picking up components support pin pushes up the components from the bottom of base tape. In this case, please remove the support pin. The support pin may damage the components and break wire. In rare case, the laser recognition can not recognize this component. Please contact us when you use laser recognition. (There is no problem with the permeation and reflection type.)

9.11 Handling of a substrate

After mounting products on a substrate, do not apply any stress to the product caused by bending or twisting to the substrate when cropping the substrate, inserting and removing a connector from the substrate or tightening screw to the substrate.

Excessive mechanical stress may cause cracking in the product.

Bending

Twisting



9.12 Storage and Handing Requirements

(1) Storage period

Use the products within 12 months after delivered.

Solderability should be checked if this period is exceeded.

- (2) Storage conditions
 - Products should be stored in the warehouse on the following conditions.

Temperature : -10°C to 40°C

Humidity : 15% to 85% relative humidity No rapid change on temperature and humidity

- Don't keep products in corrosive gases such as sulfur, chlorine gas or acid, or it may cause oxidization of electrode, resulting in poor solderability.
- Products should not be stored on bulk packaging condition to prevent the chipping of the core and the breaking of winding wire caused by the collision between the products.
- Products should be stored on the palette for the prevention of the influence from humidity, dust and so on.
- Products should be stored in the warehouse without heat shock, vibration, direct sunlight and so on.
- (3) Handling Condition

Care should be taken when transporting or handling product to avoid excessive vibration or mechanical shock.



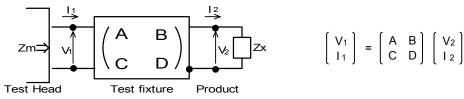
10. **A** Note

- (1) Please make sure that your product has been evaluated in view of your specifications with our product being mounted to your product.
- (2) You are requested not to use our product deviating from the reference specifications.
- (3) The contents of this reference specification are subject to change without advance notice.

 Please approve our product specifications or transact the approval sheet for product specifications before ordering.

-<Electrical Performance:Measuring Method of Inductance / Q> -

(1) Residual elements and stray elements of test fixture can be described by F-parameter shown in following.



(2) The impedance of chip coil Zx and measured value Zm can be described by input/output current/voltage.

$$Zm = \frac{V_1}{I_1}$$
, $Zx = \frac{V_2}{I_2}$

(3) Thus, the relation between Zx and Zm is following;

$$Zx= \alpha$$
 $\frac{Zm-\beta}{1-Zm\Gamma}$ where, $\alpha=D/A=1$ $\beta=B/D=Zsm-(1-Yom~Zsm)Zss$ $\Gamma=C/A=Yom$

Zsm: measured impedance of short chip
Zss: residual impedance of short chip (0.771nH)
Yom: measured admittance when opening the fixture

(4) Lx and Qx shall be calculated with the following equation.

$$Lx = \frac{Im(Zx)}{2\pi f}, \quad Qx = \frac{Im(Zx)}{Re(Zx)} \qquad \begin{array}{c} Lx: \text{ Inductance of chip coil} \\ Qx: Q \text{ of chip coil} \\ f: \text{ Measuring frequency} \end{array}$$