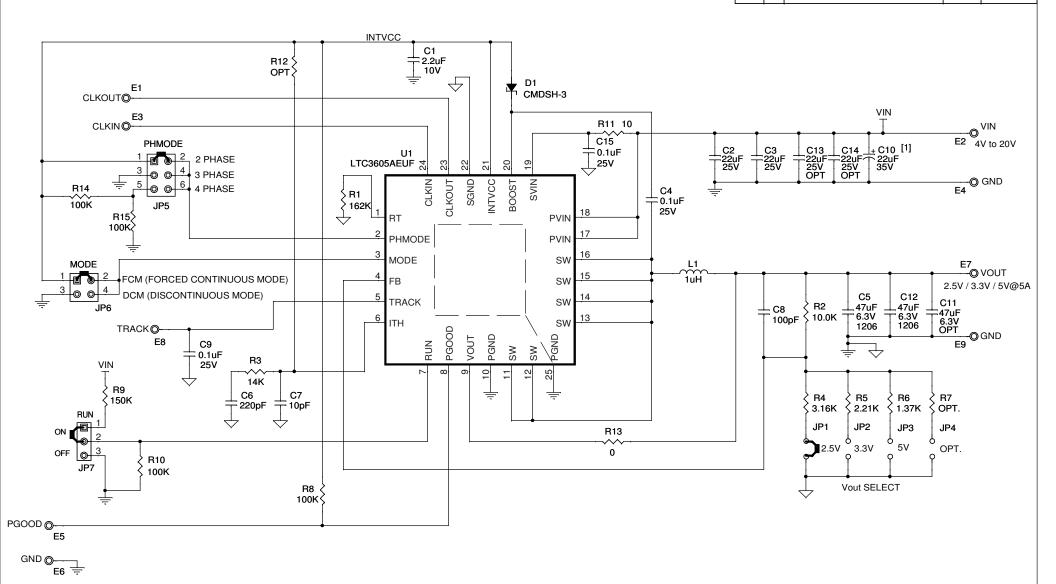
		REVISION HISTORY		
ECO	REV	DESCRIPTION	DATE	APPROVED
	1	PRODUCTION	11/01/13	TOM G.



NOTES:

[1] C10 IS AN OPTIONAL CAPACITOR. IT IS INSERTED ON THE DC1897A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, THE CAPACITOR IS NOT NEEDED.

CUSTOMER NOTICE LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS;	APPROVALS	1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507 www.linea	ar.com
HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL	PCB DES. MI	LTC Confidential-For Customer Use	Only
APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT	APP ENG. TOM G.	TITLE: SCHEMATIC 5A MONOLITHIC SYNCHRONOUS	
PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		BUCK REGULATOR	
		SIZE IC NO. LTC3605AEUF	REV.
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND	 	N/A PCB NO. DEMO CIRCUIT 1897B	
SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.	SCALE = NONE	DATE: 11/07/13 10:52:40 SHEET 1 0)F 1