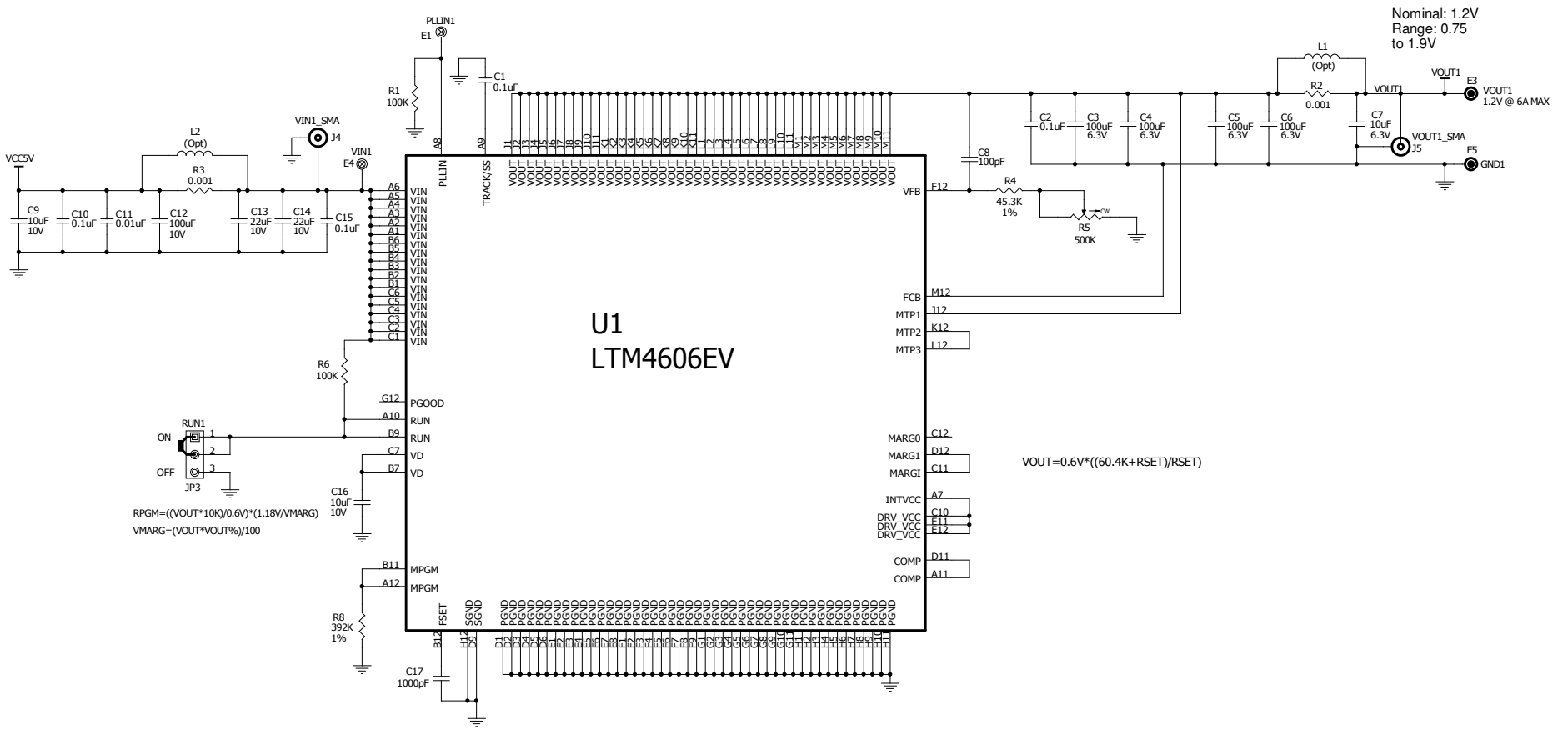


REVISION #				REVISION HISTORY		
SD	PC	AD	FD	DESCRIPTION	DATE	APPROVED
1	0	0	0	1st Release		



U1  
LTM4606EV

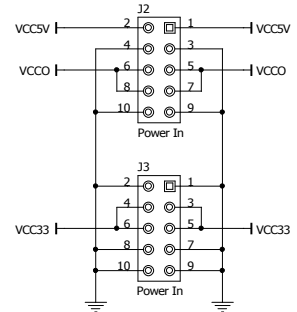
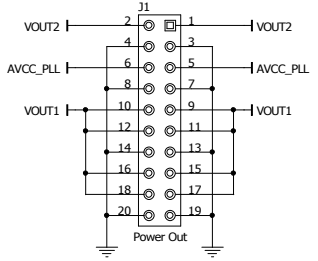
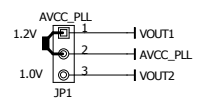
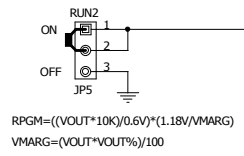
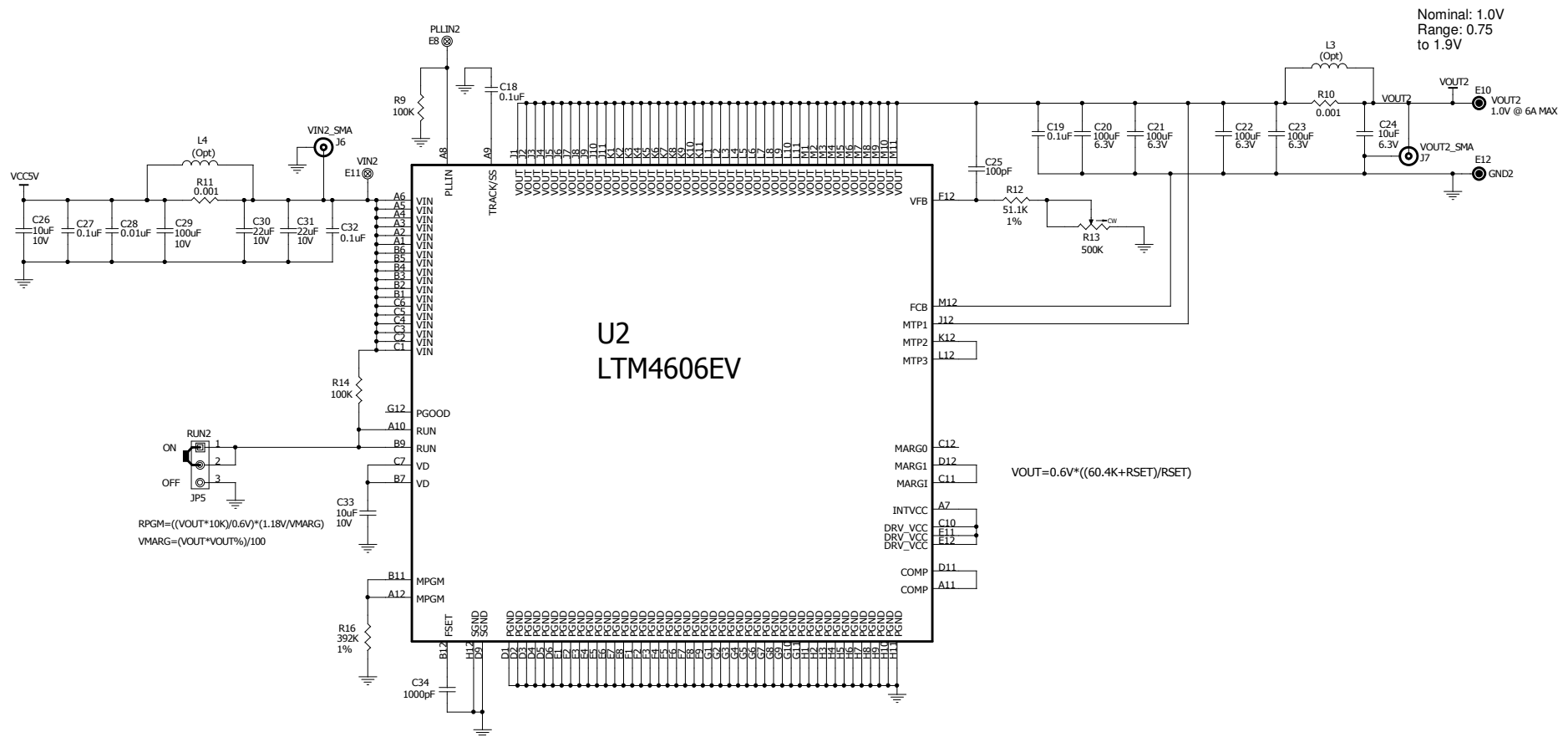
$$VOUT = 0.6V * ((60.4K + RSET) / RSET)$$

$$RPGM = ((VOUT * 10K) / 0.6V) * (1.18V / VMARG)$$

$$VMARG = (VOUT * VOUT\%) / 100$$

This circuit is proprietary to Linear Technology and supplied for use with Linear Technology parts.  
**Customer Notice:** Linear Technology has made a best effort to design a circuit that meets customer-supplied specifications; however, it remains the customers responsibility to verify proper and reliable operation in the actual application. Component substitution and printed circuit board layout may significantly affect circuit performance or reliability. Contact Linear Applications Engineering for assistance.

CONTRACT NO.		 1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408) 432-1600 Fax: (408) 434-0507		TITLE: LTM4606EV	
APPROVALS	DATE			Dual 6A High Density Step-Down Power $\mu$ Module	
DRAWN: <i>Randy Beaudette</i>		SIZE	CAGE CODE	DWG NO	REV
CHECKED		SD		Demo Circuit 1374B	1
APPROVED					
ENGINEER: Alan Chern					
DESIGNER					
Friday, April 24, 2009		FILENAME:		SHEET 1 OF 2	
		C:\ORCADWIN\CAPTURE\1374B\1374B_00_REV1.DSN			



CONTRACT NO.				1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408) 432-1600 Fax: (408) 434-0507	
APPROVALS	DATE			TITLE: LTM4606EV	
DRAWN <i>Ruby Bautista</i>		Dual 6A High Density Step-Down Power $\mu$ Module			
CHECKED		SIZE	CAGE CODE	DWG NO	REV
APPROVED		SD		Demo Circuit 1374B	1
DESIGNER					
		FILENAME:	C:\ORCAD\WIN\CAPTURE\1374B\1374B_00_REV1.DSN		SHEET 2 OF 2
Friday, April 24, 2009					