PCN Number:	20140214000	PCN Date:	02/20/2014
Title:	ADS54T02/ADS54T01 datasheet		
Customer Contact:	PCN Manager	Phone: +1(214) 480-6037	Dept: Quality Services

Change Type:

☐	Assembly Site	☐	Assembly Process	☐	Assembly Materials
☐	Design	☒	Electrical Specification	☐	Mechanical Specification
☐	Test Site	☐	Packing/Shipping/Labeling	☐	Test Process
☐	Wafer Bump Site	☐	Wafer Bump Material	☐	Wafer Bump Process
☐	Wafer Fab Site	☐	Wafer Fab Materials	☐	Wafer Fab Process
		☐	Part number change		

PCN Details

Description of Change:

The product datasheet(s) is being update to change in address 0x02 bit D13 from 0 to 1 in table (page 34) in ADS54T02 data sheet and add description below: D13 read back 1. Change also applies to ads54t01 datasheet. Also have two more changes to reset function and low resolution output.

The following change history provides further details. These changes may be reviewed at the datasheet links provided

ADS54T02

www.ti.com SLAS933B – JANUARY 2013 – REVISED JANUARY 2014

REVISION HISTORY

Changes from Revision A (August 2013) to Revision B	Page
• Added text to TRDYP/N description	4
• Added text to HRESP/N description	4
• Added text and figure to TEST PATTERN OUTPUT section	20
• Deleted text from last paragraph in INTERLEAVING CORRECTION section	23
• Changed 9 or 11bit to 7 or 11bit in FEEDBACK MODE: BURST MODE section	25
• Changed Figure 40	27
• Changed second paragraph in MULTI DEVICE SYNCHRONIZATION section	28
• Deleted Register Initialization section and added Device Initialization section	29
• Changed Register Address 2 Bit D13 from 0 to 1 in SERIAL REGISTER MAP	32
• Changed Register Address E Bits D1 and D0 to 0 in SERIAL REGISTER MAP	32
• Changed Register Address 38 Bits D3 to D0 from 0 to 1 in SERIAL REGISTER MAP	32
• Changed Register Address 1 Bit D14 from 1 to 0	34
• Changed Register Address 2 Bit D13 from 0 to 1 and add D13 Read back 1	35
• Changed Register Address E Bit D1 and D0 to 0	36
• Changed Register Address 38 Bits D3 to D0 from 0 to 1 and add D3 to D0 Read back 1	38
• Changed Register Address 66 D15-D10 to D15-D0 and DA11-D0 to DA11-DA0	40
• Changed Register Address 67 D15-D10 to D15-D0	40

REVISION HISTORY

Changes from Original (December 2012) to Revision A	Page
• Deleted P7, N7 from TRDYP/N pin numbers	4
• Changed package from QFN to nFBGA in THERMAL INFORMATION	5
• Added text and figure to TEST PATTERN OUTPUT section	20
• Deleted text from last paragraph in INTERLEAVING CORRECTION section	23
• Changed second paragraph in MULTI DEVICE SYNCHRONIZATION section	28
• Deleted Register Initialization section and added Device Initialization section	29
• Changed Register Address 2 Bit D13 from 0 to 1 in SERIAL REGISTER MAP	32
• Changed Register Address E Bits D1 and D0 to 0 in SERIAL REGISTER MAP	32
• Changed Register Address 38 Bits D3 to D0 from 0 to 1 in SERIAL REGISTER MAP	32
• Changed Register Address 2 Bit D13 from 0 to 1 and add D13 Read back 1	33
• Changed Register Address E Bit D1 and D0 to 0	34
• Changed Register Address 38 Bits D3 to D0 from 0 to 1 and add D3 to D0 Read back 1	37

The datasheet number will be changing.

Device Family	Change From:	Change To:
ADS54T02	SLAS933A	**SLAS933B**
ADS54T01	SLAS918A	**SLAS918B**

The updated datasheet(s) can be accessed by the following link(s):
http://www.ti.com/product/ads54t02
http://www.ti.com/product/ads54t01

Reason for Change:

To more accurately reflect device characteristics.

Anticipated impact on Fit, Form, Function, Quality or Reliability (positive / negative):

Electrical specification performance changes as indicated above.

Changes to product identification resulting from this PCN:

None

Product Affected:

ADS54T02IZAY	ADS54T02IZAYR	ADS54T01IZAY	ADS54T01IZAYR

For questions regarding this notice, e-mails can be sent to the regional contacts shown below or your local Field Sales Representative.

Location	E-Mail
USA	PCNAmericasContact@list.ti.com
Europe	PCNEuropeContact@list.ti.com
Asia Pacific	PCNAsiaContact@list.ti.com
Japan	PCNJapanContact@list.ti.com