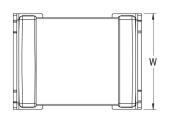
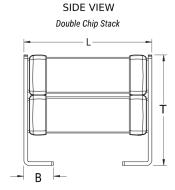


TOP VIEW Single or Double Chip Stack





General Information		
Series	KPS SMD Comm X7R	
Style	Stacked Chip	
Description	SMD, MLCC, Stacked, Double Chip, Temperature Stable	
Features	Temperature Stable	
RoHS	Yes	
Termination	Tin	
AEC-Q200	No	
Component Weight	290 mg	
Chip Size	1210-2	
Shelf Life	78 Weeks	
MSL	1	

Click here for the 3D model.

Dimensions	
L	3.5mm +/-0.3mm
W	2.6mm +/-0.3mm
т	6.15mm +/-0.15mm
В	0.8mm +/-0.15mm

Packaging Specifications	kaging Specifications	
Packaging	T&R, 330mm, Plastic Tape	
Packaging Quantity	1000	
5 5	, , ,	

Specifications	
Capacitance	47 uF
Measurement Condition	120 Hz 0.5Vrms
Capacitance Tolerance	20%
Voltage DC	10 VDC
Dielectric Withstanding Voltage	25 VDC
Temperature Range	-55/+125°C
Temperature Coefficient	X7R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 120Hz 0.5Vrms
Dissipation Factor	10% 120 Hz 0.5Vrms
Aging Rate	3% Loss/Decade Hour: Referee Time is 1000 Hours
Insulation Resistance	10.6 MOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.