

1.0A SURFACE MOUNT STANDARD RECOVERY RECTIFIER
Product Summary (@ T_A = +25°C)

V _{RRM} (V)	I _o (A)	V _F Max (V)	I _R Max (μA)
1000	1	1.1	5

Description

The FS1MED is a rectifier packaged in the low-profile DO-219AA package and is suited for AC to DC rectification. It is ideal as a snubber in adapters for quick chargers and for discretely building a bridge for LED lighting circuits. The controlled reverse recovery time helps reduce power loss.

Applications

- Adapters for Quick Chargers
- Bridge Circuits in LED Lighting Systems

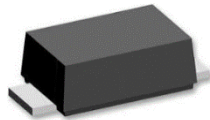
Features and Benefits

- Low Profile, Small Form Factor Package
- Low Leakage Current
- Glass Passivated Die Construction
- Low Forward Voltage, Low Power Loss
- **Lead-Free Finish; RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free. “Green” Device (Note 3)**
- **For automotive applications requiring specific change control (i.e. parts qualified to AEC-Q100/101/200, PPAP capable, and manufactured in IATF 16949 certified facilities), please [contact us](mailto:contact@diodes.com) or your local Diodes representative. <https://www.diodes.com/quality/product-definitions/>**

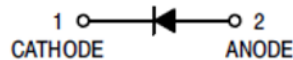
Mechanical Data

- Case: DO-219AA
- Case Material: Molded Plastic, “Green” Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Matte Tin Finish Annealed over Copper Lead-Frame. Solderable per MIL-STD-202, Method 208 (E3)
- Polarity: Cathode Band
- Weight: 0.016 grams (Approximate)

DO-219AA



Top View



Schematic View

Ordering Information (Note 4)

Part Number	Qualification	Case	Packaging
FS1MED-7	Commercial	DO-219AA	3000/Tape & Reel

- Notes:
1. EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant. All applicable RoHS exemptions applied.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information

DO-219AA



1MD = Product Type Marking Code
 YWW = Date Code Marking
 Y = Last Digit of Year (ex: 0 = 2020)
 WW = Week Code (01 to 53)

Date Code Key

Year	2020	2021	2022	2023	2024	2025	2026	2027	2028	2029	2030	2031
Code	0	1	2	3	4	5	6	7	8	9	0	1

Maximum Ratings (@ T_A = +25°C, unless otherwise specified.)

Single phase, half wave, 60Hz, resistive or inductive load.
For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}	1000	V
Working Peak Reverse Voltage	V _{RWM}		
DC Blocking Voltage	V _R		
Average Rectified Output Current	I _O	1	A
Non-Repetitive Peak Forward Surge Current	I _{FSM}	30	A
8.3ms Single Half Sine-Wave Superimposed on Rated Load			
I ² t Rating for Fusing (t = 8.3ms)	I ² t	7.47	A ² S

Thermal Characteristics

Characteristic	Symbol	Value	Unit
Typical Thermal Resistance Junction to Case	R _{θJC}	30	°C/W
Typical Thermal Resistance Junction to Ambient (Note 5)	R _{θJA}	50	°C/W
Typical Thermal Resistance Junction to Lead (Note 5)	R _{θJL}	18	°C/W
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C

Electrical Characteristics (@ T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 6)	V _{(BR)R}	1000	—	—	V	I _R = 10μA
Forward Voltage	V _F	—	— 0.86	1.1 —	V	I _F = 1A, T _J = +25°C I _F = 1A, T _J = +125°C
Reverse Leakage Current (Note 6)	I _R	—	—	5 500	μA	V _R = 1000V, T _J = +25°C V _R = 1000V, T _J = +125°C
Reverse Recovery Time	t _{RR}	500	—	1000	ns	I _F = 0.5A, I _R = 1.0A, I _{RR} = 0.25A
Typical Total Capacitance	C _T	—	7.6	—	pF	V _R = 4V, f = 1MHz

Notes: 5. Thermal resistance test performed in accordance with JESD-51. Unit mounted on glass-epoxy substrate with 5mm × 7mm copper pad.
6. Short duration pulse test used to minimize self-heating effect.

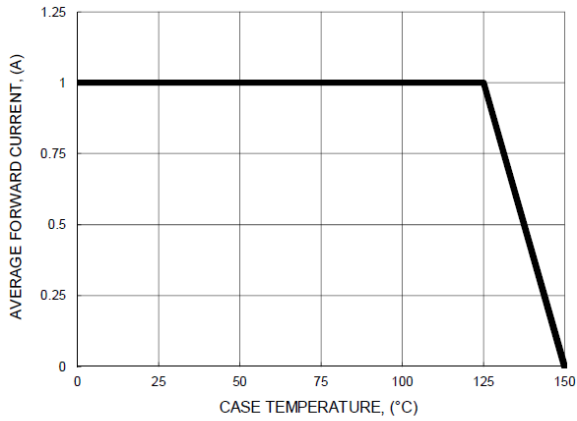


FIG. 1 FORWARD CURRENT DERATING CURVE

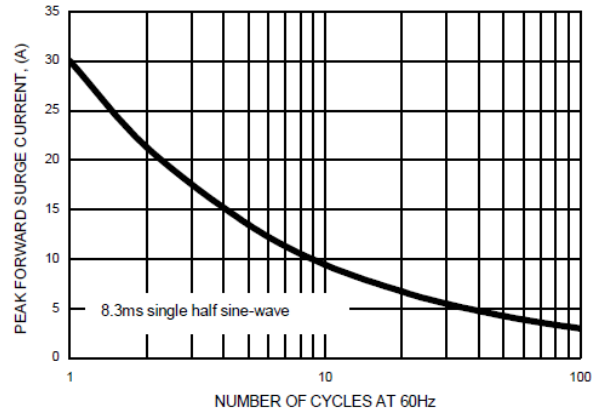


FIG. 2 MAXIMUM NON-REPETITIVE SURGE CURRENT

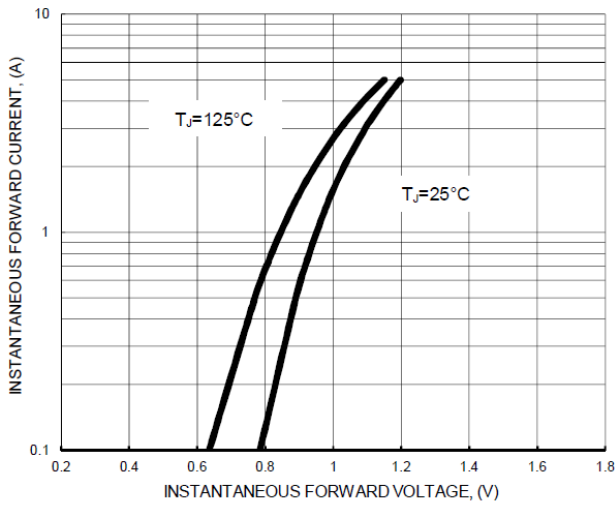


FIG. 3 TYPICAL FORWARD CHARACTERISTICS

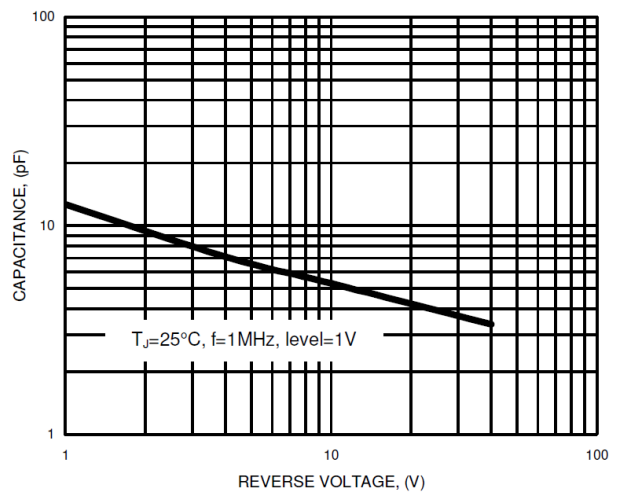


FIG. 4 TYPICAL TOTAL CAPACITANCE

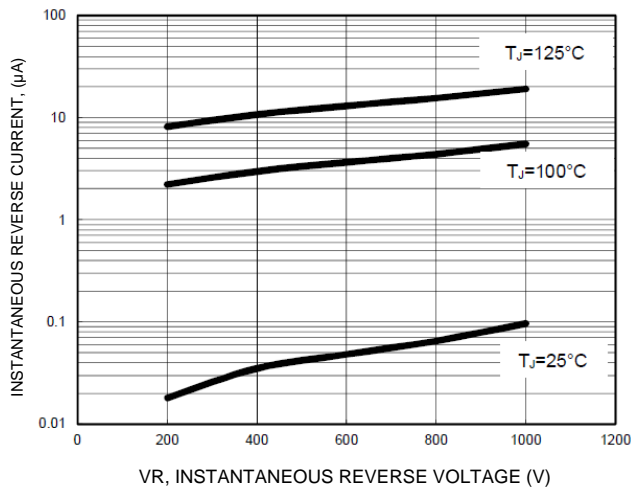
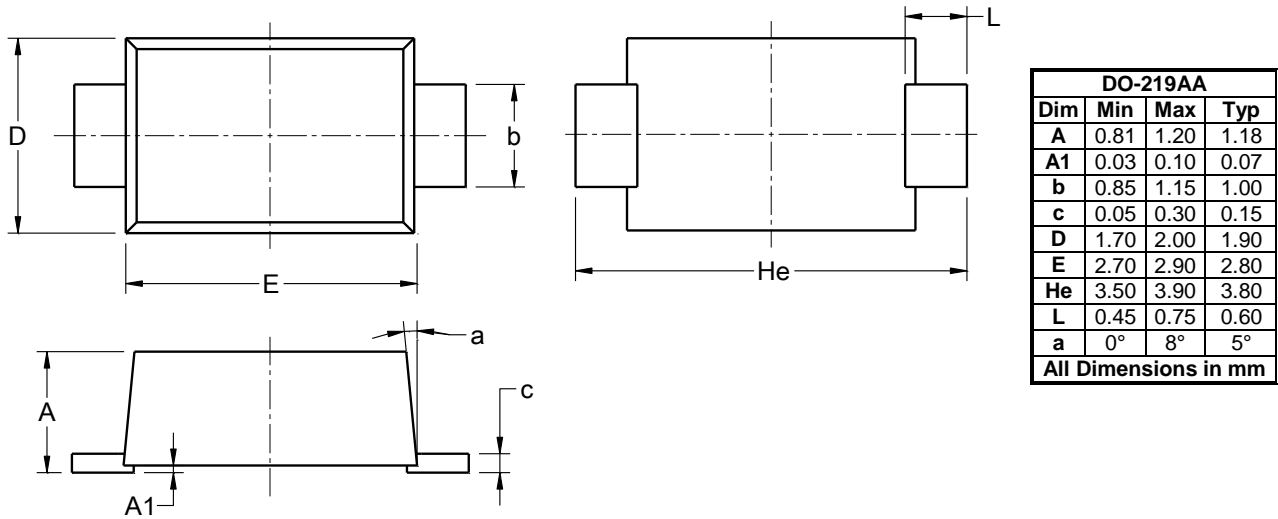


FIG. 5 TYPICAL REVERSE CHARACTERISTICS

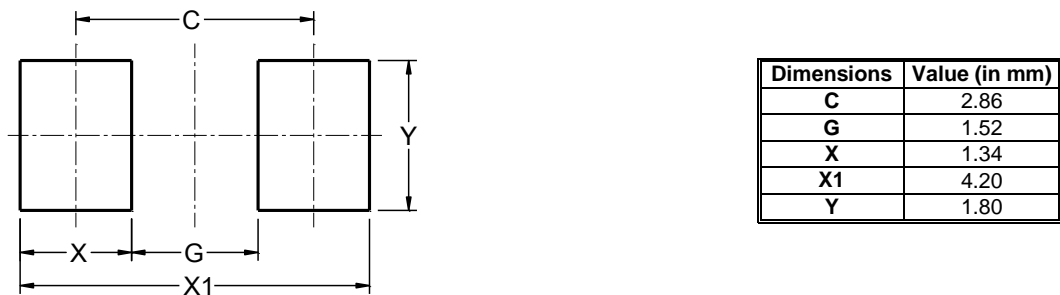
Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.



Suggested Pad Layout

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