



## AHVA500V2X10MA





Figure 1. The Physical Photos of AHVA500V2X10MA

#### **MAIN FEATURES**

Built-in High Voltage Converter

**○** Compact Size: 181.5(L)×149.0(W)×38.3(H) mm

⇒ High Current Capability: Up to 10mA

Table 1. Descriptions of Terminal Block Pin Functions

⇒ High Slew Rate: 100V/us

⇒ Wide Output Voltage Range: V<sub>OUT</sub>=0~500V@V<sub>IN</sub>=24V

Offset Voltage Range: 10V **⇒** Bandwidth: Up to 10kHz

Weight: 2.2lb (1.0kg)

#### APPLICATIONS

High voltage amplifications for driving piezos and other high voltage loads.

#### DESCRIPTION

The AHVA500V2X10MA is an electronic module for amplifying an analog input voltage into a high voltage output. Figure 1 shows its physical photo. It comes with a high voltage DC-DC converter, which converts the 24V input voltage into a 0 to 500V output voltage. The analog output voltage can swing almost from 0 to 500V when it is powered by a 24V power supply. There is three LEDs indicating if the amplifier works properly.

#### **CAUTION**

First, set up the AC power supply and fix it stably and firmly. Then make sure that the two switches of the high voltage amplifier are OFF. Connect the 24V DC power supply to the VPS and PGND of the high voltage amplifier. After the connection is complete, turn on the low voltage switch and set the input AC voltage or DC voltage. Then use the output monitor to check whether the input set voltage is correct. Finally turn on the high voltage switch.

Pin #	Name	Туре	Description	
1	VPS	Power Input	Power supply 24V.	
2	PGND	Power Ground	Power ground pin.	
3	SBDN	Digital Input	This is a duplex pin. It sets the amplifier into Off, Standby or On mode.	
4	AGND	Signal Ground	Signal ground pin. Connect ADC and DAC grounds to here.	
5	10VR	Analog Output	10V voltage reference.	
6	IHVMON	Analog Input	-	
7	HVMON	Analog Output	Output voltage indication. When going from 0 to 10V, it indicates the output voltage is from 0 to 500V.	
8	OFFSO	Analog Input	Output voltage setting. When going from 0 to 10V, it indicates the output voltage is from 0 to 500V. The pin is controlled by a potentiometer.	
9	GND	Signal Ground	Signal ground pin. Connect ADC and DAC grounds to here.	

# AHVA500V2X10MA

Pin #	Name	Type	Description	
BNC 1	INPUT	Analog Input	Output voltage setting. When going from 0 to 10V, it indicates the output voltage is from 0 to 500V.	
BNC 2	INPUT+DC	Analog Input	INPUT+DC input control signal indication.	
	VOUT	Analog Output	Output voltage for driving the load.	
BNC 3	OGND	Output Ground	Connect this pin to the load return terminal.	

#### **SPECIFICATIONS**

Table 2. Characteristics (Test ambient temperature  $T_A = 25$ °C)

Parameter	Symbol	<b>Test Conditions</b>	Min.	Тур.	Max.	Units
Power Supply Input						
Input Range	$V_{ m VPS}$		23	24	25	V
Input Current	$I_{IN}$		0		4	A
Voltage Output						
Output Voltage	$V_{ m OUT}$		0		500	V
Output Current	$I_{OUT}$		0		10	mA
SBDN Pin (Pin 3)						
	$ m V_{SBDN ext{-}ON}$		2.64		$V_{VPS}$	V
	$V_{SBDN ext{-STANDBY}}$		2.1		2.5	V
	$V_{SBDN ext{-}OFF}$		0		0.4	V
	V <sub>SBDN-SB-HI</sub> Going up from Standby to On threshold voltage		2.508		2.64	V
SBDN Voltage	V <sub>SBDN-SB-LOW</sub> Going down from On to Standby threshold voltage		2.5		2.6	V
	V <sub>SBDN-OFF-HI</sub> Going up from Off to Standby threshold voltage				2.1	V
	V <sub>SBDN-OFF-LOW</sub> Going down from Standby to Off threshold voltage		0.4			V
SBDN Current	$I_{SBDN}$			10	20	μΑ
10VR Pin (Pin 5)	•					
Voltage Reference	$V_{REF}$			10		V
Maximum Input Power				15		W
Maximum Slew Rate				100		V/µs

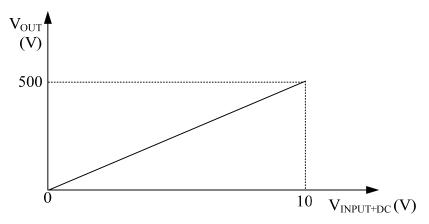
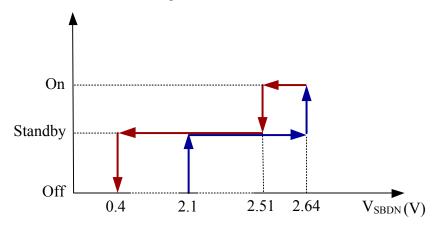


Figure 2. V<sub>OUT</sub> vs. V<sub>VIN</sub>



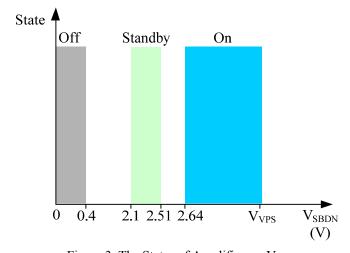


Figure 3. The States of Amplifier vs.  $V_{\text{SBDN}}$ 

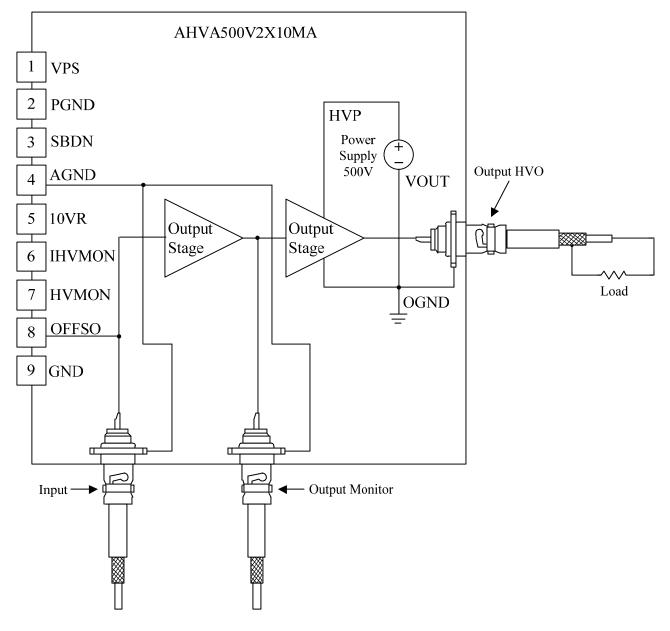


Figure 4. Schematic for Driving the Load

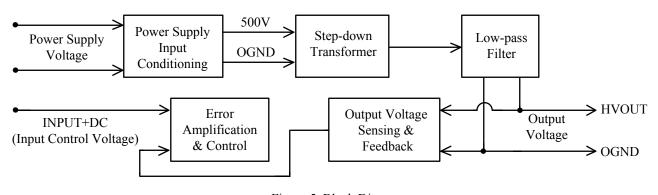
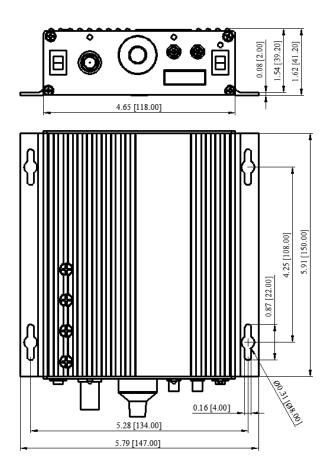
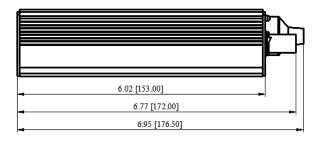


Figure 5. Block Diagram



#### **DIMENSIONS**





End View	Side View
Top View	Unit: inch [mm]

Figure 6. Dimensions of AHVA500V2X5MA

# High Voltage Amplifier/Piezo Driver



## AHVA500V2X10MA

#### **NOTICE**

- 1. ATI warrants performance of its products for one year to the specifications applicable at the time of sale, except for those being damaged by excessive abuse. Products found not meeting the specifications within one year from the date of sale can be exchanged free of charge.
- ATI reserves the right to make changes to its products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete.
- 3. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgment, including those pertaining to warranty, patent infringement, and limitation of liability. Testing and other quality control techniques are utilized to the extent ATI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.
- 4. Customers are responsible for their applications using ATI components. In order to minimize risks associated with the customers' applications, adequate design and operating safeguards must be provided by the customers to minimize inherent or procedural hazards. ATI assumes no liability for applications assistance or customer product design.
- 5. ATI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of ATI covering or relating to any combination, machine, or process in which such products or services might be or are used. ATI's publication of information regarding any third party's products or services does not constitute ATI's approval, warranty or endorsement thereof.
- 6. IP (Intellectual Property) Ownership: ATI retains the ownership of full rights for special technologies and/or techniques embedded in its products, the designs for mechanics, optics, plus all modifications, improvements, and inventions made by ATI for its products and/or projects.