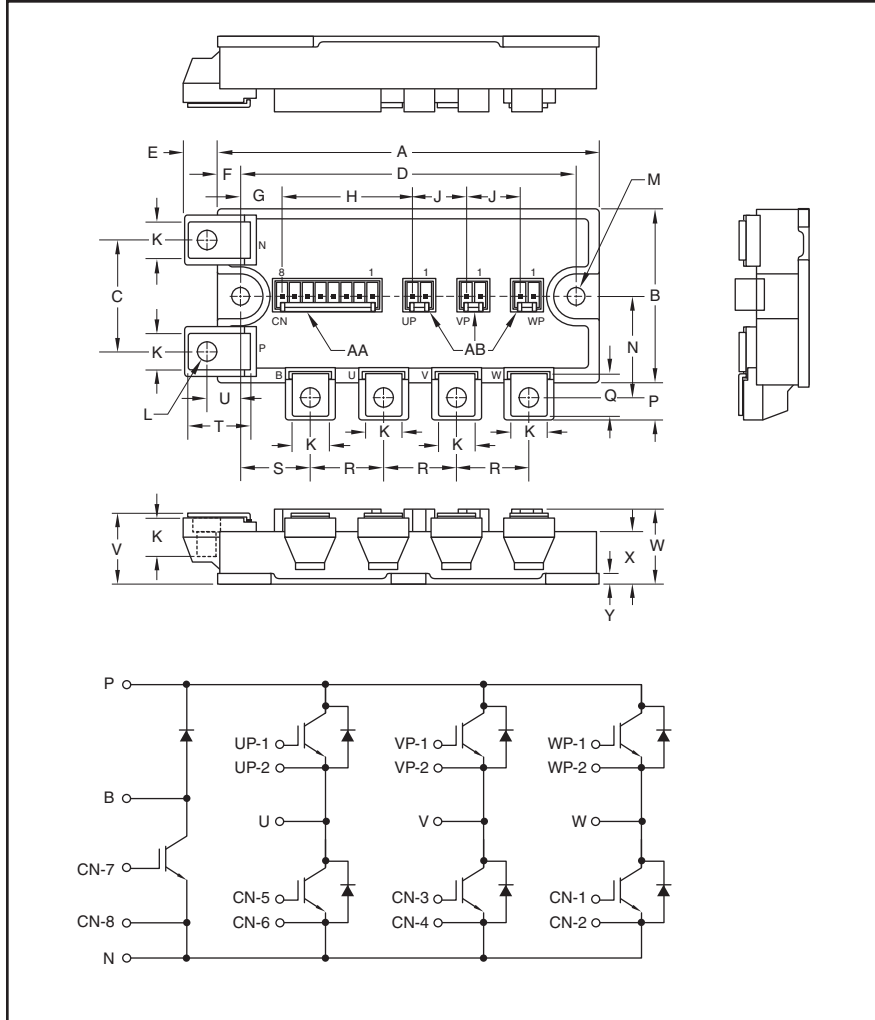


Six IGBTMOD™ + Brake NF-Series Module 100 Amperes/600 Volts



Description:

Powerex IGBTMOD™ Modules are designed for use in switching applications. Each module consists of six IGBT Transistors in a three phase bridge configuration and a seventh IGBT with free-wheel diode for dynamic braking. All components and interconnects are isolated from the heat sinking baseplate, offering simplified system assembly and thermal management.

Features:

- Low Drive Power
- Low $V_{CE(sat)}$
- Discrete Super-Fast Recovery Free-Wheel Diode
- Isolated Baseplate for Easy Heat Sinking

Applications:

- AC Motor Control
- Motion/Servo Control
- Photovoltaic/Fuel Cell

Ordering Information:

Example: Select the complete module number you desire from the table below -i.e. CM100RL-12NF is a 1200V (V_{CES}), 100 Ampere Six-IGBTMOD™ + Brake Power Module.

| Type | Current Rating Amperes | V_{CES} Volts (x 50) |
|------|---------------------------|---------------------------|
| CM | 100 | 12 |

Outline Drawing and Circuit Diagram

| Dimensions | Inches | Millimeters |
|------------|-----------|-------------|
| A | 4.72 | 120.0 |
| B | 2.17 | 55.0 |
| C | 1.39 | 35.0 |
| D | 4.17±0.02 | 106.0±0.5 |
| E | 0.43 | 11.0 |
| F | 0.28 | 7.0 |
| G | 0.54 | 13.62 |
| H | 1.61 | 40.78 |
| J | 0.67 | 17.0 |
| K | 0.47 | 12.0 |
| L | M5 | M5 |
| M | 0.22 Dia. | Dia. 5.5 |

| Dimensions | Inches | Millimeters |
|------------|-----------------|---------------|
| N | 1.23 | 32.0 |
| P | 0.47 | 11.75 |
| Q | 0.53 | 13.5 |
| R | 0.91 | 23.0 |
| S | 0.87 | 22.0 |
| T | 0.76 | 19.75 |
| U | 0.42 | 10.75 |
| V | 0.87+0.04/-0.02 | 22.0+1.0/-0.5 |
| W | 0.91 | 23.2 |
| X | 0.63 | 16.0 |
| Y | 0.12 | 3.0 |

Housing Types (J.S.T. Mfg. Co. Ltd.)

- AA – B8P-VH-FB-B
- AB – B2P-VH-FB-B



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CM100RL-12NF

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100 Amperes/600 Volts

Absolute Maximum Ratings, $T_j = 25^\circ\text{C}$ unless otherwise specified

| Characteristics | Symbol | CM100RL-12NF | Units |
|---|------------------|--------------|------------------|
| Power Device Junction Temperature | T_j | -40 to 150 | $^\circ\text{C}$ |
| Storage Temperature | T_{stg} | -40 to 125 | $^\circ\text{C}$ |
| Mounting Torque, M5 Mounting Screws | — | 31 | in-lb |
| Mounting Torque, M5 Main Terminal Screws | — | 31 | in-lb |
| Module Weight (Typical) | — | 350 | Grams |
| Isolation Voltage, AC 1 minute, 60Hz Sinusoidal | V_{ISO} | 2500 | Volts |

Inverter Sector

| | | | |
|--|------------------|----------|---------|
| Collector-Emitter Voltage (G-E Short) | V_{CES} | 600 | Volts |
| Gate-Emitter Voltage (C-E Short) | V_{GES} | ± 20 | Volts |
| Collector Current ($T_C = 99^\circ\text{C}$)* | I_C | 100 | Amperes |
| Peak Collector Current ($T_j \leq 150^\circ\text{C}$) | I_{CM} | 200** | Amperes |
| Emitter Current*** | I_E | 100 | Amperes |
| Peak Emitter Current*** | I_{EM} | 200** | Amperes |
| Maximum Collector Dissipation ($T_C = 25^\circ\text{C}$, $T_j < 150^\circ\text{C}$) | P_C | 540 | Watts |

Brake Sector

| | | | |
|--|------------------|----------|---------|
| Collector-Emitter Voltage (G-E Short) | V_{CES} | 600 | Volts |
| Gate-Emitter Voltage (C-E Short) | V_{GES} | ± 20 | Volts |
| Collector Current ($T_C = 107^\circ\text{C}$)* | I_C | 50 | Amperes |
| Peak Collector Current ($T_j \leq 150^\circ\text{C}$) | I_{CM} | 100** | Amperes |
| Maximum Collector Dissipation ($T_C = 25^\circ\text{C}$, $T_j < 150^\circ\text{C}$) | P_C | 320 | Watts |
| Repetitive Peak Reverse Voltage (Clamp Diode Part) | V_{RRM} | 600 | Volts |
| Forward Current (Clamp Diode Part) | I_{FM} | 50 | Amperes |

* T_C , T_f measured point is just under the chips.

**Pulse width and repetition rate should be such that device junction temperature (T_j) does not exceed $T_{j(\text{max})}$ rating.

***Represents characteristics of the anti-parallel, emitter-to-collector free-wheel diode (FWDi).

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Electrical and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

Inverter Sector

| Characteristics | Symbol | Test Conditions | Min. | Typ. | Max. | Units |
|--------------------------------------|---------------------|---|--------------------------------|------|------|---------|
| Collector Cutoff Current | I_{CES} | $V_{CE} = V_{CES}, V_{GE} = 0V$ | — | — | 1.0 | mA |
| Gate-Emitter Threshold Voltage | $V_{GE(th)}$ | $I_C = 10mA, V_{CE} = 10V$ | 6 | 7 | 8 | Volts |
| Gate Leakage Current | I_{GES} | $V_{GE} = V_{GES}, V_{CE} = 0V$ | — | — | 0.5 | μA |
| Collector-Emitter Saturation Voltage | $V_{CE(sat)}$ | $I_C = 100A, V_{GE} = 15V, T_j = 25^\circ\text{C}$ | — | 1.7 | 2.2 | Volts |
| | | $I_C = 100A, V_{GE} = 15V, T_j = 125^\circ\text{C}$ | — | 1.7 | — | Volts |
| Input Capacitance | C_{ies} | | — | — | 15.0 | nf |
| Output Capacitance | C_{oes} | $V_{CE} = 10V, V_{GE} = 0V$ | — | — | 1.9 | nf |
| Reverse Transfer Capacitance | C_{res} | | — | — | 0.6 | nf |
| Total Gate Charge | Q_G | $V_{CC} = 300V, I_C = 100A, V_{GE} = 15V$ | — | 400 | — | nC |
| Inductive | Turn-on Delay Time | $t_{d(on)}$ | — | — | 120 | ns |
| Load | Turn-on Rise Time | t_r | $V_{CC} = 300V, I_C = 100A,$ | | 100 | ns |
| Switch | Turn-off Delay Time | $t_{d(off)}$ | $V_{GE1} = V_{GE2} = 15V,$ | | 300 | ns |
| Time | Turn-off Fall Time | t_f | $R_G = 6.3\Omega, I_E = 100A,$ | | 300 | ns |
| Reverse Recovery Time* | t_{rr} | Inductive Load Switching Operation | — | — | 120 | ns |
| Reverse Recovery Charge* | Q_{rr} | | — | 2.1 | — | μC |
| Emitter-Collector Voltage* | V_{EC} | $I_E = 100A, V_{GE} = 0V$ | — | — | 2.8 | Volts |

Thermal and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

| Characteristics | Symbol | Test Conditions | Min. | Typ. | Max. | Units |
|--|----------------|--|------|------|-------|---------------------------|
| Thermal Resistance, Junction to Case** | $R_{th(j-c)Q}$ | Per IGBT 1/6 Module | — | — | 0.23 | $^\circ\text{C}/\text{W}$ |
| Thermal Resistance, Junction to Case** | $R_{th(j-c)D}$ | Per FWDi 1/6 Module | — | — | 0.41 | $^\circ\text{C}/\text{W}$ |
| Contact Thermal Resistance | $R_{th(c-f)}$ | Per 1/6 Module, Thermal Grease Applied | — | — | 0.085 | $^\circ\text{C}/\text{W}$ |
| External Gate Resistance | R_G | | 6.3 | — | 63 | Ω |

*Represents characteristics of the anti-parallel, emitter-to-collector free-wheel diode (FWDi).

** T_C, T_f measured point is just under the chips.



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Electrical and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

Brake Sector

| Characteristics | Symbol | Test Conditions | Min. | Typ. | Max. | Units |
|--------------------------------------|---------------|--|------|------|------|---------------|
| Collector Cutoff Current | I_{CES} | $V_{CE} = V_{CES}, V_{GE} = 0V$ | — | — | 1.0 | mA |
| Gate-Emitter Threshold Voltage | $V_{GE(th)}$ | $I_C = 5.0\text{mA}$ | 6 | 7 | 8 | Volts |
| Gate Leakage Current | I_{GES} | $V_{GE} = V_{GES}, V_{CE} = 0V$ | — | — | 0.5 | μA |
| Collector-Emitter Saturation Voltage | $V_{CE(sat)}$ | $I_C = 50\text{A}, V_{GE} = 15\text{V}, T_j = 25^\circ\text{C}$ | — | 1.7 | 2.2 | Volts |
| | | $I_C = 50\text{A}, V_{GE} = 15\text{V}, T_j = 125^\circ\text{C}$ | — | 1.7 | — | Volts |
| Input Capacitance | C_{ies} | | — | — | 7.5 | nf |
| Output Capacitance | C_{oes} | $V_{CE} = 10\text{V}, V_{GE} = 0\text{V}$ | — | — | 1.0 | nf |
| Reverse Transfer Capacitance | C_{res} | | — | — | 0.3 | nf |
| Total Gate Charge | Q_G | $V_{CC} = 300\text{V}, I_C = 50\text{A}, V_{GE} = 15\text{V}$ | — | 200 | — | nC |
| Forward Voltage Drop | V_{FM} | $I_F = 50\text{A}$ | — | — | 2.6 | Volts |

Thermal and Mechanical Characteristics, $T_j = 25^\circ\text{C}$ unless otherwise specified

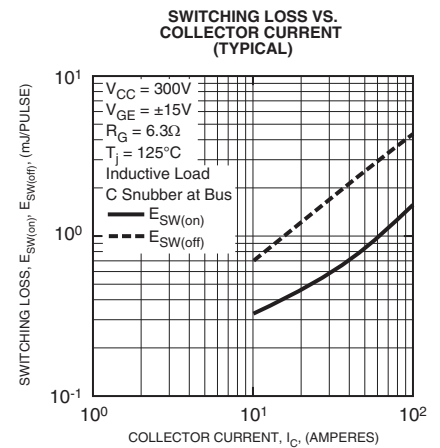
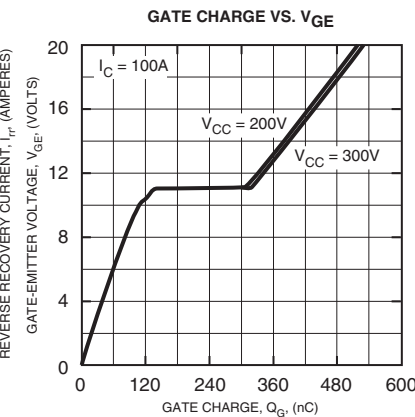
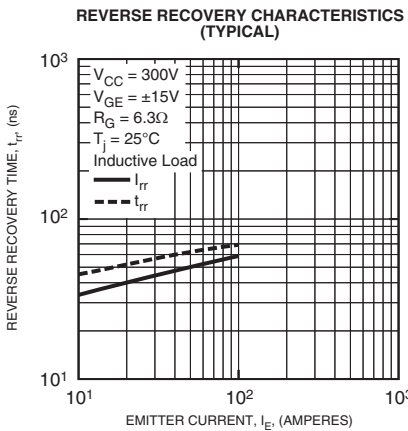
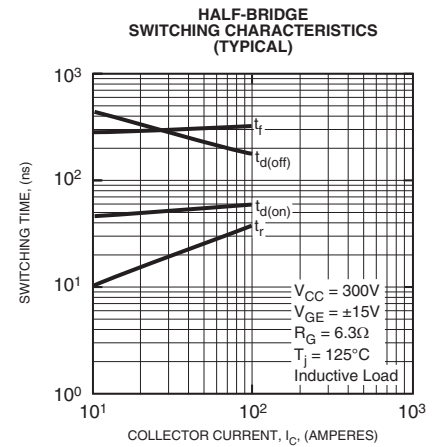
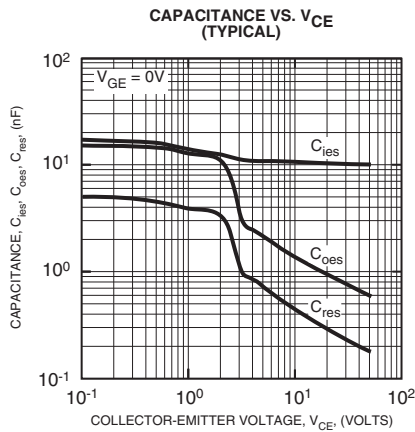
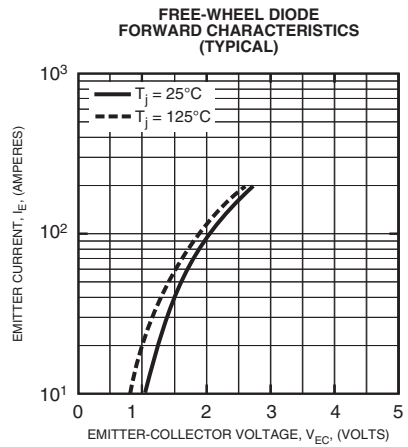
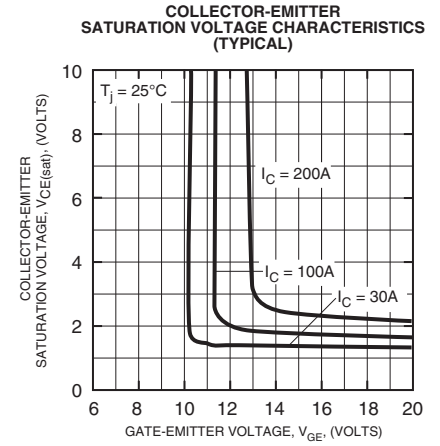
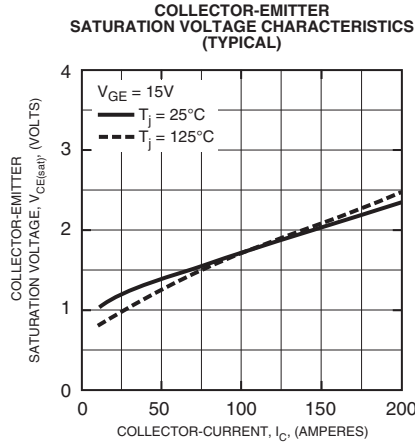
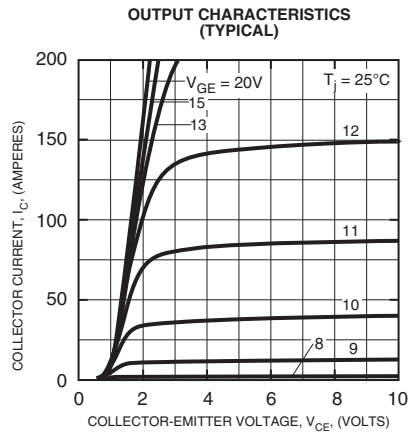
| Characteristics | Symbol | Test Conditions | Min. | Typ. | Max. | Units |
|---------------------------------------|----------------|---------------------|------|------|------|--------------------|
| Thermal Resistance, Junction to Case* | $R_{th(j-c)Q}$ | Per IGBT 1/6 Module | — | — | 0.39 | $^\circ\text{C/W}$ |
| Thermal Resistance, Junction to Case* | $R_{th(j-c)D}$ | Per FWDi 1/6 Module | — | — | 0.70 | $^\circ\text{C/W}$ |

* T_C, T_f measured point is just under the chips.



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