RClamp1255P
Low Capacitance RClamp®
Surge Protection for uUSB Interfaces

PROTECTION PRODUCTS - RailClamp®

Description

RailClamp® TVS arrays are low capacitance ESD protection devices designed to protect high speed data interfaces. The RClamp®1255P provides dedicated surge and ESD protection for uUSB ports. It is designed to replace multiple discrete components in portable applications. This device features low capacitance TVS diodes for protection of the USB data (DP, DM) and USB ID pins operating up to +/- 4 volts. These diodes provide ESD protection to ±10kV contact discharge per IEC 61000-4-2. Loading capacitance on these lines is <0.50pF. An integrated 12 volt TVS diode is used for protection of the USB voltage bus. The VBus TVS is designed with a high surge current capability of 100A (tp=8/20us) and low clamping voltage.

The RClamp1255P is in a 6-pin SLP2018P6 package. It measures 2.0 x 1.8mm with a nominal height of 0.57mm. This highly integrated device requires less board space than existing solutions.

The combination of small size, low capacitance, and high level of surge and ESD protection makes this device a flexible solution for protection of USB interfaces in mobile phones, laptops, and other portable electonics.

Features

- ESD and surge protection for USB Voltage Bus to
  IEC 61000-4-2 (ESD) ±30kV (air), ±30kV (contact)
  IEC 61000-4-5 (Lightning) 100A (8/20µs)
  IEC 61000-4-4 (EFT) 40A (5/50ns)
- ESD protection for USB data lines to
  IEC 61000-4-2 (ESD) ±15kV (air), ±10kV (contact)
- Protects USB DP, DM, and ID Pins operating up to +/- 4V
- Protects USB VBus operating up to 12V
- Low capacitance **(<0.50pF)** on DP, DM, and ID Pins
- Low clamping voltage
- Low dynamic resistance on DP, DM, and ID Pins
- Solid-state silicon-avalanche technology

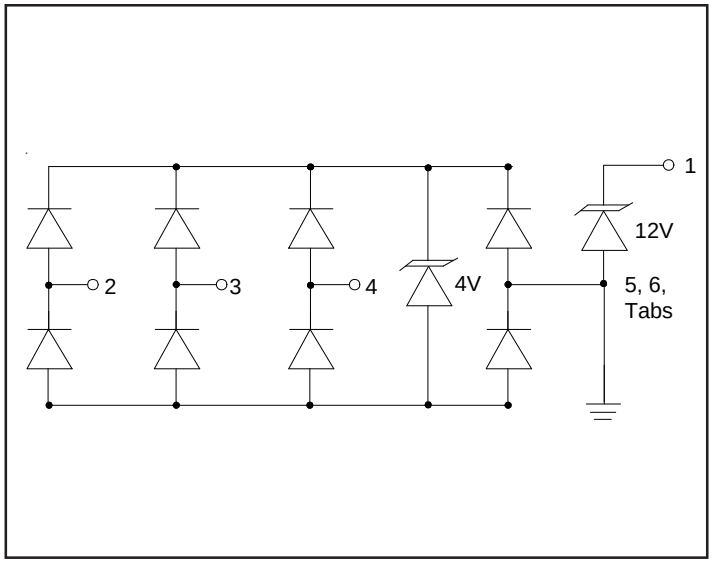
Mechanical Characteristics

- SLP2018P6 6L package
- Pb-Free, Halogen Free, RoHS/WEEE Compliant
- Nominal Dimensions: 2.0 x 1.8 x 0.57 mm
- Lead Finish: NiPdAu
- Molding compound flammability rating: UL 94V-0
- Marking : Marking Code + Date Code
- Packaging : Tape and Reel

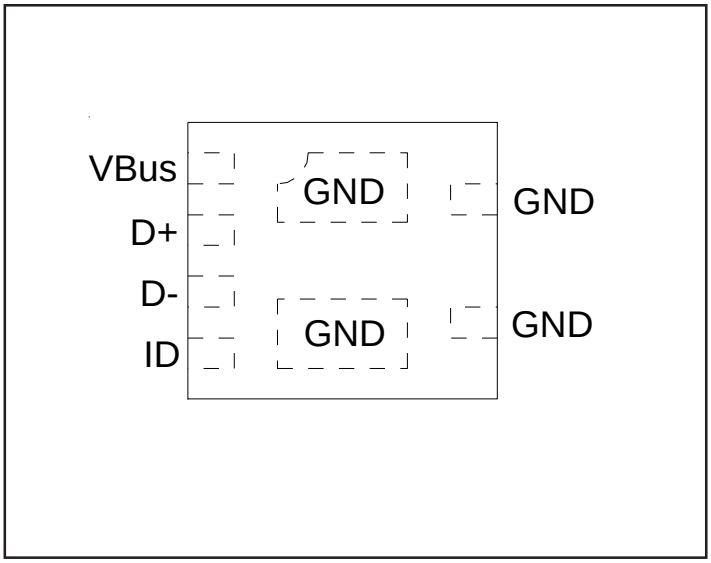
Applications

- USB 2.0
- USB OTG
- uUSB

Circuit Diagram



Pin Configuration (Top View)



Absolute Maximum Rating

Rating	Symbol	Value	Units
DP, DM, USB ID TVS			
Peak Pulse Power (tp = 8/20µs)	P_{pk}	40	Watts
Peak Pulse Current (tp = 8/20µs)	I_{PP}	3	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	±15 ±10	kV
Operating Temperature	T_J	-55 to +125	°C
Storage Temperature	T_{STG}	-55 to +150	°C
VBus TVS			
Peak Pulse Power (tp = 8/20µs)	P_{pk}	2500	Watts
Peak Pulse Current (tp = 8/20µs)	I_{PP}	100	A
ESD per IEC 61000-4-2 (Air) ESD per IEC 61000-4-2 (Contact)	V_{ESD}	±30 ±30	kV
Operating Temperature	T_J	-55 to +125	°C
Storage Temperature	T_{STG}	-55 to +150	°C

Electrical Characteristics (T=25°C)

VBus TVS (Pin 1)

Parameter	Symbol	Conditions	Minimum	Typical	Maximum	Units
Reverse Stand-Off Voltage	V_{RWM}	Pin 1 to GND			12	V
Reverse Breakdown Voltage	V_{BR}	I_t = 1mA, Pin 1 to GND	13.5	14.5	16.5	V
Reverse Leakage Current	I_R	V_{RWM} = 12V Pin 1 to GND			0.300	µA
Forward Voltage	V_F	I_f = 10mA GND to Pin 1	0.6	0.7	1.0	V
Clamping Voltage	V_C	I_{PP} = 30A, tp = 8/20µs Pin 1 to Ground		15.5	18	V
Clamping Voltage	V_C	I_{PP} = 100A, tp = 8/20µs Pin 1 to Ground		18.5	25	V
Junction Capacitance	C_j	V_R = 0V, f = 1MHz Pin 1 to GND		1950	2500	pF

Electrical Characteristics (T=25°C)

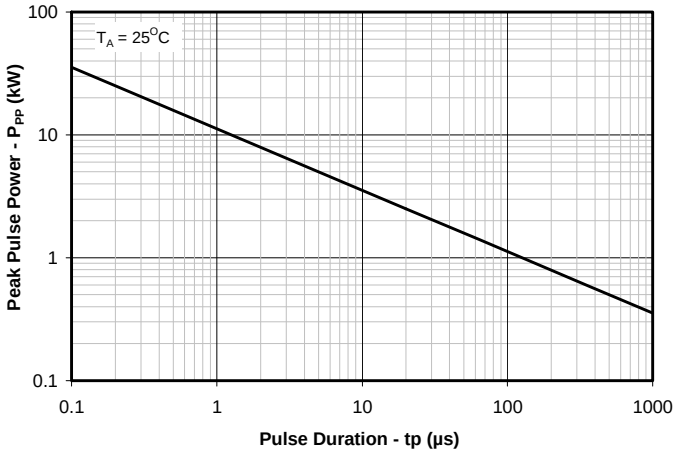
DM, DP, USB ID (Pins 2, 3, 4)						
Parameter	**Symbol**	**Conditions**	**Minimum**	**Typical**	**Maximum**	**Units**
Reverse Stand-Off Voltage	V_{RWM}	Pin 2, 3, 4 to GND			4	V
Reverse Breakdown Voltage	V_{BR}	It = 1mA, Pin 2, 3, 4 to GND	4.5	5.7	6.3	V
Reverse Leakage Current	I_R	V_{RWM} = 2.0V, Pin 2, 3, 4 to GND		<0.005	0.020	µA
Reverse Leakage Current	I_R	V_{RWM} = 4.0V, Pin 2, 3, 4 to GND		0.005	0.100	µA
Clamping Voltage	V_C	I_{PP} = 1A, tp = 8/20µs Pin 2, 3, 4 to GND			10.5	V
Clamping Voltage	V_C	I_{PP} = 3A, tp = 8/20µs Pin 2, 3, 4 to GND			12.5	V
Dynamic Resistance[1]	R_{Dyn}	Ipp = 4A to Ipp = 16A		0.70		Ohms
		Ipp = -4A to Ipp = -16A		0.70		Ohms
Junction Capacitance	C_j	V_R = 0V, f = 1MHz, Pin 2, 3, 4 to GND		0.35	0.50	pF

Notes

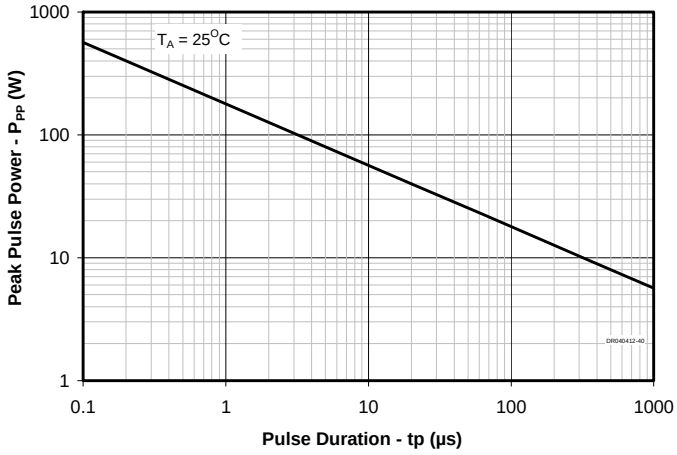
1)Transmission Line Pulse Test (TLP) Settings: t_p = 100ns, t_r = 0.2ns, I_{TLP} and V_{TLP} averaging window: t_1 = 70ns to t_2 = 90ns

Typical Characteristics

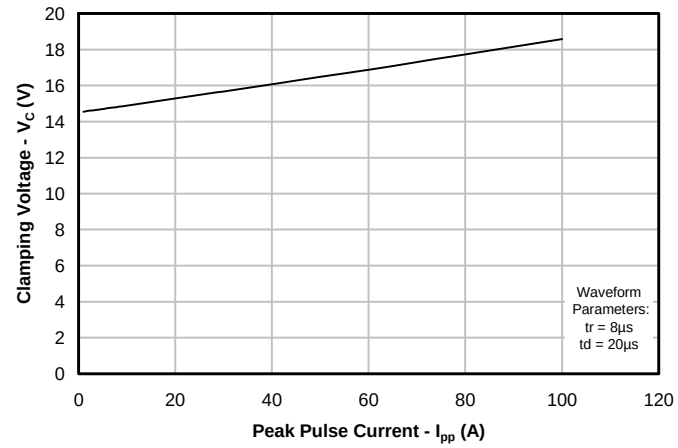
Non-Repetitive Peak Pulse Power vs. Pulse Time VBus Pin (Pin 1)



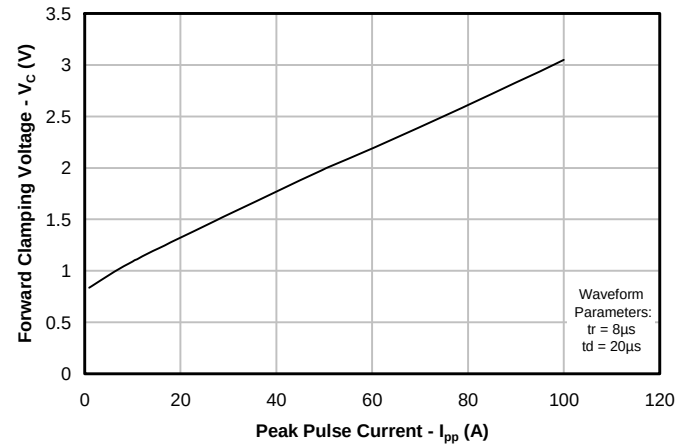
Non-Repetitive Peak Pulse Power vs. Pulse Time D+, D-. ID Pins (Pins 2, 3, 4)



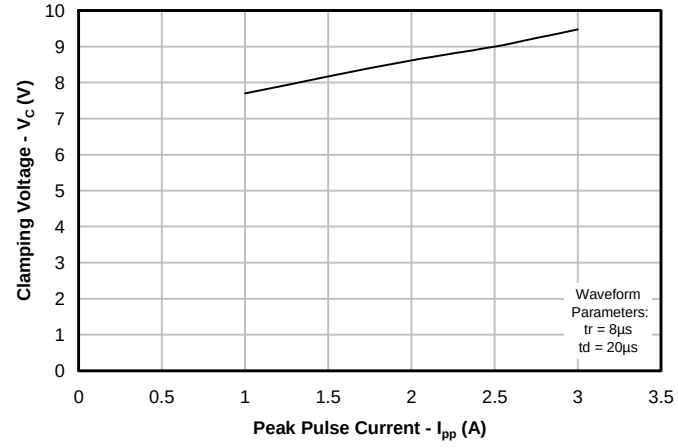
Clamping Voltage vs. Peak Pulse Current VBus Pin (Pin 1)



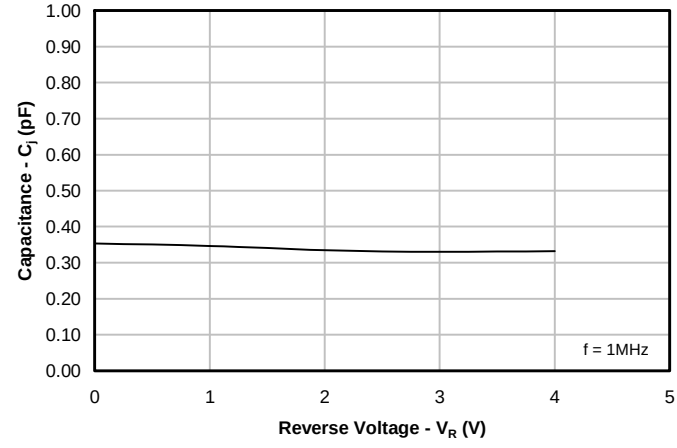
Forward Voltage vs. Peak Pulse Current VBus Pin (Pin 1)



Clamping Voltage vs. Peak Pulse Current D+, D-. ID Pins (Pins 2, 3, 4)

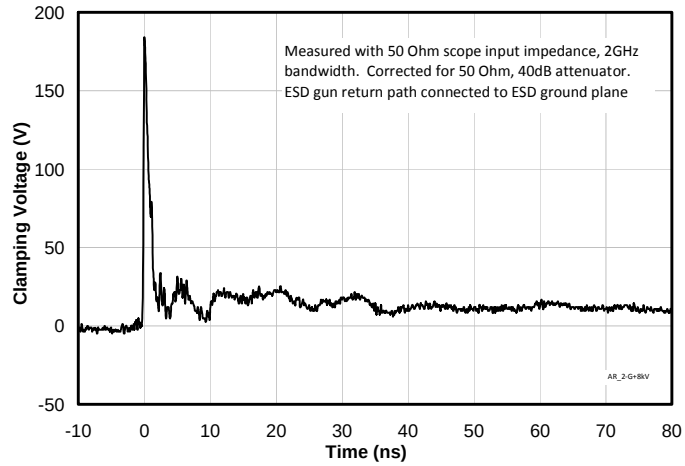


Capacitance vs. Reverse Voltage D+, D-. ID Pins (Pins 2, 3, 4)

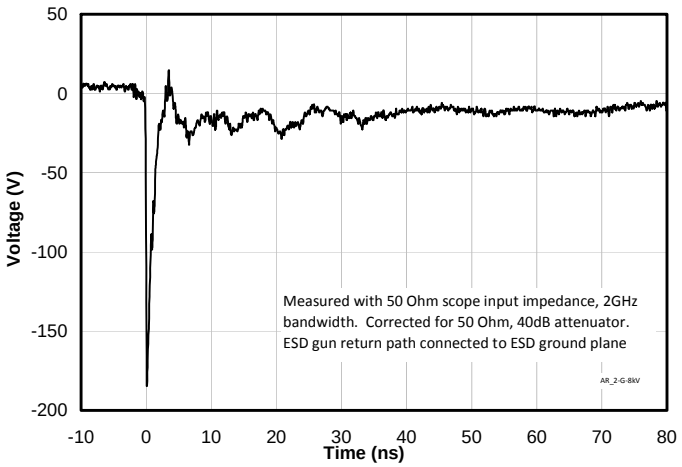


Typical Characteristics

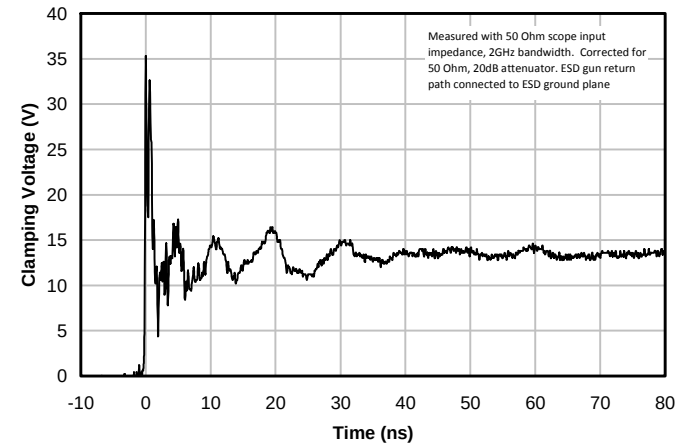
ESD Clamping (+8kV Contact per IEC 61000-4-2) D+, D-. ID Pins (Pins 2, 3, 4)



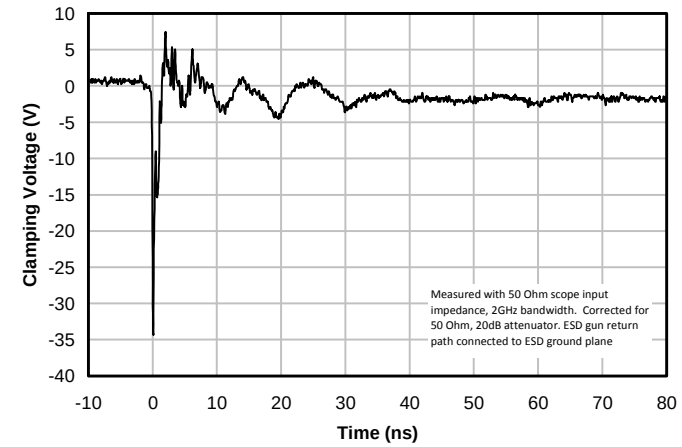
ESD Clamping (-8kV Contact per IEC 61000-4-2) D+, D-. ID Pins (Pins 2, 3, 4)



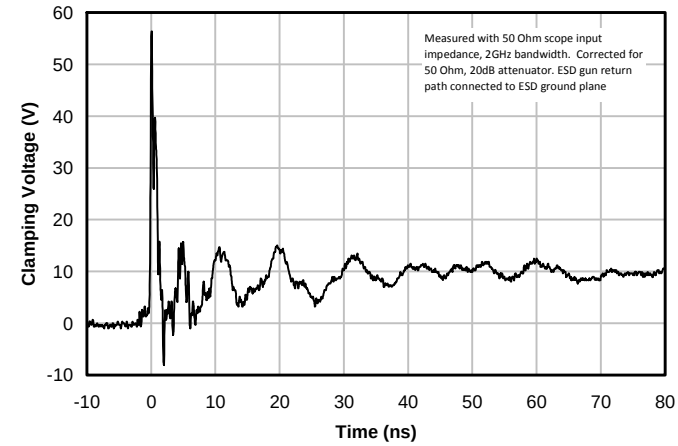
ESD Clamping (+8kV Contact per IEC 61000-4-2) VBus Pin (Pin 1)



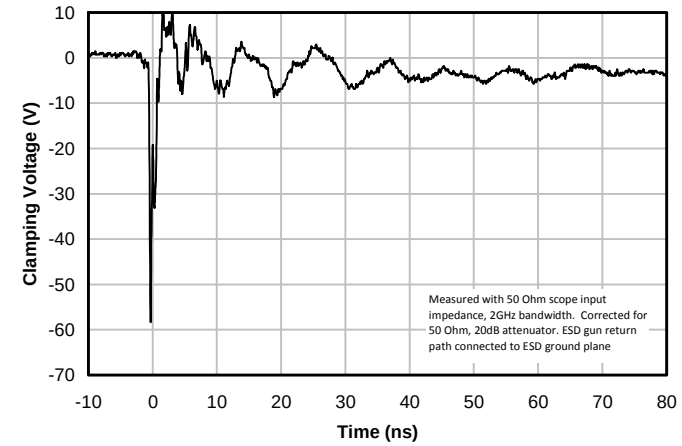
ESD Clamping (-8kV Contact per IEC 61000-4-2) VBus Pin (Pin 1)



ESD Clamping +30kV Contact per IEC 61000-4-2) VBus Pin (Pin 1)

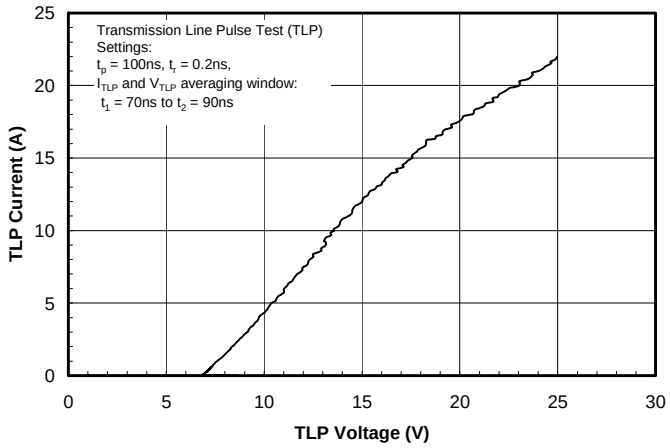


ESD Clamping -30kV Contact per IEC 61000-4-2) VBus Pin (Pin 1)

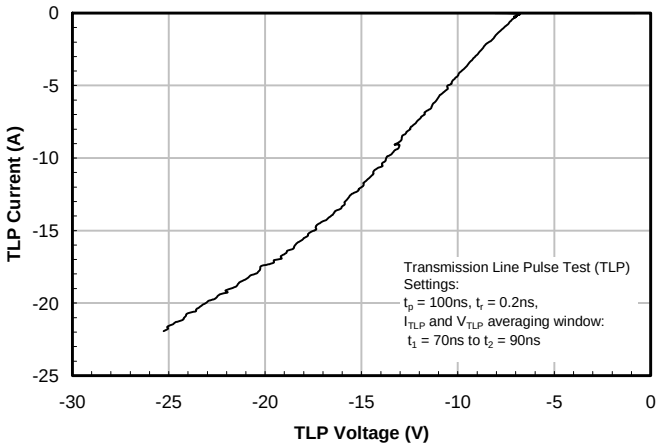


Typical Characteristics

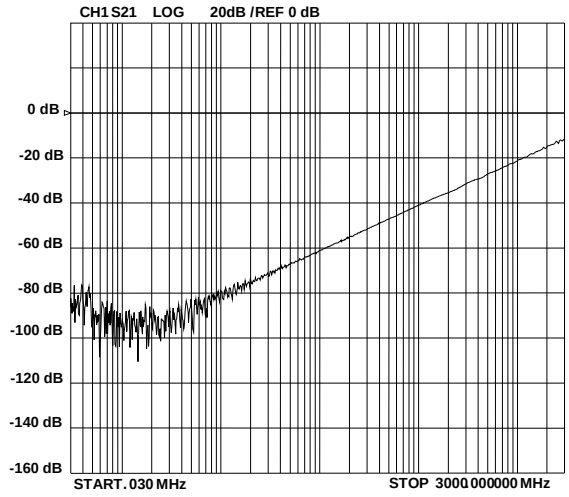
TLP Characteristic (Positive Pulse)
D+, D-. ID Pins (Pins 2, 3, 4)



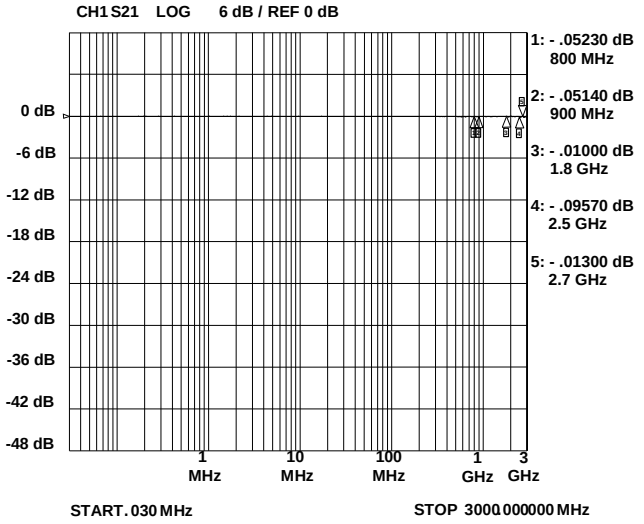
TLP Characteristic (Negative Pulse)
D+, D-. ID Pins (Pins 2, 3, 4)



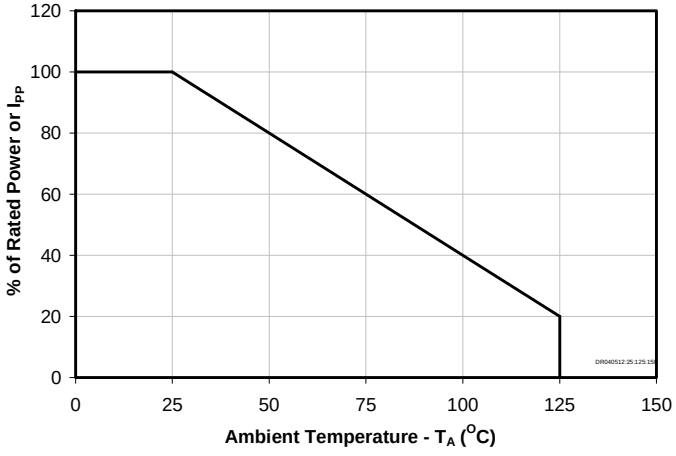
Analog Crosstakk
D+, D-. ID Pins (Pins 2, 3, 4)



Typical Insertion Loss S21
D+, D-. ID Pins (Pins 2, 3, 4)



Non-Repetitive Peak Pulse Power Derating Curve



Applications Information

Device Connection and Layout Options for Protecting One USB Port

The RClamp1255P is optimized for protection of USB ports. Low capacitance protection is provided for the USB data (DM, DP) and USB ID pins. The maximum capacitance on these lines is <0.5pF for maximum signal integrity. USB Data and ID lines are connected at pins 2, 3, and 4. These inputs are referenced to an internal 4 volt TVS protection device. When the voltage on these lines exceed 4 volts, the TVS will conduct. Pin 1 is connected to the USB voltage bus (VBus). This device will conduct when the voltage on the bus exceeds 12 volts. Ground is provided at pins 5, 6, and the center tabs. Multiple micro vias connected to ground are recommended for best ESD performance. This will reduce parasitic inductance in the ground path and minimize the clamping voltage seen by the protected device.

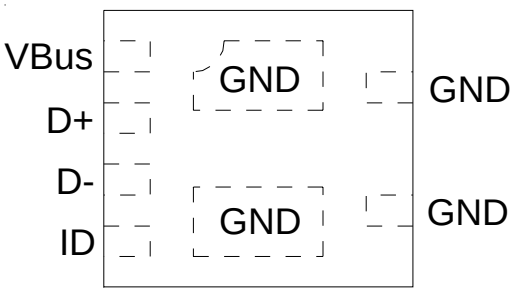


Figure 1 - Pin Configuration (Top View)

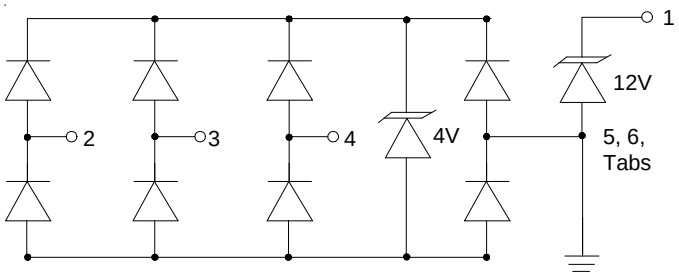
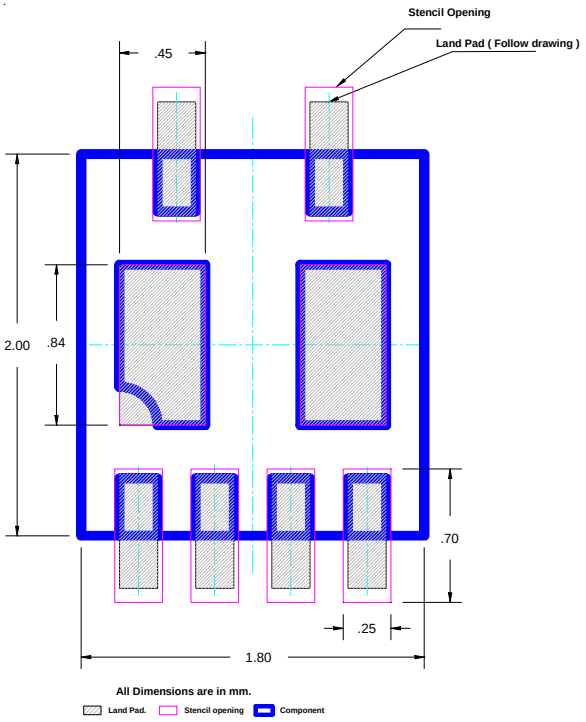


Figure 2 - Schematic

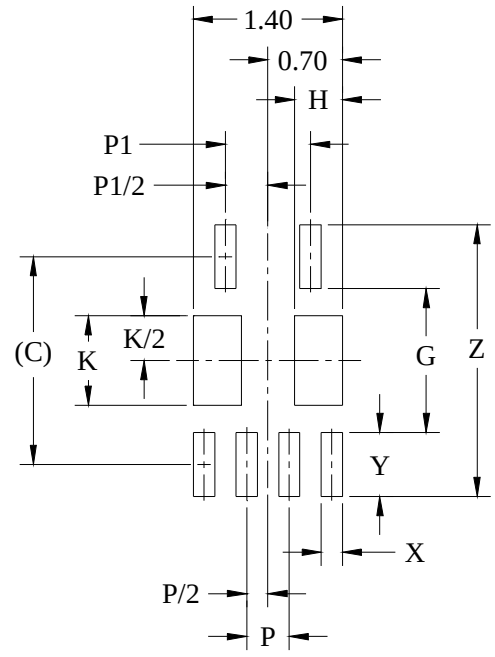
Applications Information

Assembly Parameter	Recommendation
Solder Stencil Design	Laser cut, Electro-polished
Aperture shape	Rectangular
Solder Stencil Thickness	0.100 mm (0.004")
Solder Paste Type	Type 3 size sphere or smaller
Solder Reflow Profile	Per JEDEC J-STD-020
PCB Solder Pad Design	Non-Solder mask defined
PCB Pad Finish	OSP OR NiAu



Recommended Mounting Pattern

Land Pattern - SLP2018P6

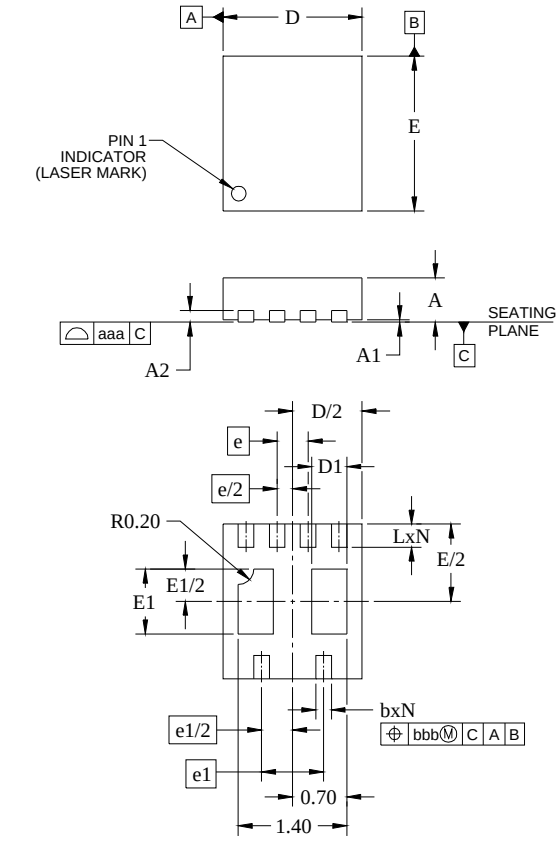


DIMENSIONS	
DIM	MILLIMETERS
C	(1.95)
G	1.35
H	0.45
K	0.84
P	0.40
P1	0.80
X	0.20
Y	0.60
Z	2.55

NOTES:

1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).
2. THIS LAND PATTERN IS FOR REFERENCE PURPOSES ONLY. CONSULT YOUR MANUFACTURING GROUP TO ENSURE YOUR COMPANY'S MANUFACTURING GUIDELINES ARE MET.

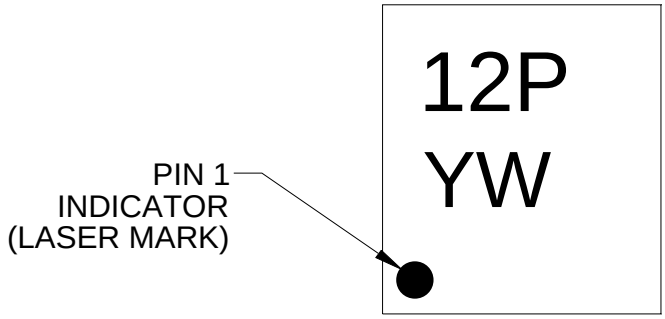
Outline Drawing - SLP2018P6



DIMENSIONS			
DIM	MILLIMETERS		
	MIN	NOM	MAX
A	0.50	0.575	0.65
A1	0.00	0.03	0.05
A2		(0.127)	
b	0.15	0.20	0.25
D	1.70	1.80	1.90
E	1.90	2.00	2.10
D1	0.30	0.45	0.55
E1	0.69	0.84	0.94
e	0.40 BSC		
e1	0.80 BSC		
L	0.25	0.30	0.35
N	6		
aaa	0.08		
bbb	0.10		

NOTES:
1. CONTROLLING DIMENSIONS ARE IN MILLIMETERS (ANGLES IN DEGREES).

Marking



YW = Date Code

Ordering Information

Part Number	Qty per Reel	Reel Size
RClamp1255P.TGT	10,000	13 Inch

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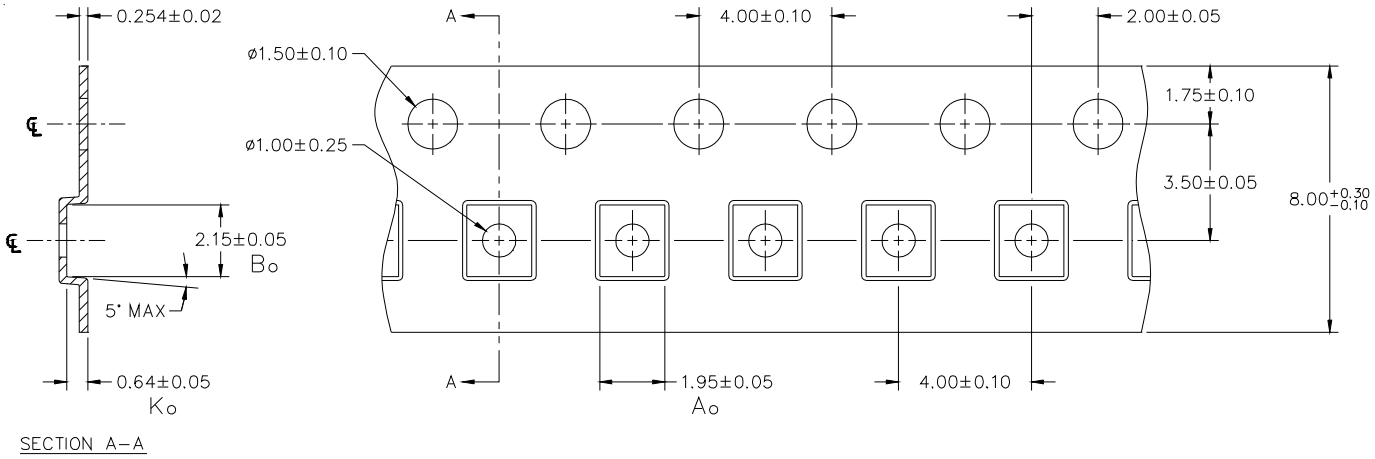
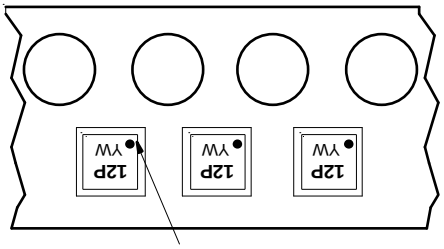
Carrier Tape Specification



NOTE: ALL DIMENSIONS IN MILLIMETERS UNLESS OTHERWISE SPECIFIED.



Pin 1 Location
(Towards Sprocket Holes)

Device Orientation in Tape

Contact Information

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Protection Products Division
200 Flynn Road, Camarillo, CA 93012
Phone: (805)498-2111 FAX (805)498-3804