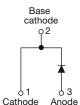
VS-8ETX06-M3

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PRIMARY CHARACTERISTICS								
I _{F(AV)}	8 A							
V _R	600 V							
V _F at I _F	1.4 V							
t _{rr} (typ.)	15 ns							
T _J max.	175 °C							
Package	TO-220AC 2L							
Circuit configuration	Single							

Hyperfast Rectifier, 8 A FRED Pt[®]

FEATURES

- Hyperfast recovery time
- Benchmark ultra low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recover time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of SMPS, inverters or as freewheeling diodes.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS								
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Repetitive peak reverse voltage	V _{RRM}		600	V				
Average rectified forward current	I _{F(AV)}	T _C = 143 °C	8					
Non-repetitive peak surge current	I _{FSM}	$T_J = 25 \ ^{\circ}C$	110	А				
Repetitive peak forward current	I _{FM}		18					
Operating junction and storage temperatures	T _J , T _{Stg}		-65 to +175	°C				

ELECTRICAL SPECIFICATIONS ($T_J = 25$ °C unless otherwise specified)									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-				
Forward voltage	V	I _F = 8 A	-	2.3	3.0	V			
Forward voltage	V _F	I _F = 8 A, T _J = 150 °C	-	1.4	1.7				
Poweree leekage eurrent	1	$V_{R} = V_{R}$ rated	-	0.3	50				
Reverse leakage current		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	35	500	μA			
Junction capacitance	CT	V _R = 600 V	-	17	-	pF			
Series inductance	L _S	Measured lead to lead 5 mm from package body	-	8.0	-	nH			

RoHS

HALOGEN

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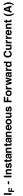


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DYNAMIC RECOVERY CHARACTERISTICS ($T_C = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS		
		$I_F = 1 \text{ A}, dI_F/dt = 100$	A/ μ s, V _R = 30 V	-	15	19		
Reverse recovery time	+	$I_F = 8 \text{ A}, dI_F/dt = 100$	A/ μ s, V _R = 30 V	-	16	24	50	
Reverse recovery time	t _{rr}	T _J = 25 °C		-	17	-	- ns	
		T _J = 125 °C	I _F = 8 A dI _F /dt = 200 A/μs V _R = 390 V	-	40	-		
Pook recovery ourrent	1	T _J = 25 °C		-	2.3	-	A	
Peak recovery current	I _{RRM}	T _J = 125 °C		-	4.5	-		
		T _J = 25 °C		-	20	-	nC	
Reverse recovery charge	Q _{rr}	T _J = 125 °C		-	100	-		
Reverse recovery time	t _{rr}		I _F = 8 A	-	31	-	ns	
Peak recovery current	I _{RRM}	T _J = 125 °C	$dI_F/dt = 600 \text{ A/}\mu\text{s}$	-	12	-	А	
Reverse recovery charge	Q _{rr}		V _R = 390 V	-	195	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS			
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C			
Thermal resistance, junction-to-case	R _{thJC}		-	1.4	2	°C/W			
Thermal resistance, junction-to-ambient per leg	R _{thJA}	Typical socket mount	-	-	70				
Thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-				
Weight			-	2.0	-	g			
Weight			-	0.07	-	oz.			
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)			
Marking device		Case style TO-220AC 2L		8ET	X06				



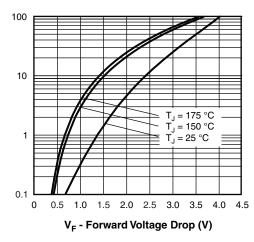


Fig. 1 - Typical Forward Voltage Drop Characteristics

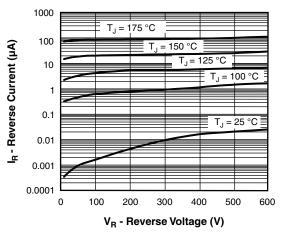


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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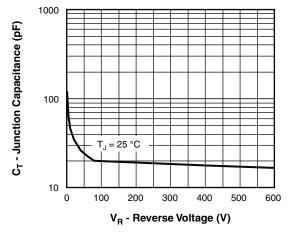


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

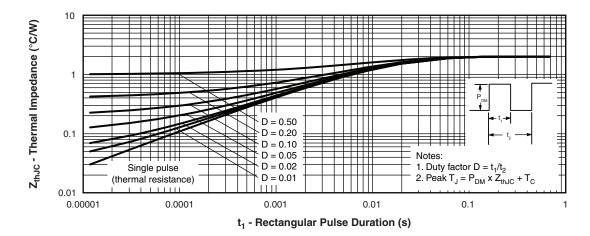
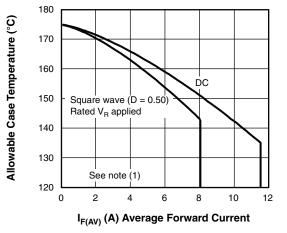
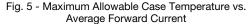


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

Average Power Loss (W)



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Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at ($I_{F(AV)}/D$) (see fig. 5); Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = rated V_R

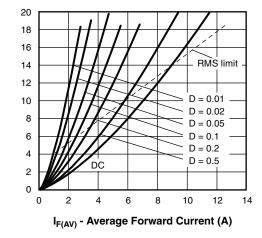


Fig. 6 - Forward Power Loss Characteristics

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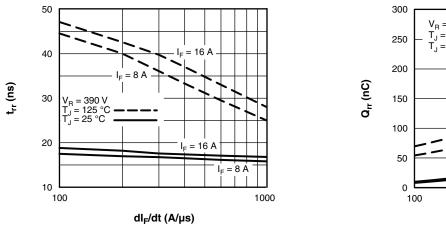


Fig. 7 - Typical Reverse Recovery Time vs. dl_F/dt

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SHAY

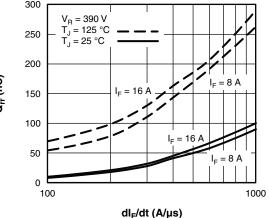


Fig. 8 - Typical Stored Charge vs. dl_F/dt

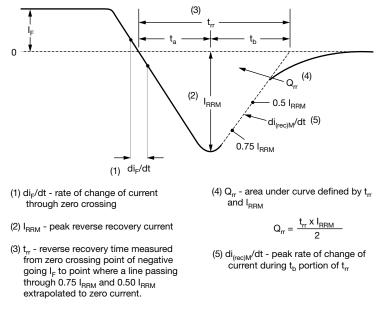


Fig. 9 - Reverse Recovery Waveform and Definitions



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ORDERING INFORMATION TABLE

Device code	vs-	8	Е	т	х	06	-M3
		2	3	4	5	6	(7)
	1	- Vis	hay Sen	nicondu	ctors pr	oduct	
	2	- Cu	rrent rati	ng (8 =	8 A)		
	3	- E=	single				
	4	- T=	TO-220), D ² PAk	(TO-26	63AB)	
	5	- X =	hyperfa	ist rectif	ier		
	6	- Vol	tage rati	ing (06 =	= 600 V)		
	7		/ironmei 3 = halog	0		-compli	iant, and

ORDERING INFORMATION (Example)								
PREFERRED P/N BASE QUANTITY PACKAGING DESCRIPTION								
VS-8ETX06-M3	50	Antistatic plastic tubes						

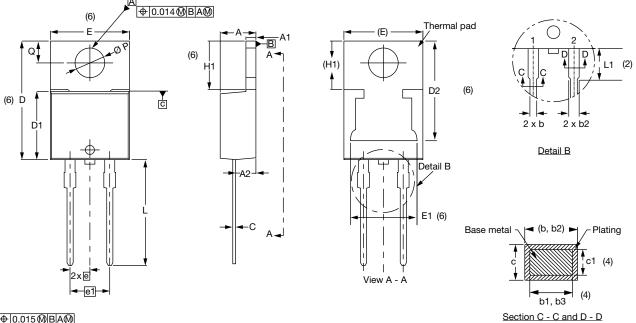
LINKS TO RELATED DOCUMENTS					
Dimensions www.vishay.com/doc?96156					
Part marking information	www.vishay.com/doc?95391				



Vishay Semiconductors

TO-220AC 2L

DIMENSIONS in millimeters and inches



⊕0.015@BA@



SYMBOL	MILLIN	IETERS	INC	HES	NOTES
STWIDOL	MIN.	MAX.	MIN.	MAX.	NOTES
А	4.25	4.65	0.167	0.183	
A1	1.14	1.40	0.045	0.055	
A2	2.50	2.92	0.098	0.115	
b	0.69	1.01	0.027	0.040	
b1	0.38	0.97	0.015	0.038	4
b2	1.20	1.73	0.047	0.068	
b3	1.14	1.73	0.045	0.068	4
С	0.36	0.61	0.014	0.024	
c1	0.36	0.56	0.014	0.022	4
D	14.85	15.35	0.585	0.604	3
D1	8.38	9.02	0.330	0.355	

SYMBOL	MILLIN	IETERS	INCHES		NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
D2	11.68	13.30	0.460	0.524	6, 7
Е	10.11	10.51	0.398	0.414	3, 6
E1	6.86	8.89	0.270	0.350	6
е	2.41	2.67	0.095	0.105	
e1	4.88	5.28	0.192	0.208	
H1	6.09	6.48	0.240	0.255	6
L	13.52	14.02	0.532	0.552	
L1	3.32	3.82	0.131	0.150	2
ØР	3.54	3.91	0.139	0.154	
Q	2.60	3.00	0.102	0.118	

Conforms to JEDEC[®] outline TO-220AC

Notes

⁽²⁾ Lead dimension and finish uncontrolled in L1

(4) Dimension b1, b3, and c1 apply to base metal only

- (6) Thermal pad contour optional within dimensions E, H1, D2, and E1
- ⁽⁷⁾ Outline conforms to JEDEC[®] TO-220, except D2

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Document Number: 96156

 $^{^{(1)}\,}$ Dimensioning and tolerancing as per ASME Y14.5M-1994 $\,$

⁽³⁾ Dimension D, D1, and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Controlling dimensions: inches



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