## **Product Change Notice**



iWave PCN Number	PCN00015		
Document Number	iW-PRFOZ-EC-01-R4.0-REL1.0-PCN00015		
Notification Date	27 <sup>th</sup> November 2021		
Title of Change	Feature enhancement		
Affected iWave Product Part Numbers	t Refer the Table 1.		
Type of Change	PCB upgraded for feature enhancement		
Description/ Reason of the change	<ul> <li>Feature Enhancement:</li> <li>To access JTAG Boot mode function from the carrier Board, BOOT MODE[0] MIO5 Signal is connected to 184<sup>th</sup> pin of SODIMM Edge connector through Zero-ohm resistor and by default this resistor is populated in SOM.</li> <li>To control Ethernet and USB PHY IC reset signal from Zynq 7000 SoC, optionally MIO0 GPIO signal is connected from E6<sup>th</sup> pin of Zynq 7000 SoC through Zero-ohm and not populated by default.</li> <li>Optionally System monitor VREFP signal is connected from 3rd pin of SODIMM Edge connected to L9<sup>th</sup> pin of Zynq 7000 SoC through Zero Ohm to support external 1.25V reference voltage for best performance of ADC. By default, this resistor is not populated in SOM.</li> <li>Optionally VCCBATT signal is connected from 49<sup>th</sup> pin of SODIMM Edge connector to F11<sup>th</sup> pin of Zynq 7000 SoC through Zero Ohm to support battery backup voltage from Carrier Board. By default, this resistor is not populated in SOM.</li> <li>PL HD BANK34 System Clock oscillator and PL HD BANK35 Optional differential reference clock oscillator power source voltage is changed from</li> </ul>		
	<ul> <li>fixed VCC_3V3 to concern bank voltage supply (VCCO_34 and VCCO_35) to control the voltage from PMIC.</li> <li>VCC_1V2 LDO regulator is changed from RT9043GB (400mA) to TPS74801DRCR (1.5A).</li> </ul>		

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Other affected items	Software BSP and FPGA Design changes:		
	There is no Software BSP and FPGA changes.		
	Mechanical:		
	<ul> <li>There is no mechanical dimension changes in the board except few components are re-arranged.</li> </ul>		
Method of Identification	In all the Zynq 7000 SoC SODIMM SOM, serial number label is pasted as shown below.		
	Serial Number iW-PRF0Z-01-R4.0 OK21-4500001		
	In the label, See the first line of serial number and find the version number.		
	iW-PRGFOZ-01- <mark>R4</mark> .0		
	<ul> <li>R4.0 represents the newer version of Zynq 7000 SoC SODIMM SOM</li> </ul>		
	Note: If serial number shows R2.0, then it is older version of Zynq 7000 SoC SODIMM SOM.		
Batch numbers affected by the change	All Zynq 7000 SoC SODIMM SOM which are R2.0 version.		
Product availability	New enhanced R4.0 version SOM available from November 2021.		
New iWave Product Part Number	Refer the <b>Table 1</b> .		
PCN effective from	11 <sup>th</sup> Nov 2021		
Contact Information	Email id: mktg@iwavesystems.com		

### Table 1 iWave Product Part Number

Affected iWave Product Part Numbers	Equivalent New iWave Product Part Numbers
iW-G28M-SM20-3D512M-E008G-LIE	iW-G28M-SM20-3D512M-E008G-LIF
iW-G28M-SM20-3D512M-E008G-BIE	iW-G28M-SM20-3D512M-E008G-BIF
iW-G28M-SM20-3D512M-E008G-LIA	iW-G28M-SM20-3D512M-E008G-LIB
iW-G28M-SM20-3D512M-E008G-BIA	iW-G28M-SM20-3D512M-E008G-BIB

#### Table 2 SODIMM EDGE Pinout change

SODIMM EDGE Connector Pin Number	SODIMM EDGE Connector Pin Name	R2.0 Version Zynq 7000 SoC SODIMM SOM	R4.0 Version Zynq 7000 SoC SODIMM SOM
184	B92	NC	NAND_DATA0/QSPI0_IO_3/MIO5
3	A2	NC	NC. Note: Optionally connected to L9 <sup>th</sup> pin of Zynq 7000 SoC through Zero Ohm resistor and not populated by default.
49	A25	NC	NC. Note: Optionally connected to F11 <sup>th</sup> pin of Zynq 7000 SoC through Zero Ohm resistor and not populated by default.

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#### **Document Revision History**

Revision	Date	Description
1.0	27 <sup>th</sup> Nov 2021	Draft Release Version

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