

C0603C105K3PACTU

Aliases (C0603C105K3PAC7867)

SMD Comm X5R, Ceramic, 1 uF, 10%, 25 VDC, X5R, SMD, MLCC, Temperature Stable, Class II, 0603



Click here for the 3D model.

Dimensions	
Chip Size	0603
L	1.6mm +/-0.15mm
W	0.8mm +/-0.15mm
Т	0.8mm +/-0.15mm
S	0.7mm MIN
В	0.35mm +/-0.15mm

Packaging Specifications		
Packaging	T&R, 180mm, Paper Tape	
Packaging Quantity	4000	

General Information	
Series	SMD Comm X5R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Component Weight	7.3 mg
Shelf Life	78 Weeks
MSL	1

Specifications	
Capacitance	1 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	25 VDC
Dielectric Withstanding Voltage	62.5 VDC
Temperature Range	-55/+85°C
Temperature Coefficient	X5R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	10% 1 kHz 1.0Vrms
Aging Rate	5% Loss/Decade Hour: Referee Time is 48 Hours
Insulation Resistance	100 MOhms

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