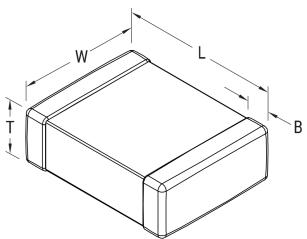


C1210C475K3PACTU

Aliases (C1210C475K3PAC7800)

SMD Comm X5R, Ceramic, 4.7 uF, 10%, 25 VDC, X5R, SMD, MLCC, Temperature Stable, Class II, 1210



Click here for the 3D model.

Dimensions	
Chip Size	1210
L	3.2mm +/-0.4mm
W	2.5mm +/-0.3mm
Т	1.9mm +/-0.2mm
В	0.5mm +/-0.25mm

Packaging Specifications		
Packaging	T&R, 180mm, Plastic Tape	
Packaging Quantity	2000	

General Information	
Series	SMD Comm X5R
Style	SMD Chip
Description	SMD, MLCC, Temperature Stable, Class II
Features	Temperature Stable, Class II
RoHS	Yes
Termination	Tin
Marking	No
AEC-Q200	No
Component Weight	105 mg
Shelf Life	78 Weeks
MSL	1

Specifications	
Capacitance	4.7 uF
Measurement Condition	1 kHz 1.0Vrms
Capacitance Tolerance	10%
Voltage DC	25 VDC
Dielectric Withstanding Voltage	62.5 VDC
Temperature Range	-55/+85°C
Temperature Coefficient	X5R
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 1kHz 1.0Vrms
Dissipation Factor	10% 1 kHz 1.0Vrms
Aging Rate	5% Loss/Decade Hour: Referee Time is 48 Hours
Insulation Resistance	21.3 MOhms

Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.