Vishay Semiconductors

ROHS COMPLIANT

HALOGEN

FREE

Hyperfast Rectifier, 2 x 30 A FRED Pt[®]



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LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS							
I _{F(AV)} 30 A							
V _R	600 V						
V _F at I _F	1.40 V						
t _{rr} (typ.)	22 ns						
T _J max.	175 °C						
Package	TO-3PF						
Circuit configuration	Common cathode						

FEATURES

- Hyperfast soft recovery time
- Low forward voltage drop
- 175 °C operating junction temperature
- Low leakage current
- Fully isolated package (V_{INS} = 2500 V_{RMS})
- Designed and qualified according to JEDEC[®]-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

Hyperfast recovery rectifiers designed with optimized performance of forward voltage drop, hyperfast recovery time, and soft recovery.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in PFC boost stage in the AC/DC section of switch mode power supplies and inverters (air conditioning, high-frequency welding, UPS, and motor drives)

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

MECHANICAL DATA

Case: TO-3PF

Molding compound meets UL 94 V-0 flammability rating **Terminals:** matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage	V _{RRM}		600	V
Average rectified forward current in DC, per leg	I _{F(AV)}		30	^
Non-repetitive peak surge current, per leg	I _{FSM}	$T_J = 25$ °C, both anodes connection	280	A
Operating junction and storage temperatures	T _J , T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)										
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS				
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	600	-	-					
Forward voltage, per leg	V	I _F = 30 A	-	1.70	2.15	15 V				
Forward voltage, per leg	V _F	I _F = 30 A, T _J = 150 °C	-	1.40	1.65					
	1	$V_{\rm R} = V_{\rm R}$ rated	-	0.02	10					
Reverse leakage current, per leg	IR	$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	36	300	μA				
Junction capacitance, per leg	CT	V _R = 600 V	-	19	-	pF				

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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)										
PARAMETER	SYMBOL	TEST CO	MIN.	TYP.	MAX.	UNITS				
	t _{rr}	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 100$	-	22	-					
Reverse recovery time, per leg		T _J = 25 °C		-	90	-	ns			
		T _J = 125 °C		-	110	-				
Peak recovery current, per leg	I _{RRM}	T _J = 25 °C	I _F = 30 A, dI _F /dt = 200 A/μs,	-	4.1	-	A nC			
Feak recovery current, per leg		T _J = 125 °C	$V_{\rm B} = 400 {\rm V}$	-	9.4	-				
	Q _{rr}	T _J = 25 °C		-	230	-				
Reverse recovery charge, per leg		T _J = 125 °C		-	730	-				

THERMAL - MECHANICAL SPECIFICATIONS								
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS		
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C		
Thermal resistance, junction-to-case, per leg	R _{thJC}		-	2.30	2.90			
Thermal resistance, junction-to-ambient, per leg	R _{thJA}	Typical socket mount	-	30	-	°C/W		
Typical thermal resistance, case-to-heatsink	R _{thCS}	Mounting surface, flat, smooth, and greased	-	0.5	-			
Weight			-	6.2	-	g		
Weight			-	0.21	-	oz.		
Mounting torque		(3		-	6 (5.3)	kgf · cm (lbf · in)		
Marking device		Case style TO-3PF	CZH6106FP					



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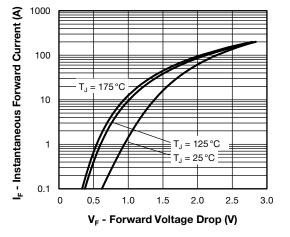


Fig. 1 - Forward Voltage Drop Characteristics, Per Leg

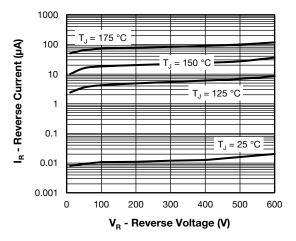


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage, Per Leg

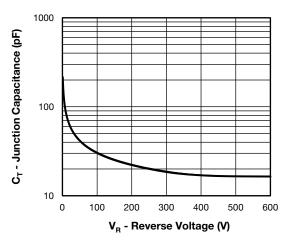


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage, Per Leg

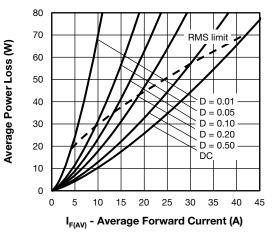
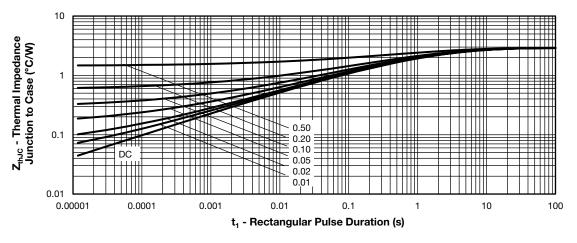
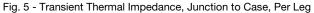


Fig. 4 - Forward Power Loss Characteristics, Per Leg





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VS-CZH6106FP-M3

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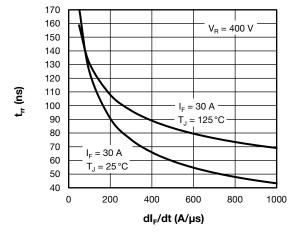


Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt, Per Leg

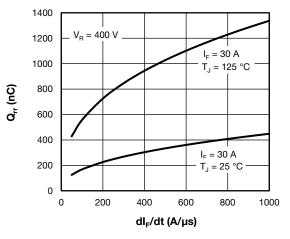


Fig. 7 - Typical Reverse Recovery Charge vs. dl_F/dt, Per Leg

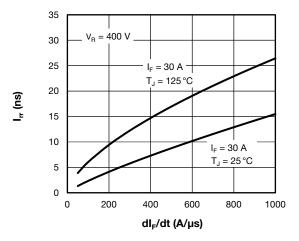


Fig. 8 - Typical Reverse Recovery Current vs. dl_F/dt, Per Leg



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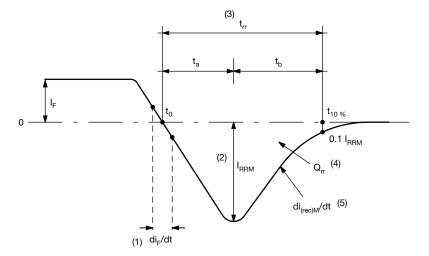


Fig. 9 - Reverse Recovery Waveform and Definitions

Notes

- $^{(1)}$ di_F/dt rate of change of current through zero crossing
- $^{(2)}$ I_{RRM} peak reverse recovery current
- $^{(3)}$ t_{rr} reverse recovery time measured from t₀, crossing point of negative going I_F, to point t_{10%}, 0.1 I_{RRM}
- $^{(4)}$ $\, \dot{Q}_{rr}$ area under curve defined by t_0 and $t_{10~\%}$

$$Q_{rr} = \int_{t_0}^{t_{10\%}} I(t) dt$$

 $^{(5)}$ di_{(rec)}M/dt - peak rate of change of current during t_b portion of t_{rr}

ORDERING INFORMATION TABLE

Device code

ode	VS-	С	z	н	61	06	FP	-M3		
	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)		
	1 .	· Visł	nay Sem	nicondu	ctors pr	oduct	U	U		
	2 -	Circ	uit con	iguratio	n:					
		C =	commo	on catho	de					
	3 -	- Z =	Z = TO-3FP package							
	4 -	H =	H = hyperfast recovery time							
	5 -	Cur	Current code: 61 = 60 A (2 x 30 A)							
	6 -	Volt	Voltage code: 06 = 600 V							
	7 -	FP :	FP = FullPAK							
	8 -	Env	ironmer	ntal digit	:					
		-M3	3 = halog	gen-free	, RoHS	-compli	ant, and	d termin	nations lead (P	

LINKS TO RELATED DOCUMENTS						
Dimensions www.vishay.com/doc?96691						
Part marking information	www.vishay.com/doc?96690					

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