### **DELIVERY SPECIFICATION**

SPEC. No. C2020-FG
D A T E: 2020 April

То

## Non-Controlled Copy

CUSTOMER'S PRODUCT NAME

Multilayer Ceramic Capacitors

Dipped Radial Lead Type

FG-Series

General (Up to 50V)

Mid voltage (100 to 630V)

[Halogen-free, RoHS compliant]

Please return this specification to TDK representatives with your signature. If orders are placed without returned specification, please allow us to judge that specification is accepted by your side.

#### RECEIPT CONFIRMATION

DATE: YEAR MONTH DAY

**TDK** Corporation

Sales Engineering

Electronic Components Electronic Components Business Company Sales & Marketing Group Ceramic Capacitors Business Group

APPROVED	Person in charge	APPROVED	CHECKED	Person in charge

#### **SCOPE**

This delivery specification shall be applied to Multilayer ceramic chip capacitors to be delivered to

#### **PRODUCTION PLACES**

Production places defined in this specification shall be TDK Xiamen Co., (China).

#### **PRODUCT NAME**

The name of the product to be defined in this specifications shall be  $\underline{FGOO\Delta\Delta\Box\Box\Box\times\times\times\otimes***}$ .

#### REFERENCE STANDARD

JIS C 5101-1 Fixed capacitors for use in electronic equipment-Part 1:

Generic specification

C 0806-2 Packaging of components for automatic handing-Part 2:

Packaging of components with unidirectional leads on continuous tapes

JEITA RCR-2335 C Safety application guide for fixed ceramic capacitors for use in electronic

equipment

#### **CONTENTS**

- 1. CODE CONSTRUCTION
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#### <EXPLANATORY NOTE>

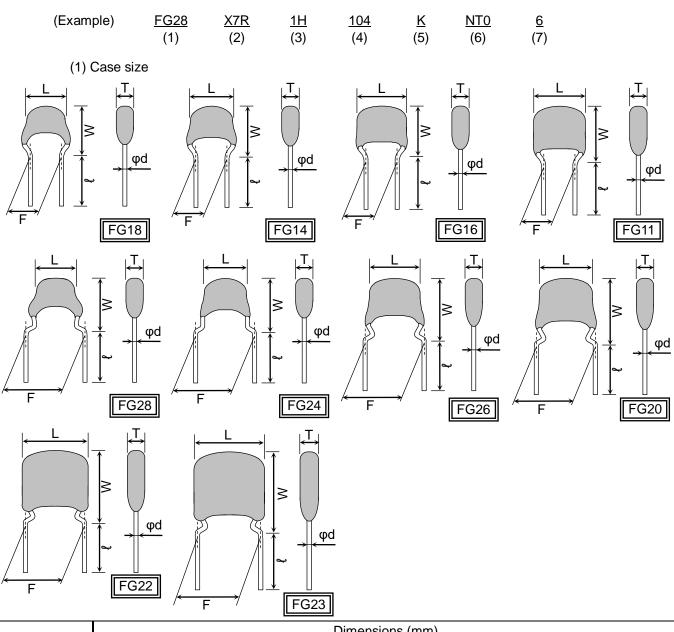
When the mistrust in the spec arises, this specification is given priority. And it will be confirmed by written spec change after conference of both posts involved.

This specification warrants the quality of the ceramic chip capacitor. Capacitors should be evaluated or confirmed a state of mounted on your product.

If the use of the capacitors goes beyond the bounds of this specification, we can not afford to guarantee.

Division	Date	SPEC. No.
Ceramic Capacitors Business Group	Apr., 2020	C2020-FG

#### 1. CODE CONSTRUCTION



Case size *1		Dimensions (mm)						
Case size i	L(max.) *2	W(max.)	T(max.)	F *3	l *3	φd		
FG18	4.0	5.5	2.5					
FG14	4.5	5.5	3.0	2.5±0.8	7.0±2.0	0.5 +0.10		
FG16	5.5	6.0	3.5	2.5±0.6	7.0±2.0	-0.03		
FG11	5.5	7.0	4.0					
FG28	4.0	5.5	2.5					
FG24	4.5	5.5	3.0					
FG26	5.5	6.0	3.5	5.0±1.0	7.0±2.0	0.5 +0.10		
FG20	5.5	7.0	4.0	5.0±1.0	7.0±2.0	-0.03		
FG22	7.5	8.5	4.5					
FG23	8.5	11.0	5.5					

<sup>\*1</sup> FG denotes forming lead.

The first digit refers to a distance between leads (1:2.5mm, 2:5.0mm), the second digit is for TDK internal code.

<sup>\*2</sup> The FG18, FG14, FG28 and FG24 types represent dimensions 1 mm below the top of the body.

Other types represent the dimensions of the central part of the body.

<sup>\*3</sup> Dimension F and  $\ell$  is applied to bulk packaging.

Refer to Appendix 2 and 3 for dimension of taping packaging.

(2) Temperature Characteristics (Details are shown in para 6 No.7,8)

(3) Rated Voltage

Symbol	Rated Voltage
2 J	DC 630 V
2 W	DC 450 V
2 E	DC 250 V
2 A	DC 100 V
1 H	DC 50 V
1 E	DC 25 V
1 C	DC 16 V
1 A	DC 10 V
0 J	DC 6.3 V

(4) Rated Capacitance

Stated in three digits and in units of pico farads (pF). The first and second digits identify the first and second significant figures of the capacitance, the third digit identifies the multiplier. R is designated for a decimal point.

(Example)

Symbol		Rated Capacitance
	2R2	2.2pF
٠	104	100,000pF

(5) Capacitance tolerance

Symbol	Tolerance		Capacitance(C)
С	±0.25 pF		C≦5pF
D	±0.5	pF	5pF <c≦10pf< td=""></c≦10pf<>
J	± 5	%	
K	±10	%	Over 10pF
М	±20	%	

(6) Internal code

Symbol	Applied voltage of Life
NT0	Rated voltage ×2 (*1)
RT0	Rated voltage ×1

\*1 2E : Rated voltage×1.5 2W : Rated voltage×1.2 2J : Rated voltage×1.2

(7) Packaging

Symbol	Packaging
0	Bulk
6	Ammo Pack

#### 2. COMBINATION OF RATED CAPACITANCE AND TOLERANCE

Class	Temperature Characteristics	Capacitance tol	erance	Rated capacitance(C)
		C≦5 pF	C (±0.25 pF)	1, 1.5, 2, 2.2, 3, 3.3, 4, 4.7, 5
4	606	5 pF <c≦10 pf<="" td=""><td>D (±0.5 pF)</td><td>6, 6.8, 7, 8, 9, 10</td></c≦10>	D (±0.5 pF)	6, 6.8, 7, 8, 9, 10
1	C0G	10 pF <c≦10,000 pf<="" td=""><td>J (± 5 %)</td><td>E-12 series</td></c≦10,000>	J (± 5 %)	E-12 series
		10,000 pF <c< td=""><td>J (± 5 %)</td><td>E- 6 series</td></c<>	J (± 5 %)	E- 6 series
2	X5R X7R	C≦10µF	K (±10 %) M (±20 %)	E- 6 series
۷	X7S X7T	10µF <c< td=""><td>M (±20 %)</td><td>E- 6 series</td></c<>	M (±20 %)	E- 6 series

Capacitance Step in E series

_	E series		Capacitance Step										
	E- 6	1.	.0	1.	.5	2	.2	3		4	.7	6	.8
_	E-12	1.0	1.2	1.5	1.8	2.2	2.7	3.3	3.9	4.7	5.6	6.8	8.2

#### 3. OPERATING TEMPERATURE RANGE

T.C.	Min. operating Temperature	Max. operating Temperature	Reference Temperature
X5R	-55°C	85°C	25°C
COG X7R X7S X7T	-55°C	125°C	25°C

#### 4. STORING CONDITION AND TERM

Storing temperature	Storing humidity	Storing term
5~40°C	20~70%RH	Within 6 months upon receipt.

#### **5. INDUSTRIAL WASTE DISPOSAL**

Dispose this product as industrial waste in accordance with the industrial Waste Law.

#### 6. PERFORMANCE

table 1

			table 1	1				
No.	l:	tem	Performance	Test or inspection method  By visual checking.				
1	External Ap	opearance	No defects which may affect performance.	By visua	al ched	cking.		
2	Indication	Appearance	Meet a requirement per para 7.	equirement per para 7.  solvent Solvent ten		emp.	Dipping time	
		Resistance to solvent	Shall be visible.	Isopro alcoh		20~25°C		30±5s.
3	Voltage Proof	Between termination	No insulation breakdown or other damage.	Class	Rate	ed voltage	Ар	ply voltage
					100\/	and under	2	rotod voltogo
				1		er 100V		rated voltage
								rated voltage
				2 —		and under		rated voltage
					Ov	er 100V	1.5 x	rated voltage
				Above D	Above DC voltage shall be applied for 1~5s.			
				Charge 50mA.	/ disc	harge curr	ent sh	nall not exceed
		Between termination coating	No insulation breakdown or other damage.	Apply ×2.5 rated voltage. (By metallic small ball method.)				
4	Insulation Resistance		10,000MΩ or 500 MΩ • μF min. (As for the capacitor of rated voltage 16,10 and 6.3V DC, 10,000 MΩ or 100 MΩ • μF min.,) whichever smaller.	≪450V DC and under≫ Apply rated voltage. ≪630V DC≫ Apply DC500V. Applying time : 60sec.				
5	Capacitano	е	Within the specified tolerance.	Class 1				
				Rated capac		Measur frequer		Measuring voltage
				1,000 and u	pF	1MHz±1		0.5~5
				Over 1,000	рF	1kHz±10	)%	Vrms.
				Class 2				
				Rated	itanaa	Measur		Measuring
				capac 10µF	nance		-	voltage 1.0±0.2
				and u	nder	1kHz±10	)%	Vrms.
				Over		120Hz±2	20%	0.5±0.2 Vrms.
						n which pro		
							se con	tact with our
-				sales re	preser	itative.		

	nued)		T					
No.		em	Perfo	ormance	<b>—</b>		est or inspection method	
6	Q (Class 1)		Capacitance	Q		See No.5 in this table for measuring condition.		
			30pF and over	1,000 min.		r inform	ation which product has which	
			Under 30pF	400+20×C min.			n Factor, please contact with our	
				apacitance (pF)			esentative.	
			C . Nated C	apacitatice (pi )		·		
	Dissipatio	n Factor						
	(Class 2)		T.C.	D.F.				
			X5R	0.03 max.				
			X7R	0.05 max.				
			X7S	0.075 max. 0.10 max.				
			X7T	0.10 max. 0.15 max.				
7	Temperat						re Coefficient shall be calculated	
	Character			re Coefficient			values at 25°C and 85°C	
	of Capaci (Class 1)	iance		m/°C)	ter	nperatur	e.	
	(212.00.)	Cod : 0 ± 30				easuring	temperature below 20°C shall	
			Capacitance drift		be	-10°C a	nd -25°C	
			Within ±0.2% or : whichever larger.					
8	Temperature		willchever larger.		Ca	apacitano	ce shall be measured by the	
	Characteristics		Capacitance Change(%)			ps show	vn in the following table, after	
	of Capacit (Class 2)	tance				thermal equilibrium is obtained for each step.		
	(Class 2)		No volta	ge applied			culated ref. STEP3 reading.	
			VED .	· 1F		Step	Temperature(°C)	
			X5R : X7R :	±15 ±15		1	Reference temp. ±2	
			X7S: ±22			<u> </u>	Nererence temp. 12	
			X7T :	+22, -33		2	Min operating temp 12	
						2	Min. operating temp. ±2	
						3	Reference temp. ±2	
						4	Max. operating temp. ±2	
					Fo ap	r informa plied vol	voltage: 0.1, 0.2, 0.5, 1.0Vrms. ation which product has which tage, please contact with our esentative.	
9	Lead Strength				to Pu	lead dra Illing stre	ng the parts, apply pulling force wing direction gradually. ength: 10N ne: 10±1s.	
		Bending Strength	No mechanical da breakage and loos	mage such as lead sing.	ve an Th rep Be	rtical, be d put it be is opera beat the ending fo	ng the capacitors to keep the axis and it 90 degrees with weighting back to the original position. Ition shall be done for 2~3s. and following times. It is strictly a times. It is strictly a times.	

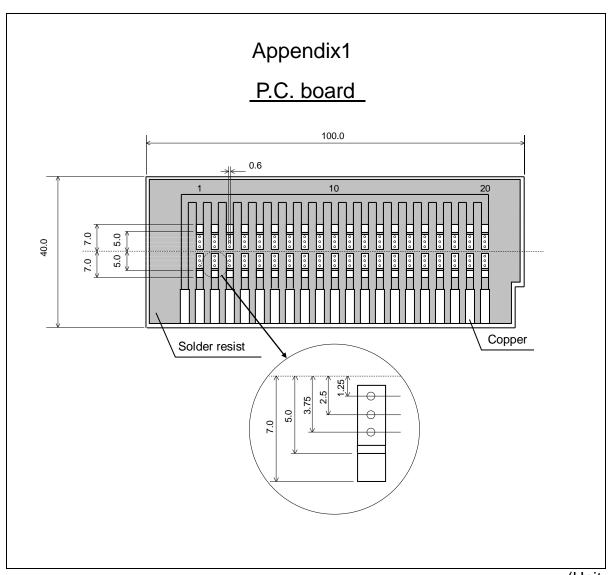
(conti	/		1			
No.		em			ormance	Test or inspection method
10	Vibration	External appearance Capacitance	No mecha	anical d	amage.	Solder the capacitors on a P.C.Board shown in Appendix1 before testing.  Vibrate the capacitor with amplitude
		Capacitario	Charact	eristics	Change from the value before test	of 1.5mm P-P changing the frequencies from 10Hz to 55Hz and
			Class 1	C0G	±2.5% or ±0.25pF, whichever larger.	back to 10Hz in about 1min. Repeat this for 2h each in 3 perpendicular directions.
			Class 2	X5R X7R X7S X7T	±7.5 %	
		Q Class1	Meet the			
		D.F. Class2	Meet the	initial sp	oec.	
11	Solderability				overed by new solder fits surface.	Completely soak both terminations in solder at 245±5°C for 2±0.5s.
						Solder: Sn-3.0Ag-0.5Cu(Pb-free) Flux: Isopropyl alcohol(JIS K 8839) Rosin(JIS K 5902) 25% solid solution. Dipping: By 1.5~2.0mm from the root of lead.
12	Resistance to solder	External appearance	No defect performar		may affect	Completely soak both terminations in solder at 260±5°C for 10±1s.
	heat	Capacitance	Charact	eristics	Change from the value before test	Solder: Sn-3.0Ag-0.5Cu(Pb-free) Flux: Isopropyl alcohol(JIS K 8839)
			Class 1	C0G	±2.5 % or ±0.25pF whichever larger.	Rosin(JIS K 5902) 25% solid solution. Dipping: By 1.5~2.0mm from the root of lead.
			Class 2	X5R X7R X7S X7T	±7.5 %	Leave the capacitors in ambient condition for the following time before measurement.
		Q Class1	Meet the initial spec.			Class1 : 6~24h Class2 : 24±2h
		D.F. Class2	Meet the	•		
		Insulation Resistance	Meet the			
		Voltage proof	No insulated damage.	insulation breakdown or other nage.		

(conti						T				
No.	Ite		NI		ormance		Test or in	spection	method	
13	Temperature Cycle and	External appearance	No mechanical damage.			Tempera	Temperature Cycle			
	Dipping Cycle	Capacitance				Step	Temp	o.(°C)	Time(min.)	
			Charact	teristics	Change from the value before test	1	Min. op Tem		30 ± 3	
			Class		±2.5 % or	2	Referen	ce temp.	Less than 3	
			1 COG	C0G	±0.25pF whichever larger.	3	May operating		30 ± 3	
			*01	X5R	7.5.0/	4	Referen	ce temp.	Less than 3	
			*Class 2	X7R X7S	± 7.5 % ± 10 %	Dipping	Cycle			
		*Applied for some parts Step	Temp.	Time	Solidy					
					•	   1	(°C) 65 +5	(min.) 15±2	liquid Pure water	
		Q Class1	Meet the	initial	spec.		0	15±2 15±2	Saturation	
		D.F Class2	Meet the	initial sp	Dec.	<u> </u>	0±3	10±2	salt water	
		Insulation	Meet the	initial sc	Dec.		he capaci n Append		P.C.Board e testing.	
		Resistance		·		Leave t	he capaci	itors in a	mbient condition	
		Voltage proof	No insulation breakdown or other damage.			Class	for the following time before measurement.  Class1 : 6~24h Class2 : 24±2h  Perform Temperature cycle(5 cycle) and dipping cycle(2 cycle) consecutively.			
						Perform				
	NA de la trans	Estable	NI							
14	Moisture Resistance (Steady	External appearance	No mech	anical d	amage.	Solder the capacitors on a P.C.Board shown in Appendix1 before testing.				
	State)	Capacitance			01		Leave at temperature 40±2°C, 90 to 95%RH for 500 +24,0h.			
	,		Charac	teristics	Change from the value before test	Leave the capacitors in ambier				
			Class 1	C0G	±5% or 0.5pF whichever larger.	Class	for the following time before measured  Class1 : 6~24h			
				X5R		Class	2 : 24=	Ł∠n		
			*Class 2	X7R X7S X7T	±12.5% ±25 %					
			*Applie		me parts					
		Q								
		Class1	Capac	citance	Q					
			•	nd over	350 min.					
				ind over r 30pF	275+5/2×C min.					
				r 10pF	200+10×C min.					
			C : Rated capacitance (pF)							
		D.F. Class2	200% of i	nitial sp	ec max.					
		Insulation Resistance	Insulation 1,000MΩ or 50 MΩ • μF min.							
			whicheve		er.					

NIC				D	f a was a sa a a	Toot or increasion methal
<u>No.</u>	Item		Performance			Test or inspection method
15	Moisture	External	No mechanical damage.		amage.	Solder the capacitors on a P.C.Board
	Resistance	appearance				shown in Appendix1 before testing.
		Capacitance				Apply the rated voltage at temperature
			Charac	teristics	Change from the	40±2°C and 90 to 95%RH for 500
			Onlarae		value before test	+24,0h.
			Class		±7.5% or	Charge/discharge current shall not
			1	C0G	±0.75pF	exceed 50mA.
					whichever larger.	Leave the capacitors in ambient
			*0'	X5R	4.5.5.0	condition for the following time
			*Class	X7R	±12.5 %	before measurement.
			2	X7S	±25 %	Classif : C 24h
			* ^ 1'	X7T		Class1 : 6~24h Class2 : 24±2h
			^Applie	*Applied for some parts		Classz . 24±211
		0				Voltage conditioning: (Only Class2)
		Q Class4				Voltage treat the capacitor under
		Class1	Capa	acitance	· Q	testing temperature and voltage for
			30pF	and ove	r 200 min.	1hour.
			Und	er 30pF	100+10/3×C min.	Leave the capacitors in ambient condition for 24±2h before
			C :	Rated	capacitance (pF)	measurement.
						Use this measurement for initial value.
	D.F. Class2 Insulation Resistance		200% of	initial sp	ec max.	
			500MΩ c	r 25MΩ	• µF min.	
					citor of rated voltage	
			16,10 an	d 6.3V [	DC, 500 M $\Omega$ or 5M $\Omega$ •	
			μF min.,)			
			whicheve	er smalle	er.	

No.	Inuea)	em		Per	formance	Test or inspection method
16	appearance  Capacitance  Characteristics  Change from the value before test	External appearance	No mech	anical o	damage.	Solder the capacitors on a P.C.Board shown in Appendix1 before testing.
		Below the voltage shall be applied at maximum operating temperature ±2°C for 1,000 +48,0h.				
			Class	COG	±3% or ±0.3pF	Applied voltage
			11		whichever larger.	Rated voltage x2
			*Class	X5R X7R	±15 %	Rated voltage x1.5
			2	X7S X7T	±25 %	Rated voltage x1.2
			*Applie	ed for so	ome parts	Rated voltage x1
		Q				For information which products has
		Class1		citance and ove		which applied voltage, please contact with our sales representative.
			10pF	and ove er 30pF		Charge/discharge current shall not
			-	er 10pF	200+10×C min.	exceed 50mA.
			C : Rated capacitance (pF)			Leave the capacitors in ambient condition for the following time before
		D.F. Class2	200% of	initial s <sub>l</sub>	pec max.	measurement.  Class1 : 6~24h  Class2 : 24±2h
		Insulation Resistance	(As for th	ne capa d 6.3V	MΩ • μF min. citor of rated voltage DC, 1,000 MΩ or 10	Voltage conditioning: (Only Class2) Voltage treat the capacitor under testing temperature and voltage for 1hour.
			MΩ · μF whicheve	min.,) er small	er.	Leave the capacitors in ambient condition for 24±2h before measurement. Use this measurement for initial value.

<sup>\*</sup> As for the initial measurement of capacitors (Class2) on number 8, 10, 12, 13, and 14, leave capacitors at 150 -10,0°C for 1h and measure the value after leaving capacitors for 24±2h in ambient condition.



(Unit : mm)

- 1. Material : Glass Epoxy(As per JIS C6484 GE4)
- 2. Thickness: 1.6mm Copper(Thickness: 0.035mm)

Solder resist

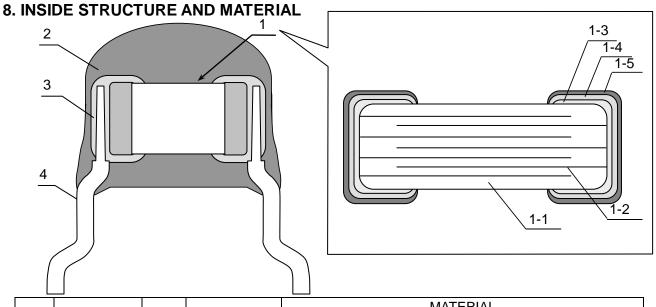
#### 7. INDICATION

#### 7.1 Indication (Example)

Type T.C.	FG18 FG14 FG28 FG24	FG16 FG11 FG26 FG20	FG22 FG23
COG	(1) -> 333	$(1) \longrightarrow 104 J \longleftrightarrow (2)$ $(3) \longrightarrow 104 J \longleftrightarrow (2)$	(1) 224J (2) (3) TDK (4)
X5R X7R X7S X7T	(1)	(1) 106K (2)	(1) 226M (2) (3) TDK (4)

#### 7.2 Meaning of indication

No.	Item	Detail
(1)	Rated Capacitance	Indicate in three digits.
(2)	Capacitance tolerance	Indicates the symbol.
(3)	Rated voltage	For DC50V, indicate a bar under the rated capacitance.
(4)	Manufacturer	Indicates " TDK ".



Na	lo. NAME	Na	NIANAE	MATE	RIAL		
INO.		No.	NAME	Class 1	Class 2		
	Multilayer	1-1	Dielectric	CaZrO₃	BaTiO₃		
		1-2	Electrode	N	li		
1	Ceramic Chip	1-3		Cu			
	Capacitors	1-4	Termination	Ni			
		1-5		Sn			
		0		Ероху			
2		Coating	9	[Halogen-free]			
3	So	lder for	joint	Lead free solder			
4	I	_ead wi	re	Tin plated copper covers steel wire			

#### 9. PACKAGING

Packaging shall be done to protect the components from the damage during Transportation and storing, and a label which has the following information shall be attached.

- 1) Inspection No. \*
- 2) TDK P/N
- 3) Quantity
- \* Composition of Inspection No.

Example 
$$\underline{X}$$
  $\underline{0}$   $\underline{A}$  -  $\underline{OO}$  -  $\underline{OOO}$  (a) (b) (c) (d) (e)

- a) Inspection factory code
- b) Last digit of year
- c) Month and A for January and B for February and so on. (Skip I)
- d) Inspection Date of the month.
- e) Serial No. of the day
- 1) Total number of components in a plastic bag.

Type	Qty.(pcs.)
FG18, FG28	
FG14, FG24	
FG16, FG26	500
FG11, FG20	
FG22	
FG23	200

2) Tape packaging is as per TDK tape packaging specification.

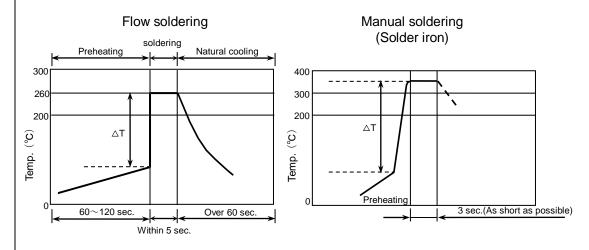
#### **10. CAUTION**

	Dragge	Condition						
No.	Process	Condition						
1	Operating Condition (Storage,Use,	<ul> <li>1-1. Storage, Use</li> <li>1) The capacitor must be stored in an ambient temperature of 5~40°C with a relative humidity of 20~70%. The products should be used within 6 months upon receipt.</li> </ul>						
	Transportation)	<ol> <li>The capacitors must be operated and stored in an environment free of dew condensation and these gases such as Hydrogen Sulphide, Hydrogen Sulphate, Chlorine, Ammonia and sulfur.</li> </ol>						
		3) Avoid storing in sun light and wet with dew.						
		Do not use capacitors under high humidity and high and low atmospheric pressure which may affect capacitors reliability.						
		5) Capacitors should be tested for the solderability when they are stored for long time.						
		<ul> <li>1-2. Handling in transportation</li> <li>1) In case of the transportation of the capacitors, the performance of the capacitors may be deteriorated depending on the transportation condition.         (Refer to JEITA RCR-2335C 9.2 Handling in transportation)     </li> </ul>						
2	Circuit design  Caution	1. Operating temperature Operating temperature should be followed strictly within this specification, especially be careful with the maximum temperature.						
		Do not use capacitor above the maximum allowable operating temperature.						
		Surface temperature including self heating should be below maximum operating temperature.						
		(Due to dielectric loss, capacitor will heat itself when AC is applied. Especially at high frequencies around its SRF, the heat might be so extreme that it may damage itself or the product mounted on. Please design the circuit so that the maximum temperature of the capacitor including the self heating to be below the maximum allowable operating temperature. Temperature rise shall be bellow 20°C.)						
		<ol> <li>The electrical characteristics of the capacitors will vary depending on the temperature. The capacitors should be selected and designed in taking the temperature into consideration.</li> </ol>						
		<ul><li>2-2. Operating voltage</li><li>1) Operating voltage across the terminals should be below the rated voltage.</li><li>When AC and DC are super imposed, V0-P must be below the rated voltage.</li></ul>						
		AC or pulse with overshooting, V <sub>P-P</sub> must be below the rated voltage.						
		———(3), (4) and (5) When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing these irregular voltage.						
		Voltage (1) DC voltage (2) DC+AC voltage (3) AC voltage						
		Positional Measurement (Rated voltage) $V_{0-P}$ $V_{0-P}$ $V_{0-P}$						
		Voltage (4) Pulse voltage (A) (5) Pulse voltage (B)						
		Positional Measurement (Rated voltage)						
	<u> </u>							

No.	Process	Condition	
2	Circuit design Caution	<ol> <li>Even below the rated voltage, if repetitive high frequancy AC or pulse is applied, the reliability of the capacitor may be reduced.</li> <li>The effective capacitance will vary depending on applied DC and AC voltages. The capacitors should be selected and designed in taking the voltages into consideration.</li> <li>Frequency</li> <li>When the capacitors (Class 2) are used in AC and/or pulse voltages, the capacitor may vibrate themselves and generate audible sound.</li> </ol>	e
3	Designing P.C.board	If capacitor leads are inserted into different pitch holes, it may induce excessive stress in the capacitor or outer resin to result in cracking, and it may degrade the quality.  Recommend capacitor layout is as following.	in
		Not recommended Recommended crack	
4	Lead wire insertion	1) If the leads clinching is too tight, the lead wire tend to be pulled excessively to cause lead wire breakage or cracking of the coating and quality degradation. Please adjust the clinching and provide sufficient preventive maintenance. Recommended capacitor layout is as following.    Not recommended   Recommended	

# No. Process Soldering 5-1. Flux selection Although highly-activated flux gives better solderability, substances which increase activity may also degrade the insulation of the capacitors. To avoid such degradation, it is recommended following. 1) It is recommended to use a mildly activated rosin flux (less than 0.1wt% chlorine). Do not use acidic flux is not recommended. 2) Excessive flux must be avoided. Please provide proper amount of flux. 3) When water-soluble flux is used, enough washing is necessary.

#### 5-2. Recommended soldering profile by various methods



#### 5-3. Avoiding thermal shock

1) Preheating condition

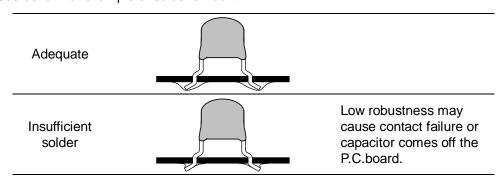
Soldering	Temp.(°C)
Wave soldering	ΔT≦150
Manual soldering	ΔT≦190

#### 2) Cooling condition

Natural cooling using air is recommended. If the chips are dipped into a solvent for cleaning, the temperature difference( $\Delta T$ ) must be less than 100°C.

#### 5-4. Amount of solder

In sufficient solder may detach the capacitor from the P.C.board. See bellow for example of solder amount.



No.	Process	Condition				
5	Soldering				out the heat shock n	
		( Recommended solder iron condition )				
		Temp. (°C)	Wattage (W)	Shape (mm)	Time (sec.)	
		350 MAX.	20 MAX.	φ3.0 MAX.	3 MAX.	
6	Cleaning	1) If an unsuitable cleaning fluid is used, flux residue or some foreign articles may stick to capacitor surface to deteriorate especially the insulation resistance.  2) If cleaning condition is not suitable, it may damage the capacitor.  2)-1. Insufficient washing (1) Terminal electrodes may corrode by Halogen in the flux. (2) Halogen in the flux may adhere on the surface of capacitor, and lower the insulation resistance. (3) Water soluble flux has higher tendency to have above mentioned problems (and (2)).  2)-2. Excessive washing (1) Excessive washing way damage the coating material of coated capacitor and deteriorate it. (2) When ultrasonic cleaning is used, excessively high ultrasonic energy output catefiect the adhesion between the ceramic dielectric and the terminal electrodes. To avoid this, following is the recommended condition.  Power: 20W/// max. Frequency: 40kHz max. Washing time: 5 minutes max.			ower the	
					ıtput can	
2)-3. If the cleaning fluid is contaminated, density of Halogen increase bring the same result as insufficient cleaning.			n increases, and it r	nay		
7	Coating and molding of the P.C.board	1) When the P.C.boar 2) Please verify caref emission during coans and a second	ully that there is no uring which may da		ing or reaction gas	luct.

No.	Process	Condition		
8	Lead wire bending	During lead wire bending process, mechanical stress often concentrates in one part of capacitor body and it may damage the ceramic and the coating.  Refer to following for bending the lead wire.  fixture  When bending the lead wire, hold the wire closer to the capacitor with a fixture so that the lead bending would not affect the capacitor body.		
		the lead bending would not affect the capacitor body.		
9	Handling of loose capacitor	If dropped the capacitor may crack. Once dropped do not use it. Especially, the large case sized capacitor is tendency to have cracks easily, so please handle with care.		
10	Capacitance aging	The capacitors (Class 2) have aging in the capacitance. They may not be used in precision time constant circuit. In case of the time constant circuit, the evaluation should be done well.		
11	Estimated life and estimated failure rate of capacitors	The estimated life and the estimated failure rate depend on the temperature and the voltage. This can be calculated by the equation described in JEITA RCR-2335C Annex F(Informative) Calculation of the estimated lifetime and the estimated failure rate (Temperature acceleration: 3rd powered low, Voltage acceleration: 10degC law) The failure rate can be decreased by reducing the temperature and the voltage but they will not be guaranteed.		

No.	No. Process Condition		
12	Caution during operation of equipment	A capacitor shall not be touched directly with bare hands during operation in order to avoid electric shock.     Electric energy held by the capacitor may be discharged through the human body when touched with a bare hand.     Even when the equipment is off, a capacitor may stay charged. The capacitor should be handled after being completely discharged using a resistor.	
	2) The terminals of a capacitor shall not be short-circuited by any accidental contact conductive object. A capacitor shall not be exposed to a conductive liquid such a acid or alkali solution. A conductive object or liquid, such as acid and alkali, betweeterminals may lead to the breakdown of a capacitor due to short circuit		
transportation and operation meets the specified condi- equipment in the following environments.  (1) Environment where a capacitor is spattered with wa (2) Environment where a capacitor is exposed to direct (3) Environment where a capacitor is exposed to Ozon (4) Environment where a capacitor exposed to corrosiv sulfur dioxide, chlorine. ammonia gas etc.)		<ol> <li>Environment where a capacitor is spattered with water or oil</li> <li>Environment where a capacitor is exposed to direct sunlight</li> <li>Environment where a capacitor is exposed to Ozone, ultraviolet rays or radiation</li> <li>Environment where a capacitor exposed to corrosive gas(e.g. hydrogen sulfide, sulfur dioxide, chlorine. ammonia gas etc.)</li> <li>Environment where a capacitor exposed to vibration or mechanical shock exceeding the specified limits.</li> </ol>	
13	Others Caution	The products listed on this specification sheet are intended for use in general electronic equipment (AV equipment, telecommunications equipment, home appliances, amusement equipment, computer equipment, personal equipment, office equipment, measurement equipment, industrial robots) under a normal operation and use condition.  The products are not designed or warranted to meet the requirements of the applications listed below, whose performance and/or quality require a more stringent level of safety or reliability, or whose failure, malfunction or trouble could cause serious damage to society, person or property. If you intend to use the products in the applications listed below or if you have special requirements exceeding the range or conditions set forth in this catalog, please contact us.  (1) Aerospace/Aviation equipment (2) Transportation equipment (cars, electric trains, ships, etc.) (3) Medical equipment (Excepting Pharmaceutical Affairs Law classification Class1, 2) (4) Power-generation control equipment (5) Atomic energy-related equipment (6) Seabed equipment (7) Transportation control equipment (8) Public information-processing equipment (9) Military equipment (10) Electric heating apparatus, burning equipment (11) Disaster prevention/crime prevention equipment (12) Safety equipment (13) Other applications that are not considered general-purpose applications. When designing your equipment even for general-purpose applications, you are kindly requested to take into consideration securing protection circuit/device or providing backup circuits in your equipment.	

#### 11.TAPE PACKAGING SPECIFICATION

#### 1. DIMENSION OF TAPING

Dimensions of FG1\* type shall be according to Appendix 2.

Dimensions of FG2\* type shall be according to Appendix 3.

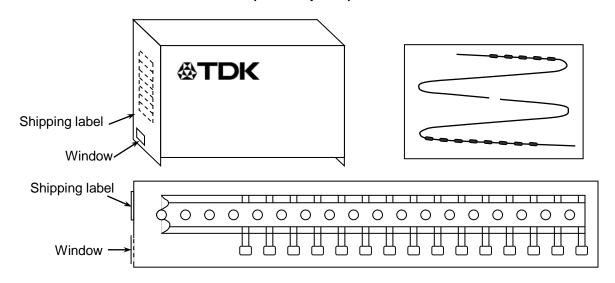
#### 2. QUANTITY

Туре	Parts quantity/box (pcs.)
FG18, FG28	
FG14, FG24	2,000
FG16, FG26	
FG11, FG20	1,500
FG22, FG23	1,000

#### 3. PERFORMANCE SPECIFICATIONS

- 3-1. The missing of components shall be within consecutive 3pcs.
- 3-2. Empty part for min 3pcs shall be provided at the beginning and the end of taping.
- 3-3. Shipping label must be attached at the side of carton.
- 3-4. When pull the carrier tape for left side with keeping the head of capacitors to the direction of the above figure, adhesive tape shall be upper side.
- 3-5. Folded tape shall contain 25pcs. of components.

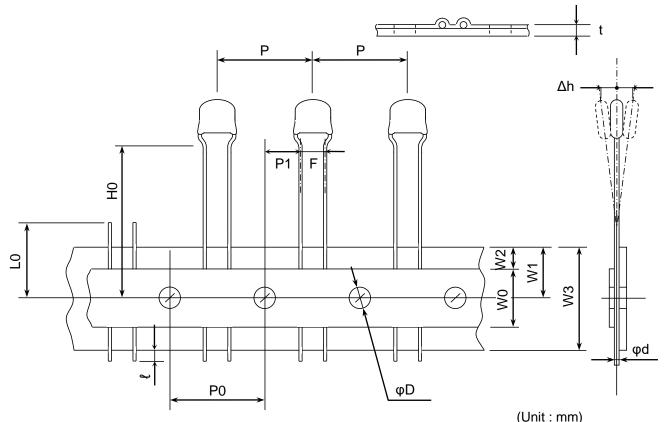
#### 4. PACKAGING SPECIFICATION (Ammo pack)



- 4-1. Head of the capacitors shall face the window.
- 4-2. In case of FG22 and FG23 series, a stainless round steel is put in a hole of tape. Please remove a stainless round steel at the time of use.

# Appendix 2

# **Taping dimensions** (FG18,FG14,FG16,FG11)

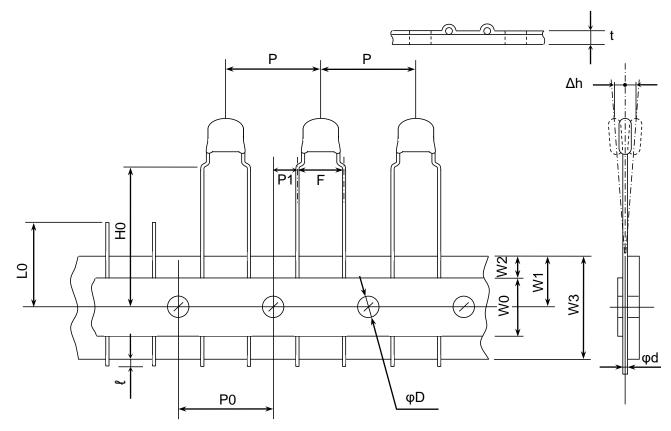


		(Unit : mm)
Symbol	Dimensions	Note
Р	(12.7)	
P0	(12.7)	
P1	( 5.1)	
W0	12.0±1.0	
W1	9.0±0.5	
W2	3.0 max.	Adhesive tape shall not stick out from carrier tape.
W3	18.0+1.0,-0.5	
H0	16.0±0.8	
l	1.0 max.	
t	0.6±0.2	
LO	11.0 max.	
F	2.5+0.5,-0.2	The measurement point is 1.5 to 2.0mm below the kink.
φd	φ0.5+0.1,-0.03	
φD	(φ4.0)	
Δh	(±2)	

) Reference value.

## **Appendix 3**

# **Taping dimensions** (FG28,FG24,FG26,FG20,FG22,FG23)



(Unit: mm)

Symbol	Dimensions	Note
Р	(12.7)	
P0	(12.7)	
P1	( 3.85)	
W0	12.0±1.0	
W1	9.0±0.5	
W2	3.0 max.	Adhesive tape shall not stick out from carrier tape.
W3	18.0+1.0,-0.5	
H0	16.0±0.8	
l	1.0 max.	
t	0.6±0.2	
L0	11.0 max.	
F	5.0+0.8,-0.2	The measurement point is 1.5 to 2.0mm below the kink.
φd	φ0.5+0.1,-0.03	
φD	(φ4.0)	
Δh	(±2)	

) Reference value.