

Product Preview

PP/655/12 April 19

CMX655D

Ultra-low Power Voice Codec

Direct connection to MEMS microphones and includes a 1W Class-D Speaker Driver

Features

- Digital MEMS microphone support
- High efficiency Class-D amplifier
- Low power consumption
- 16 bit audio data
- Supports conventional telephony and HD voice (300Hz - 3.4kHz and 50Hz -7kHz bandwidths)
- Supports audio bandwidths up to 21kHz
- Supports 8/16/32/48 ksps sample rates
- Flexible serial audio interface
- SPI™/TWI control interface
- Small 24-lead VQFN package
- 1.8V to 3.6V supply

Applications

- Security alarm panels
- Glass break detection
- Intercom and access systems
- Mobile radio and accessories
- Wired telephony
- Voice controlled equipment

Key Values

- Ultra-low operating power
 Typ. 300μA in listening mode
- Dual matched microphone channels
- Direct connection to MEMS microphones
- High efficiency 1W speaker driver

Traditionally voice codecs have interfaced to electret microphones and speakers providing A to D and D to A functions using precision oversampling data converters. Recent advances in microphone design using MEMS techniques are now changing this, along with higher efficiency speaker driver architectures such as Class D topologies. Both of these advances enable significant reductions in power consumption which is needed to address new applications such as voice control, that require always-on operation. Such applications that are often battery powered, driving the need for ultra-low power consumption.

The CMX655D addresses these needs providing an update to the traditional voice codec that is both very low power and small in size. Microphone interfaces connect single or dual MEMS microphones to the device, with parallel processing of each data stream to maintain phase alignment between the two channels.

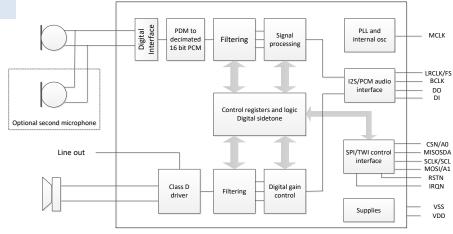
In the reverse channel digital voice data drives a class D amplifier, also featuring programmable filtering and digital gain control. This architecture operates with much higher efficiency than conventional speaker drivers. An additional low level output is also provided.

The device interfaces via standard serial busses that are commonly found on many microcontrollers, DSPs and low cost single chip radio transceivers.

The CMX655D is available in a small VQFN-24 package and supported by a range of development tools including the EV6550D evaluation kit.



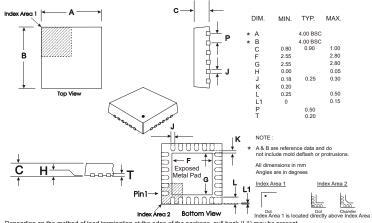
24-VQFN (4mm x 4mm)



Simplified Block Diagram

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Package SCLK/SCL CSN/A0 AOUTN IRQN VDD_PA SPIS LOUT 16 BCLK CMX655D SDO VSS A 15 VDD_A 5 SDI 14 DGND⁴ BIAS 13 L LRCLK/FS RSTN MICCLK RCLK VQFN (Q6) *Exposed Paddle



Depending on the method of lead termination at the edge of the package, pull back (L1) may be present.

L minus L1 to be equal to, or greater than 0.3mm

The underside of the package has an exposed metal pad which should ideally be soldered to the pcb to enhance the thermal conductivity and mechanical strength of the package fixing. Where advised, an electrical connection to this metal pad may also be required

Evaluation Support - EV6550D

- Demonstrates the CMX655D
- Sockets to support Digital MEMS microphone evaluation boards
- Direct Class-D connections for full evaluation of Class-D
- On board 24.576MHz clock
- On-board supply regulators operate from a single 5 volt supply
- Host Port allows for quick setup with PE0003
- Optional pull-ups and address selection for TWI

Ordering information:

- CMX655DQ6 Ultra-low Power Voice Codec
- EV6550D CMX655D Evaluation Kit



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www.cmlmicro.com

United Kingdom United States Singapore

Tel: +44 (0) 1621 875500 Tel: +1 336 744 5050 Tel: +65 62888129

email: sales@cmlmicro.com email: us.sales@cmlmicro.com email: sg.sales@cmlmicro.com