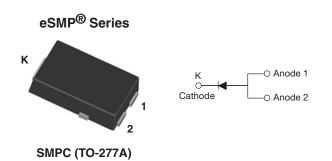
Vishay Semiconductors

Hyperfast Rectifier, 4 A FRED Pt<sup>®</sup>



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## LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	4 A				
V <sub>R</sub>	100 V				
V <sub>F</sub> at I <sub>F</sub>	0.73 V				
t <sub>rr</sub> (typ.)	27 ns				
T <sub>J</sub> max.	175 °C				
Package	SMPC (TO-277A)				
Circuit configuration	Single				

### FEATURES

Hyperfast recovery time, reduced Q<sub>rr</sub>, and soft recovery



HALOGEN

FREE

- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **DESCRIPTION / APPLICATIONS**

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

### **MECHANICAL DATA**

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS compliant

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS			
Peak repetitive reverse voltage	V <sub>RRM</sub>		100	V			
Average rectified forward current	I <sub>F(AV)</sub>	T <sub>Sp</sub> = 165 °C	4	٨			
Non-repetitive peak surge current	I <sub>FSM</sub>	$T_J = 25 \ ^{\circ}C$	130	A			
Operating junction and storage temperatures	T <sub>J</sub> , T <sub>Stg</sub>		-55 to +175	°C			

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 $^{\circ}$ C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	$V_{BR}, V_{R}$	I <sub>R</sub> = 100 μA	100	-	-		
Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 4 A	-	0.86	0.93	V	
Forward voltage		I <sub>F</sub> = 4 A, T <sub>J</sub> = 125 °C	-	0.73	0.79		
Reverse leakage current	1	$V_{R} = V_{R}$ rated	-	-	2		
neverse leakage current	IR	$T_J = 125 \ ^{\circ}C, V_R = V_R \text{ rated}$	-	1	10	μΑ	
Junction capacitance	CT	V <sub>R</sub> = 100 V	-	24	-	pF	

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<b>DYNAMIC RECOVERY CHARACTERISTICS</b> ( $T_J = 25$ °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	TEST CONDITIONS			MAX.	UNITS	
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 5$	$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, V_R = 30 \text{ V}$		27	-		
Reverse recovery time	+	I <sub>F</sub> = 0.5 A, I <sub>R</sub> = 1 A	λ, I <sub>rr</sub> = 0.25 Α	-	-	25	ns	
neverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	20	-		
		T <sub>J</sub> = 125 °C		-	31	-		
Deels receiver seurrent		T <sub>J</sub> = 25 °C	I <sub>F</sub> = 4 A dI <sub>F</sub> /dt = 200 A/μs V <sub>B</sub> = 160 V	-	2.2	-		
Peak recovery current	IRRM	T <sub>J</sub> = 125 °C		-	4.4	-	A	
	0	T <sub>J</sub> = 25 °C		-	22	-	5	
Reverse recovery charge	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	70	-	nC	

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		-55	-	175	°C	
Thermal resistance, junction to mount	R <sub>thJM</sub>		-	2.2	3	°C/W	
Thermal resistance, junction to ambient	R <sub>thJA</sub>		-	85	-	0/10	
Approximate weight				0.1		g	
Approximate weight				0.0035		oz.	
Marking device		Case style SMPC (TO-277A)		JE	H1		



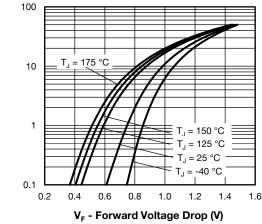


Fig. 1 - Typical Forward Voltage Drop Characteristics

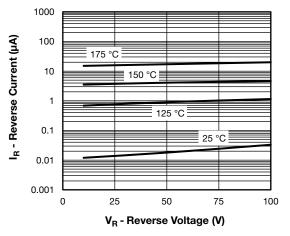
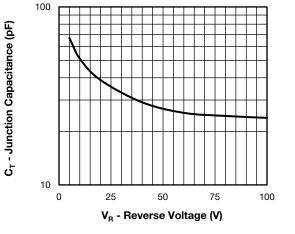


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

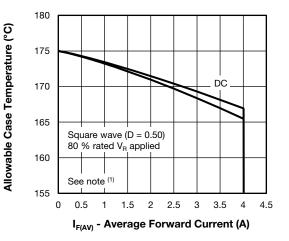


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

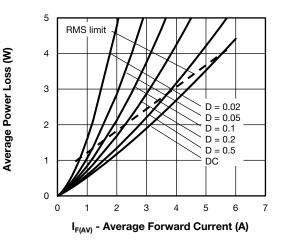


Fig. 5 - Forward Power Loss Characteristics

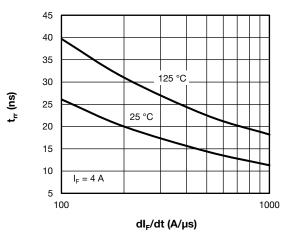


Fig. 6 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt

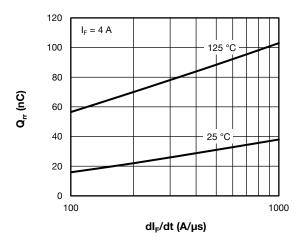


Fig. 7 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, \mathsf{x} \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ 5); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, \mathsf{x} \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$ 

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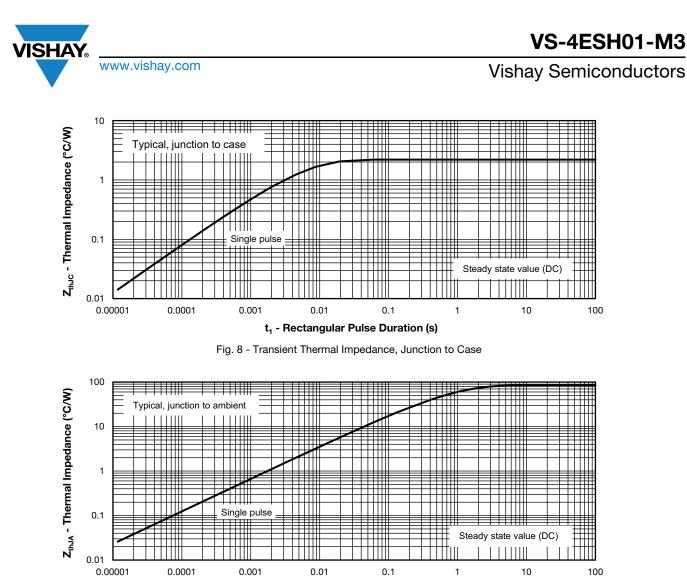
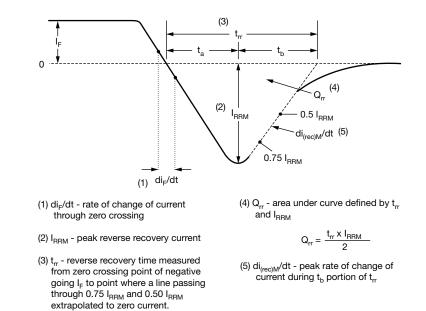
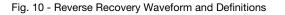




Fig. 9 - Transient Thermal Impedance, Junction to Ambient





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### **ORDERING INFORMATION TABLE**

Device code	VS-	4	E	s	н	01	-МЗ
		2	3	4	5	6	7
	1		hay Sen			oduct	
	2		rent rati cuit conf	•			
		E =	single o	liode			
	4	- S=	SMPC	package	9		
	5	- Pro	cess typ	e,			
		H =	hyperfa	st recov	very		
	6	- Vol	tage coo	de (01 =	100 V)		
	7	M3	8 = halog	gen-free	, RoHS-	complia	ant, and

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-4ESH01-M3/86A	1500	1500	7" diameter plastic tape and reel				
VS-4ESH01-M3/87A	6500	6500	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95570			
Part marking information	www.vishay.com/doc?95565			
Packaging information	www.vishay.com/doc?88869			
SPICE model	www.vishay.com/doc?96073			

# **Outline Dimensions**





SMPC (TO-277A)

#### **DIMENSIONS** in inches (millimeters)





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