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Vishay Siliconix

Automotive N-Channel 40 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	40			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 10 \text{ V}$	0.009			
$R_{DS(on)}(\Omega)$ at $V_{GS} = 4.5 \text{ V}$	0.012			
I _D (A)	20.7			
Configuration	Single			

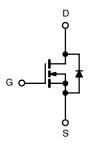
FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % Rg and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





ROHS COMPLIANT HALOGEN FREE



N-Channel MOSFET

ORDERING INFORMATION	
Package	SO-8
Lead (Pb)-free and halogen-free	SQ4840CEY (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATING	S ($T_C = 25$ °C, unles	s otherwise noted	d)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V_{DS}	40		
Gate-source voltage		V _{GS}	± 20	V	
Continuous drain current	T _C = 25 °C	- I _D	20.7		
	T _C = 125 °C		12		
Continuous source current (diode conduction)		I _S	6.5	А	
Pulsed drain current ^a		I _{DM}	82		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	30		
Single pulse avalanche energy	L = 0.1 MH	E _{AS}	45	mJ	
Maximum power dissipation	T _C = 25 °C	D	7.1	W	
	T _C = 125 °C	- P _D	2.4		
Operating junction and storage temperature	range	T _J , T _{stg}	-55 to + 175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount b	R_{thJA}	85	°C/W	
Junction-to-case (drain)		R_{thJF}	21		

Notes

- a. Pulse test; pulse width $\leq 300~\mu\text{s},$ duty cycle $\leq 2~\%$
- b. When mounted on 1" square PCB (FR-4 material)



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PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V_{DS}	$V_{GS}=0$, $I_D=250~\mu A$		40	-	-	V
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_D = 250 \mu A$		1.5	2.0	2.5	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	-	± 100	nA
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = 40 V -		-	1.0	
		V _{GS} = 0 V	V _{DS} = 40 V, T _J = 125 °C	-	=	50	μA
		V _{GS} = 0 V	V _{DS} = 40 V, T _J = 175 °C	-	-	150	
On-state drain current a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	30	-	-	Α
Drain-source on-state resistance ^a		V _{GS} = 10 V	I _D = 14 A	-	0.0062	0.009	
	Б	V _{GS} = 10 V	I _D = 14 A, T _C = 125 °C	-	-	0.014	Ω
	$R_{DS(on)}$	V _{GS} = 10 V	I _D = 14 A, T _C = 175 °C	-	-	0.018	
		V _{GS} = 4.5 V	I _D = 12 A	-	0.0090	0.012	
Forward transconductance b	9 _{fs}	V _{DS}	= 15 V, I _D = 14 A	-	50	-	S
Dynamic ^b				•			
Input capacitance	C _{iss}	V _{GS} = 0 V	V _{DS} = 20 V, f = 1 MHz	-	2028	2440	pF
Output capacitance	C _{oss}			-	493	630	
Reverse transfer capacitance	C _{rss}			-	263	290	
Total gate charge c	Qg		V _{DS} = 20 V, I _D = 14 A	-	44.8	62	nC
Gate-source charge c	Q _{gs}	V _{GS} = 10 V		-	6.3	-	
Gate-drain charge ^c	Q _{gd}			-	11.8	-	
Gate resistance	Rg	f = 1 MHz		0.2	0.45	1.6	Ω
Turn-on delay time ^c	t _{d(on)}			-	12	21	
Rise time ^c	t _r	$V_{DD} = 20 \text{ V, } R_L = 20 \Omega$ $I_D \cong 1 \text{ A, } V_{GEN} = 10 \text{ V, } R_g = 6 \Omega$		-	9	17	ns
Turn-off delay time ^c	t _{d(off)}			-	51	68	
Fall time ^c	t _f			-	25	28	
Source-Drain Diode Ratings and Charac	teristicsb			•			
Pulsed current ^a	I _{SM}			-	-	82	Α
Forward voltage	V _{SD}	I _F = 2.8 A, V _{GS} = 0		-	0.71	1.1	V
Body diode reverse recovery time	t _{rr}	I _F = 2.8 A, di/dt = 100 A/μs		-	24	48	ns
Body diode reverse recovery charge	Q_{rr}			-	20	40	nC
Reverse recovery fall time	ta			-	14	-	ns
Reverse recovery rise time	t _b			-	10	-	
Body diode peak reverse recovery current	I _{RM(REC)}			-	-1.6	-	Α

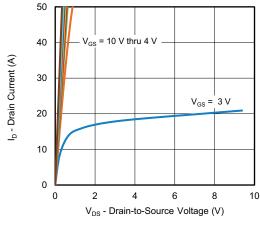
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

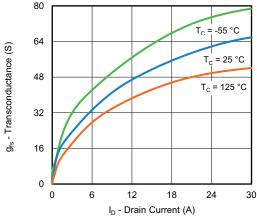
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



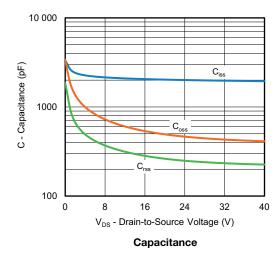
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)

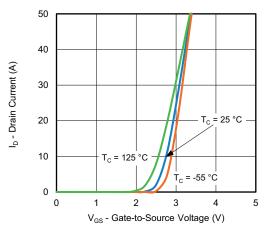


Output Characteristics

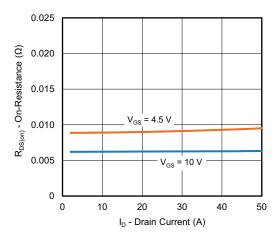


Transconductance

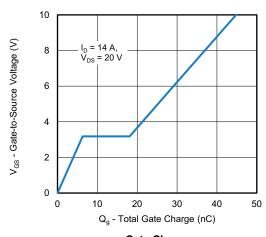




Transfer Characteristics

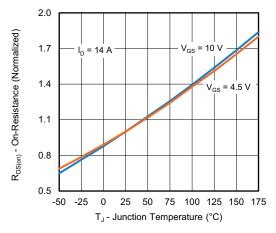


On-Resistance vs. Drain Current

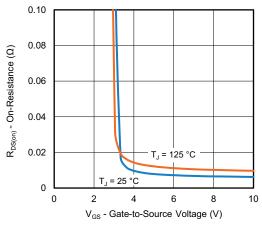




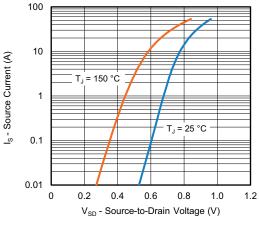
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



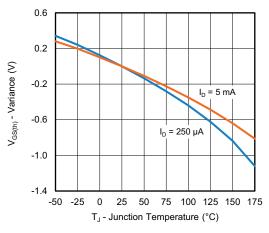
On-Resistance vs. Junction Temperature



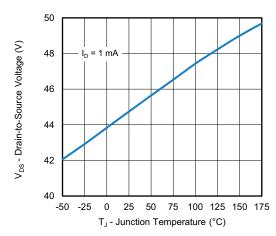
On-Resistance vs. Gate-to-Source Voltage



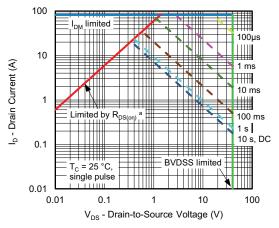
Source Drain Diode Forward Voltage



Threshold Voltage



Drain Source Breakdown vs. Junction Temperature



Safe Operating Area

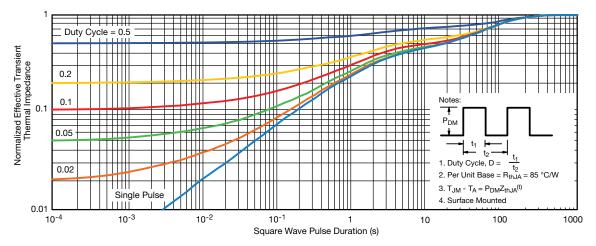
Note

a. V_{GS} > minimum V_{GS} at which $R_{DS(on)}$ is specified

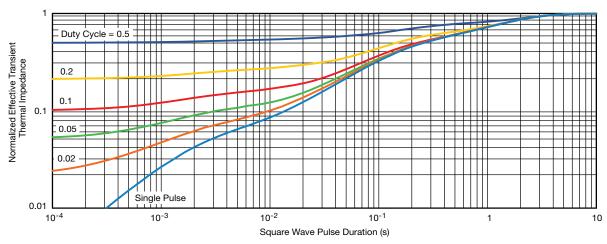
For technical questions, contact: automostechsu



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62017.



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