# **EC59** Series



# REGULATORY COMPLIANCE

Lead Free	EU RoHS	<b>China RoHS</b>	REACH
$\bigotimes$	2011/65 + 2015/863	e	ѕѵнс
COMPLIANT	COMPLIANT	COMPLIANT	COMPLIANT

#### DRC CONFLICT FREE

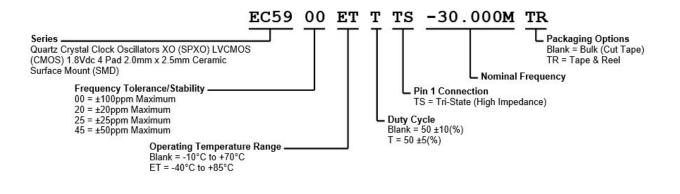
#### **ITEM DESCRIPTION**

Quartz Crystal Clock Oscillators XO (SPXO) LVCMOS (CMOS) 1.8Vdc 4 Pad 2.0mm x 2.5mm Ceramic Surface Mount (SMD)

ELECTRICAL SPECIFICATIONS		
Nominal Frequency	1MHz to 50.625MHz	
Frequency Tolerance/Stability	Inclusive of all conditions: Calibration Tolerance at 25°C, Frequency Stability over the Operating Temperature Range, Supply Voltage Change, Output Load Change, First Year Aging at 25°C, Shock, and Vibration ±100ppm Maximum ±20ppm Maximum ±25ppm Maximum ±50ppm Maximum	
Operating Temperature Range	-10°C to +70°C -40°C to +85°C	
Supply Voltage	1.8Vdc ±5%	
Input Current	2.5mA Maximum over Nominal Frequency of 1MHz to 29.999999MHz 3mA Maximum over Nominal Frequency of 30MHz to 39.999999MHz 3.5mA Maximum over Nominal Frequency of 40MHz to 50.625MHz	
Output Voltage Logic High (V <sub>OH</sub> )	IOH = -4mA 90% of Vdd Minimum	
Output Voltage Logic Low (V₀∟)	IOL = +4mA 10% of Vdd Maximum	
Rise/Fall Time	Measured at 20% to 80% of waveform 10nSec Maximum	
Duty Cycle	Measured at 50% of waveform 50 ±10(%) 50 ±5(%)	
Load Drive Capability	15pF Maximum	
Output Logic Type	CMOS	
Pin 1 Connection	Tri-State (High Impedance)	
Tri-State Input Voltage (Vih and Vil)	90% of Vdd Minimum or No Connect to Enable Output, 10% of Vdd Maximum to Disable Output (High Impedance)	
Standby Current	Disabled Output; High Impedance 10μΑ Maximum	
RMS Phase Jitter	12kHz to 20MHz offset frequency 1pSec Maximum	
Start Up Time	10mSec Maximum	
Storage Temperature Range	-55°C to +125°C	

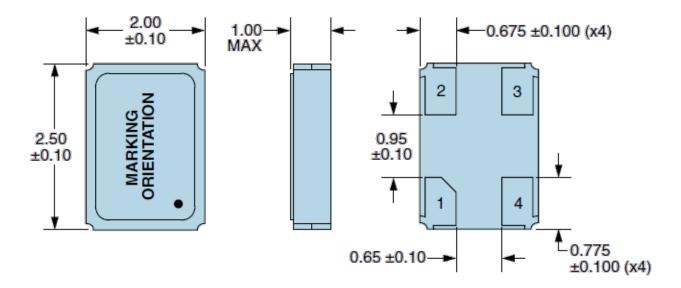


#### PART NUMBERING GUIDE

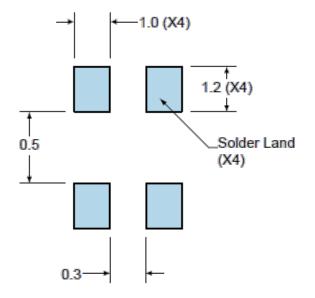




### **MECHANICAL DIMENSIONS**



#### SUGGESTED SOLDER PAD LAYOUT



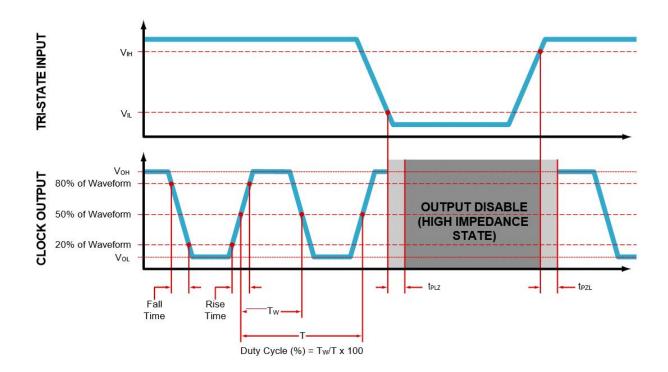
PIN	CONNECTION
1	Tri-State
2	Case/Ground
3	Output
4	Supply Voltage

All Tolerances are ±0.1

**All Dimensions in Millimeters** 

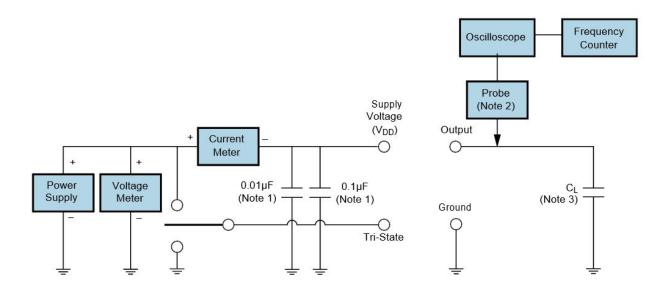


# OUTPUT WAVEFORM & TIMING DIAGRAM





## **TEST CIRCUIT FOR CMOS OUTPUT**



- Note 1: An external 0.01µF ceramic bypass capacitor in parallel with a 0.1µF high frequency ceramic bypass capacitor close (less
- Than 2mm) to the package ground and supply voltage pin is required. **Note 2:** A low capacitance (<12pF), 10X Attenuation Factor, High Impedance (>10Mohms), and High bandwidth (>300MHz) Passive probe is recommended.
- Note 3: Capacitance value CL includes sum of all probe and fixture capacitance.

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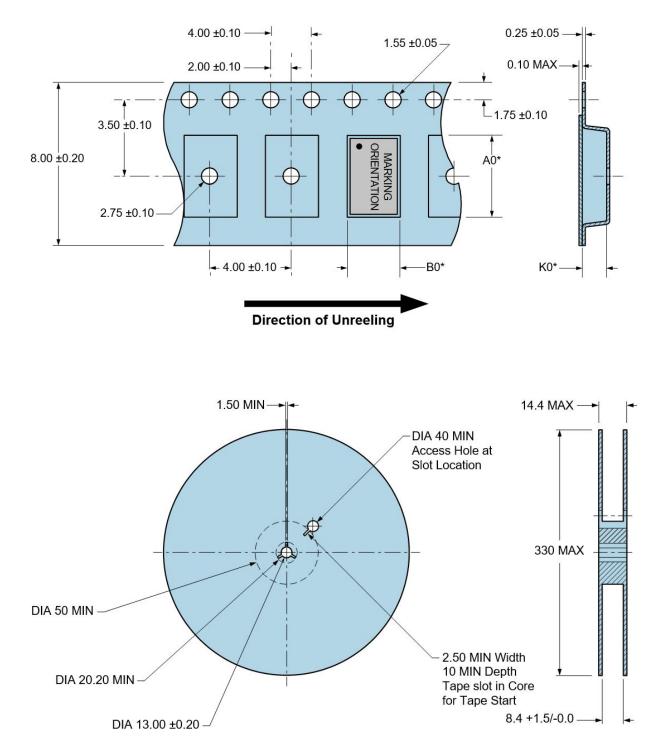


### **TAPE & REEL DIMENSIONS**

Quantity per Reel: 1,000 Units

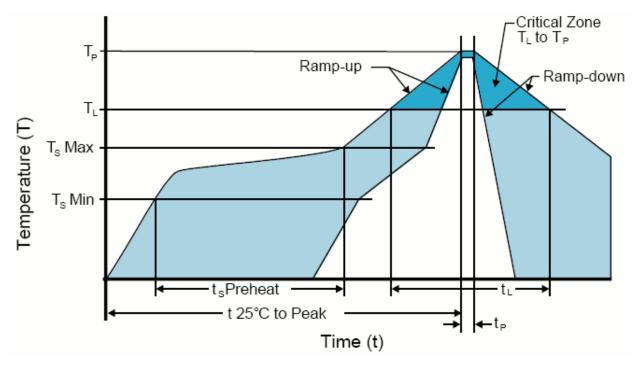
All Dimensions in Millimeters

Compliant to EIA-481





### **RECOMMENDED SOLDER REFLOW METHOD**



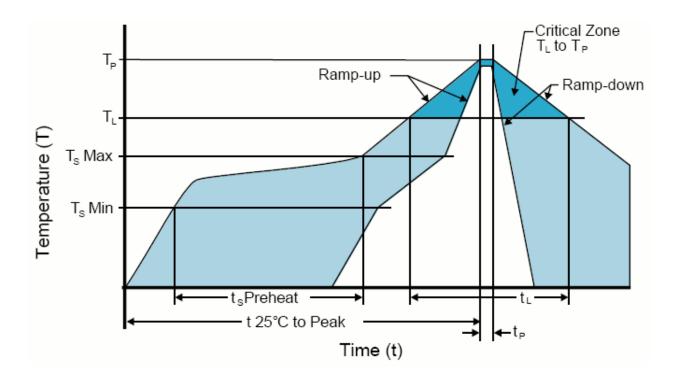
HIGH TEMPERATURE INFRARED/CONVECTION		
$T_s$ MAX to $T_L$ (Ramp-up Rate)	3°C/Second Maximum	
Preheat		
- Temperature Minimum (Ts MIN)	150°C	
- Temperature Typical (T <sub>s</sub> TYP)	175°C	
- Temperature Maximum(T <sub>s</sub> MAX)	200°C	
- Time (t <sub>s</sub> )	60 - 180 Seconds	
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	3°C/Second Maximum	
Time Maintained Above:		
- Temperature (T <sub>L</sub> )	217°C	
- Time (t∟)	60 - 150 Seconds	
Peak Temperature (T <sub>P</sub> )	260°C Maximum for 10 Seconds Maximum	
Target Peak Temperature(T <sub>P</sub> Target)	250°C +0/-5°C	
Time within 5°C of actual peak $(t_p)$	20 - 40 Seconds	
Ramp-down Rate	6°C/Second Maximum	
Time 25°C to Peak Temperature (t)	8 Minutes Maximum	
Moisture Sensitivity Level	Level 1	
Additional Notes	Temperatures shown are applied to body of device.	

#### High Temperature Manual Soldering

260°C Maximum for 5 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)



# **RECOMMENDED SOLDER REFLOW METHOD**



LOW TEMPERATURE INFRARED/CONVECTION		
$T_s$ MAX to $T_L$ (Ramp-up Rate)	5°C/Second Maximum	
Preheat		
- Temperature Minimum (Ts MIN)	N/A	
- Temperature Typical (T <sub>s</sub> TYP)	150°C	
- Temperature Maximum(T <sub>s</sub> MAX)	N/A	
- Time (t <sub>s</sub> )	60 - 120 Seconds	
Ramp-up Rate (T <sub>L</sub> to T <sub>P</sub> )	5°C/Second Maximum	
Time Maintained Above:		
- Temperature (T∟)	150°C	
- Time (t∟)	200 Seconds Maximum	
Peak Temperature (T <sub>P</sub> )	240°C Maximum	
Target Peak Temperature(T <sub>P</sub> Target)	240°C Maximum 2 Times/230°C Maximum 1Time	
Time within 5°C of actual peak (t <sub>P</sub> )	10 Seconds Maximum 2 Times / 80 Seconds Maximum 1 Time	
Ramp-down Rate	5°C/Second Maximum	
Time 25°C to Peak Temperature (t)	N/A	
Moisture Sensitivity Level	Level 1	
Additional Notes	Temperatures shown are applied to body of device.	

#### Low Temperature Manual Soldering

185°C Maximum for 10 Seconds Maximum, 2 times Maximum. (Temperatures shown are applied to body of device.)