U_FPGA-MGT
FPGA-MGT.SchDoc

U_PWR1
PWR1.SchDoc

U_PWR2
PWR2.SchDoc

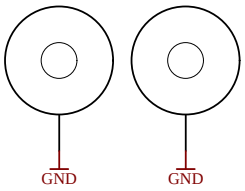
U_Clock
Clock.SchDoc

U_B2B-Connectors
B2B-Connectors.SchDoc

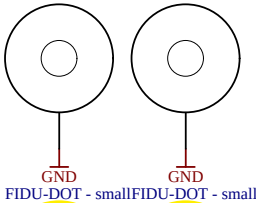
U_FPGA-MISC
FPGA-MISC.SchDoc

U_FPGA-PWR
FPGA-PWR.SchDoc

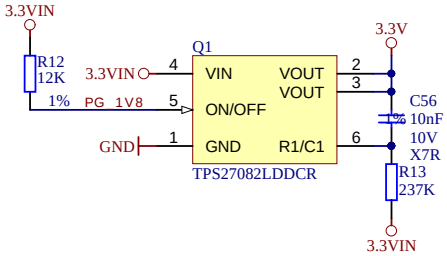
Mount.Hole 3.2mm
Mount.Hole 3.2mm



Mount.Hole 3.2mm
Mount.Hole 3.2mm

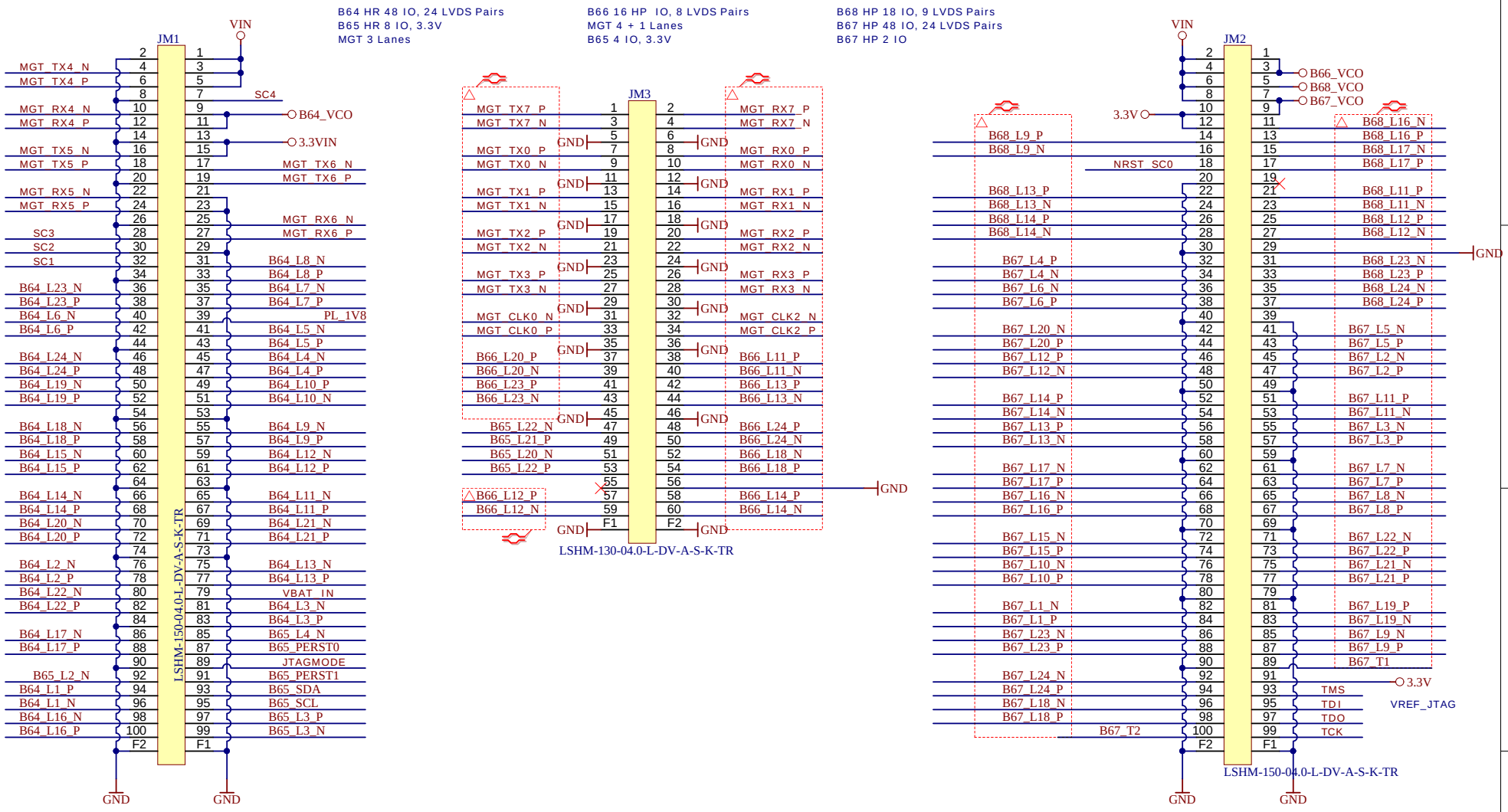


FIDU-DOT - small
FIDU-DOT - small
PM1
PM2
FIDU-DOT - small
FIDU-DOT - small
PM3
PM4
FIDU-DOT - small
FIDU-DOT - small
PM5
PM6



Top of Board

trenz electronic	Title: TE0841		
	A4	Number: TE0841 TE0841-01-035-1I	Rev. 01
	Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT	Page1 of 22
	Filename: TE0841.SchDoc		



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Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT	Page2 of 22
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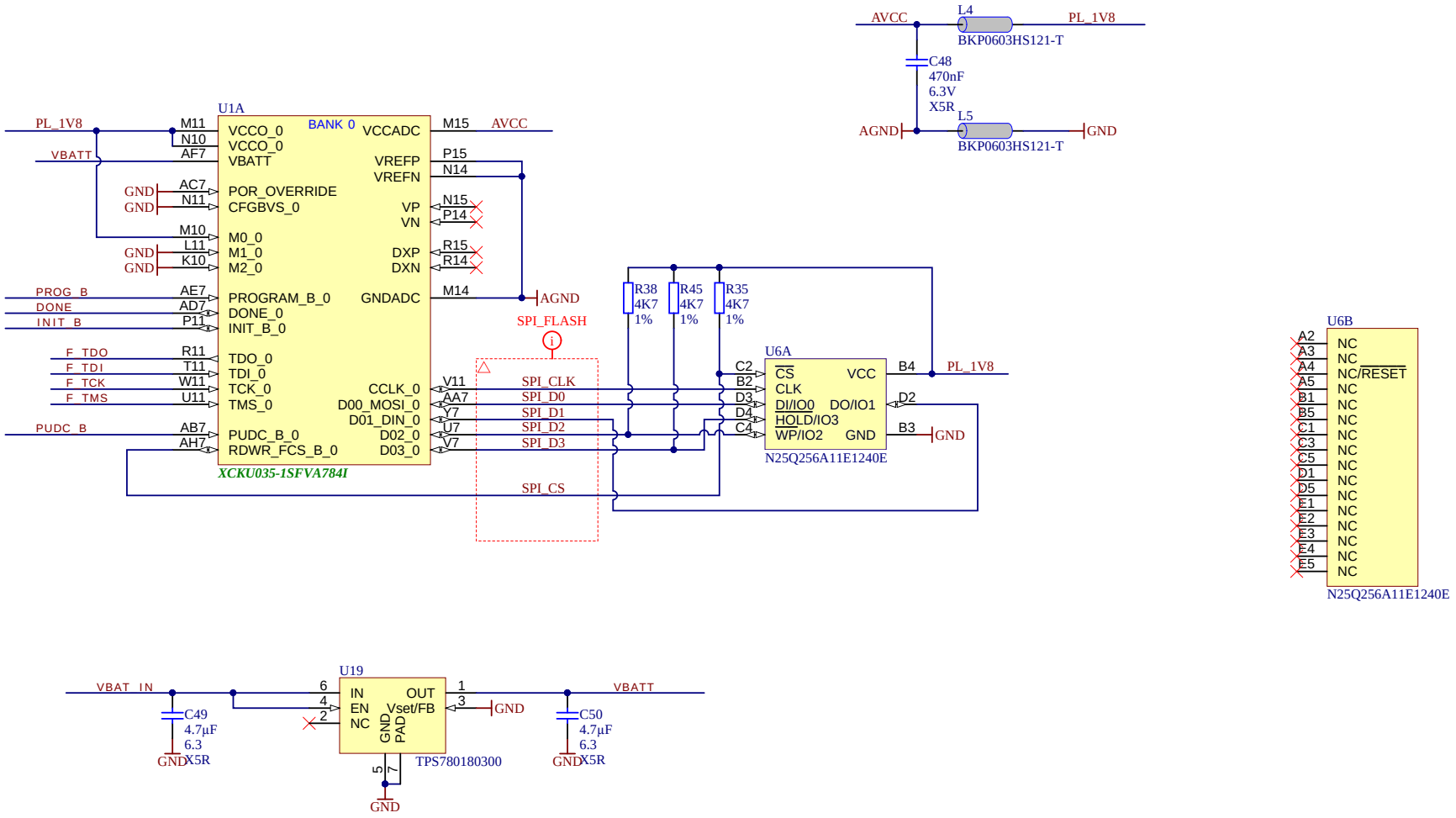

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	Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT	Page3 of 22
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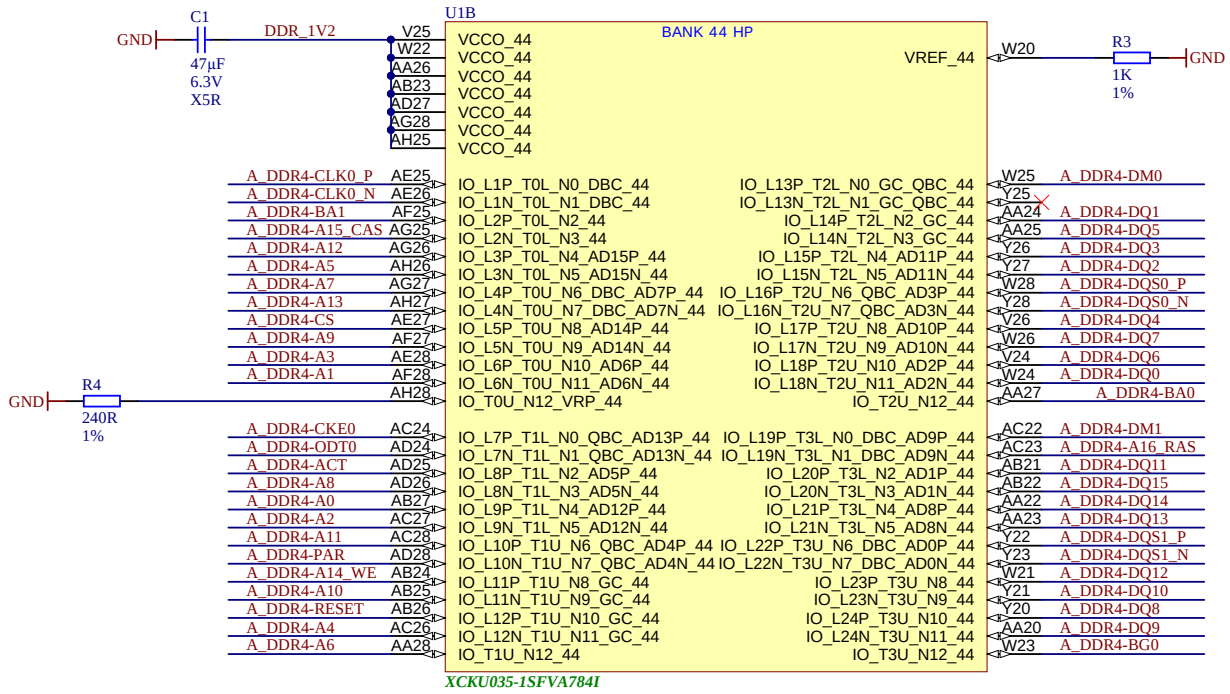

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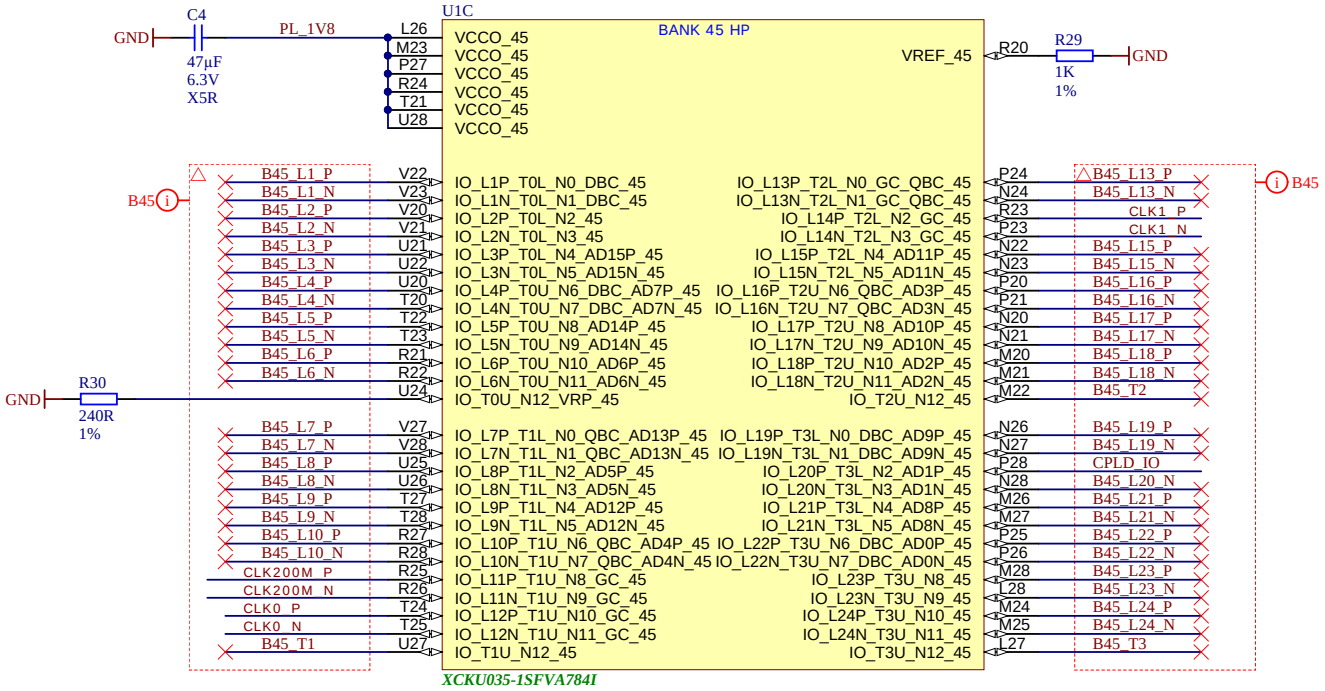
Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT		Page4 of 22
Filename: FPGA-B44.SchDoc			



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	Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT	Page5 of 22
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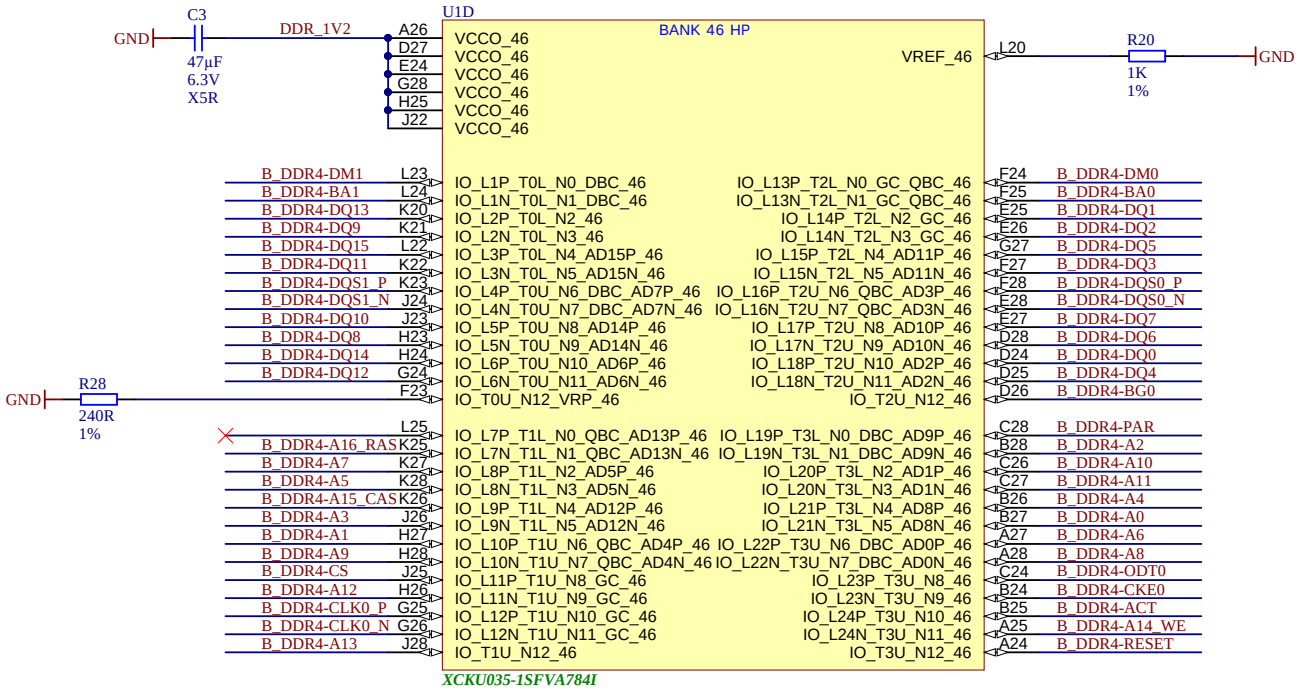

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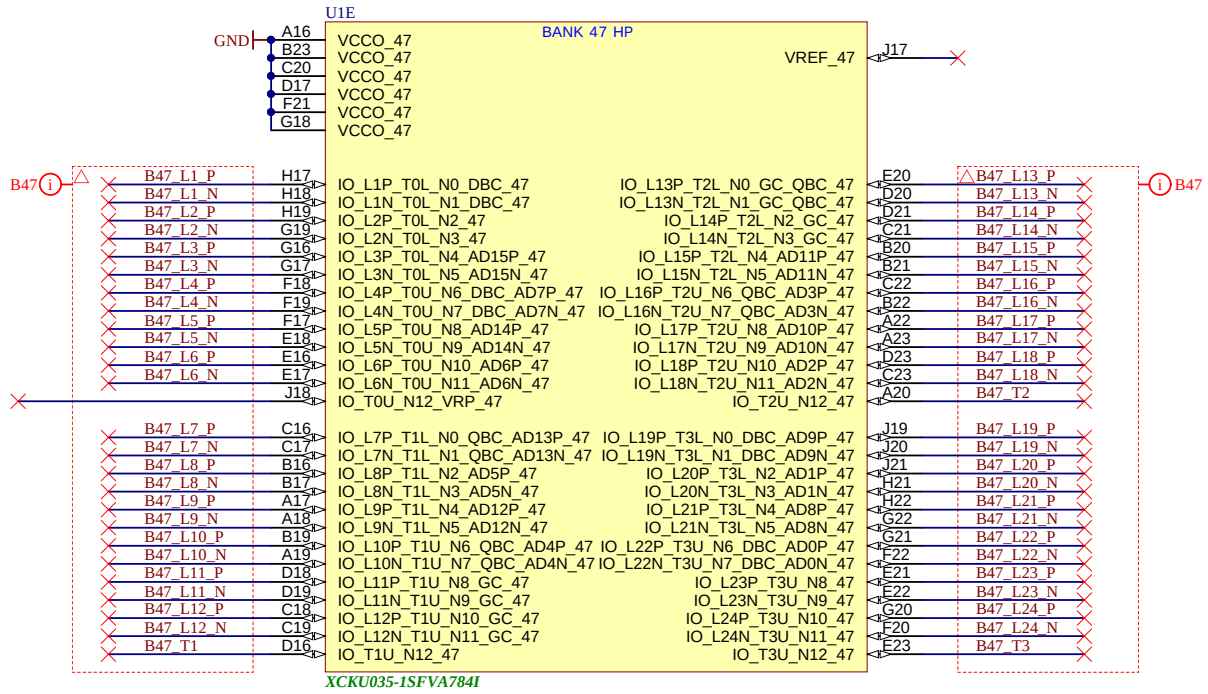
	Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT	Page6 of 22
	Filename: FPGA-B46.SchDoc		



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	Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT	Page7 of 22
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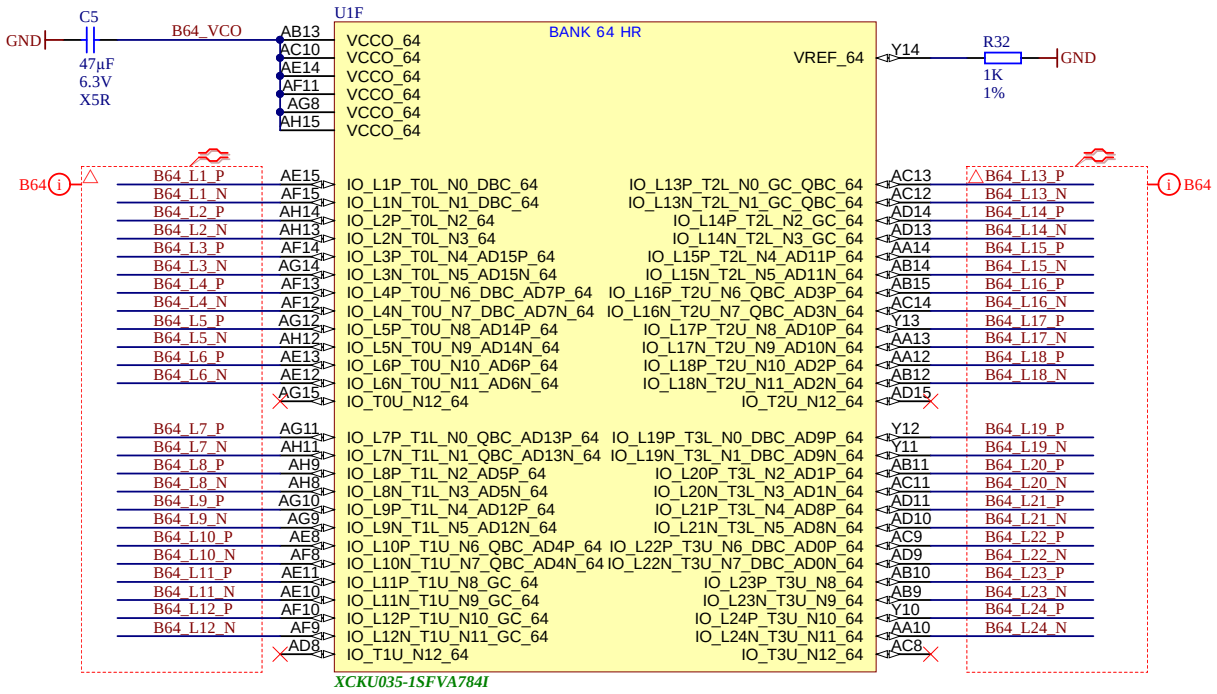

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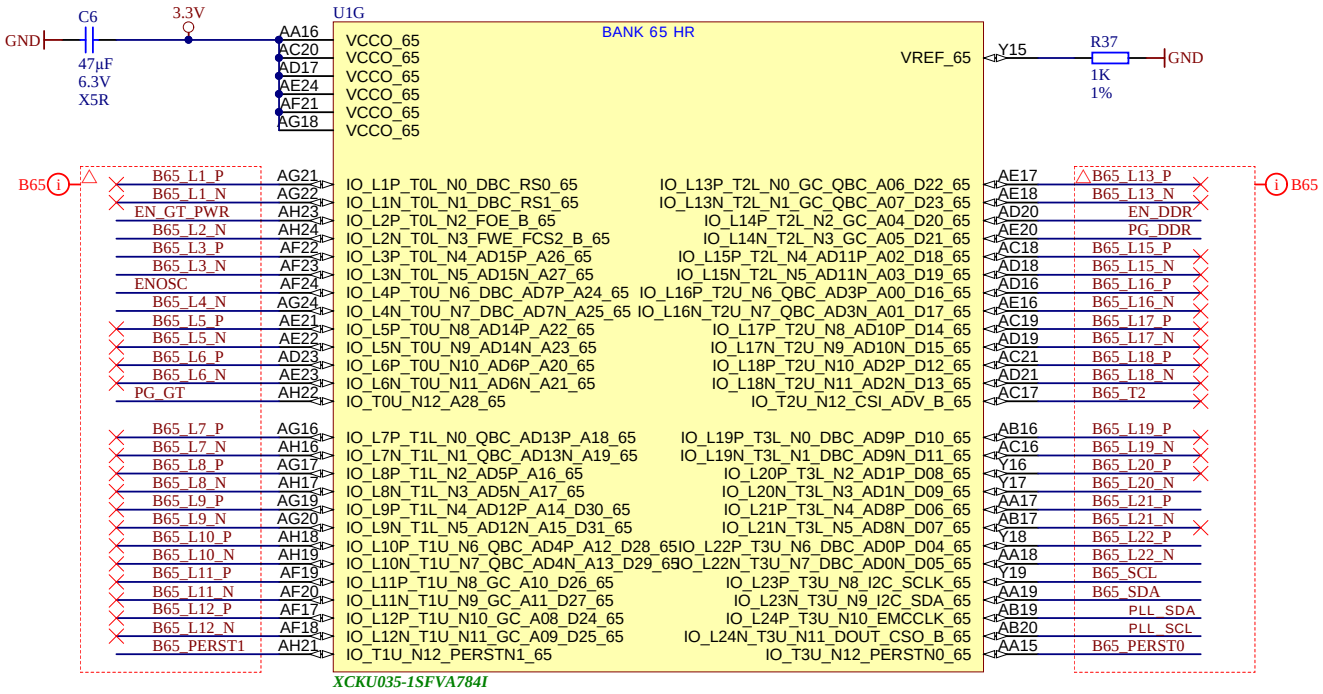

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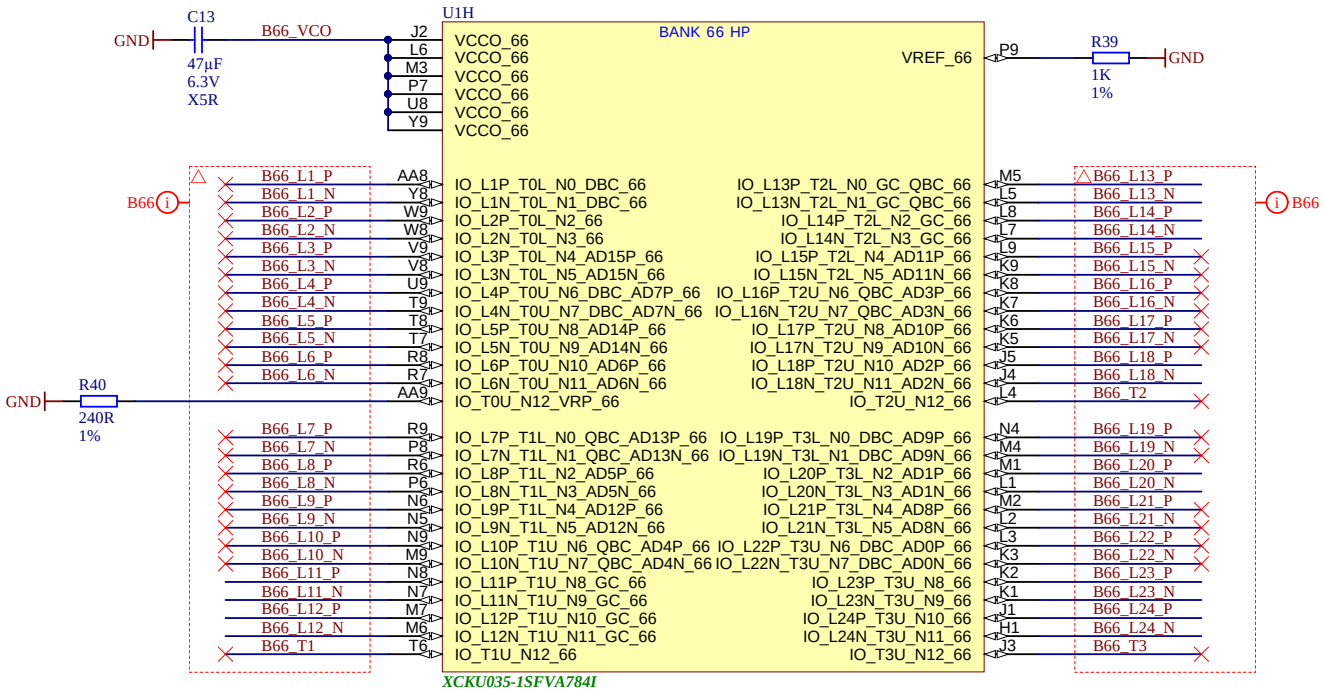
		AA16	VCCO_65				
		AC20	VCCO_65				
		AD17	VCCO_65				
		AE24	VCCO_65				
		AF21	VCCO_65				
		AG18	VCCO_65				
B65_L1_P	AG21	IO_L1P_T0L_N0_DBC_RS0_65	IO_L13P_T2L_N0_GC_QBC_A06_D22_65	AE17	B65_L13_P		
B65_L1_N	AG22	IO_L1N_T0L_N1_DBC_RS1_65	IO_L13N_T2L_N1_GC_QBC_A07_D23_65	AE18	B65_L13_N		
EN_GT_PWR	AH23	IO_L2P_T0L_N2_FOE_B_65	IO_L14P_T2L_N2_GC_A04_D20_65	AD20	EN_DDR		
B65_L2_N	AH24	IO_L2N_T0L_N3_FWE_FCS2_B_65	IO_L14N_T2L_N3_GC_A05_D21_65	AE20	PG_DDR		
B65_L3_P	AF22	IO_L3P_T0L_N4_AD15P_A26_65	IO_L15P_T2L_N4_AD11P_A02_D18_65	AC18	B65_L15_P		
B65_L3_N	AF23	IO_L3N_T0L_N5_AD15N_A27_65	IO_L15N_T2L_N5_AD11N_A03_D19_65	AD18	B65_L15_N		
ENOSC	AF24	IO_L4P_T0U_N6_DBC_AD7P_A24_65	IO_L16P_T2U_N6_QBC_AD3P_A00_D16_65	AD16	B65_L16_P		
B65_L4_N	AG24	IO_L4N_T0U_N7_DBC_AD7N_A25_65	IO_L16N_T2U_N7_QBC_AD3N_A01_D17_65	AE16	B65_L16_N		
B65_L5_P	AE21	IO_L5P_T0U_N8_AD14P_A22_65	IO_L17P_T2U_N8_AD10P_D14_65	AC19	B65_L17_P		
B65_L5_N	AE22	IO_L5N_T0U_N9_AD14N_A23_65	IO_L17N_T2U_N9_AD10N_D15_65	AD19	B65_L17_N		
B65_L6_P	AD23	IO_L6P_T0U_N10_AD6P_A20_65	IO_L18P_T2U_N10_AD2P_D12_65	AC21	B65_L18_P		
B65_L6_N	AE23	IO_L6N_T0U_N11_AD6N_A21_65	IO_L18N_T2U_N11_AD2N_D13_65	AD21	B65_L18_N		
PG_GT	AH22	IO_T0U_N12_A28_65	IO_T2U_N12_CSI_ADV_B_65	AC17	B65_T2		
B65_L7_P	AG16	IO_L7P_T1L_N0_QBC_AD13P_A18_65	IO_L19P_T3L_N0_DBC_AD9P_D10_65	AB16	B65_L19_P		
B65_L7_N	AH16	IO_L7N_T1L_N1_QBC_AD13N_A19_65	IO_L19N_T3L_N1_DBC_AD9N_D11_65	AC16	B65_L19_N		
B65_L8_P	AG17	IO_L8P_T1L_N2_AD5P_A16_65	IO_L20P_T3L_N2_AD1P_D08_65	Y16	B65_L20_P		
B65_L8_N	AH17	IO_L8N_T1L_N3_AD5N_A17_65	IO_L20N_T3L_N3_AD1N_D09_65	Y17	B65_L20_N		
B65_L9_P	AG19	IO_L9P_T1L_N4_AD12P_A14_D30_65	IO_L21P_T3L_N4_AD8P_D06_65	AA17	B65_L21_P		
B65_L9_N	AG20	IO_L9N_T1L_N5_AD12N_A15_D31_65	IO_L21N_T3L_N5_AD8N_D07_65	AB17	B65_L21_N		
B65_L10_P	AH18	IO_L10P_T1U_N6_QBC_AD4P_A12_D28_65	IO_L22P_T3U_N6_DBC_AD0P_D04_65	Y18	B65_L22_P		
B65_L10_N	AH19	IO_L10N_T1U_N7_QBC_AD4N_A13_D29_65	IO_L22N_T3U_N7_DBC_AD0N_D05_65	AA18	B65_L22_N		
B65_L11_P	AF19	IO_L11P_T1U_N8_GC_A10_D26_65	IO_L23P_T3U_N8_I2C_SCLK_65	Y19	B65_SCL		
B65_L11_N	AF20	IO_L11N_T1U_N9_GC_A11_D27_65	IO_L23N_T3U_N9_I2C_SDA_65	AA19	B65_SDA		
B65_L12_P	AF17	IO_L12P_T1U_N10_GC_A08_D24_65	IO_L24P_T3U_N10_EMCCLK_65	AB19	PLL_SDA		
B65_L12_N	AF18	IO_L12N_T1U_N11_GC_A09_D25_65	IO_L24N_T3U_N11_DOUT_CSO_B_65	AB20	PLL_SCL		
B65_PERST1	AH21	IO_T1U_N12_PERSTN1_65	IO_T3U_N12_PERSTN0_65	AA15	B65_PERST0		

Title: TE0841			
A4	Number: TE0841 TE0841-01-035-1I		Rev. 01
Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT		Page9 of 22
Filename: FPGA-B65.SchDoc			



trenz electronic	Title: TE0841		
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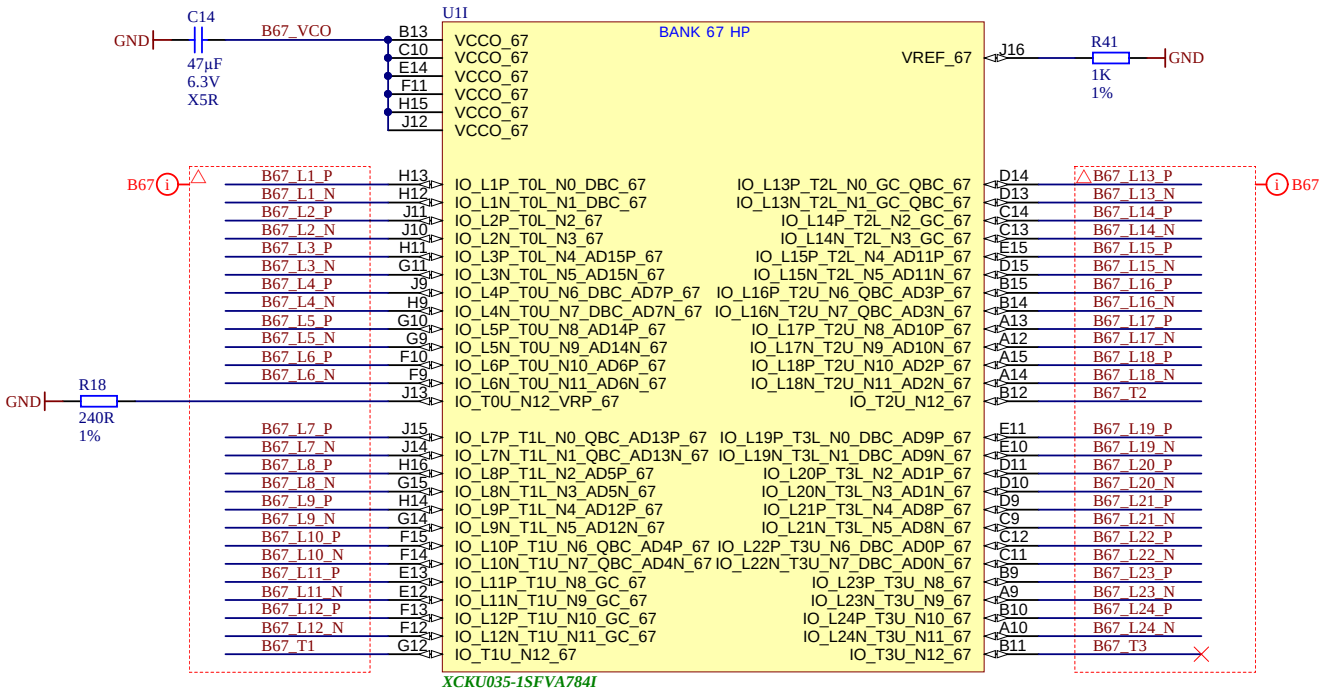
	Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT	Page10 of 22
	Filename: FPGA-B66.SchDoc		



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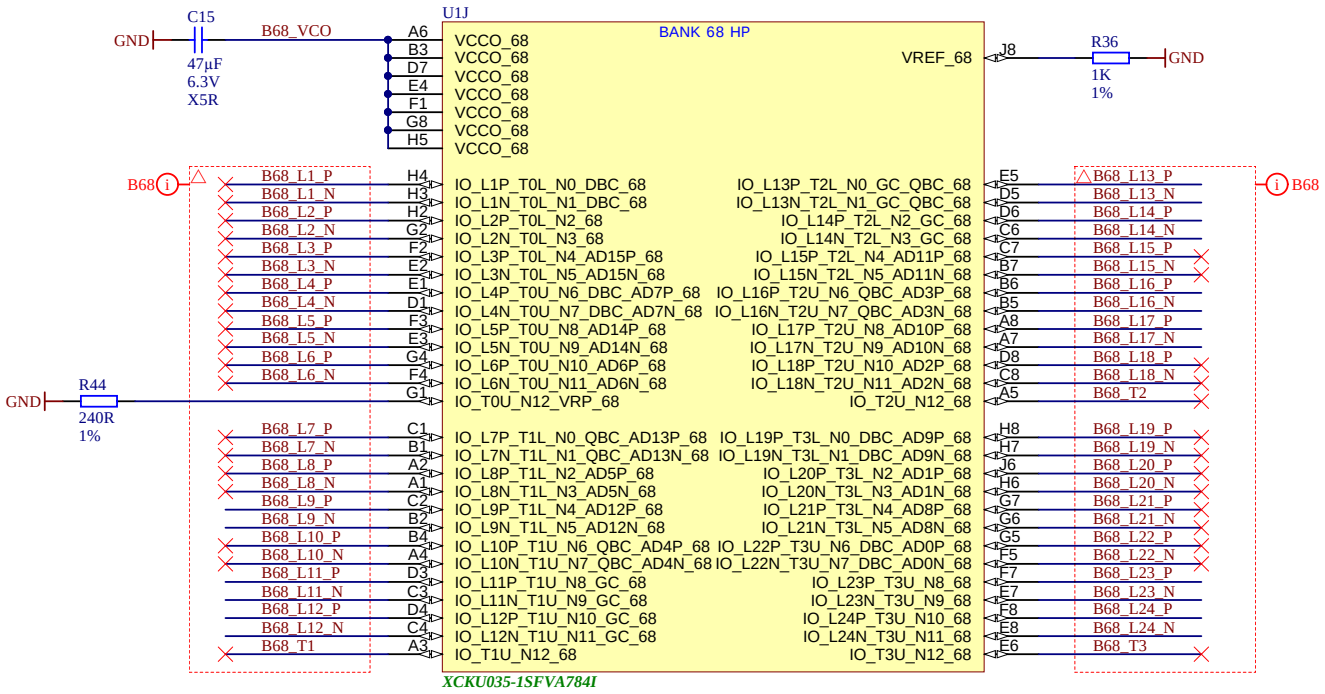
Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT		Page 11 of 22
Filename: FPGA-B67.SchDoc			



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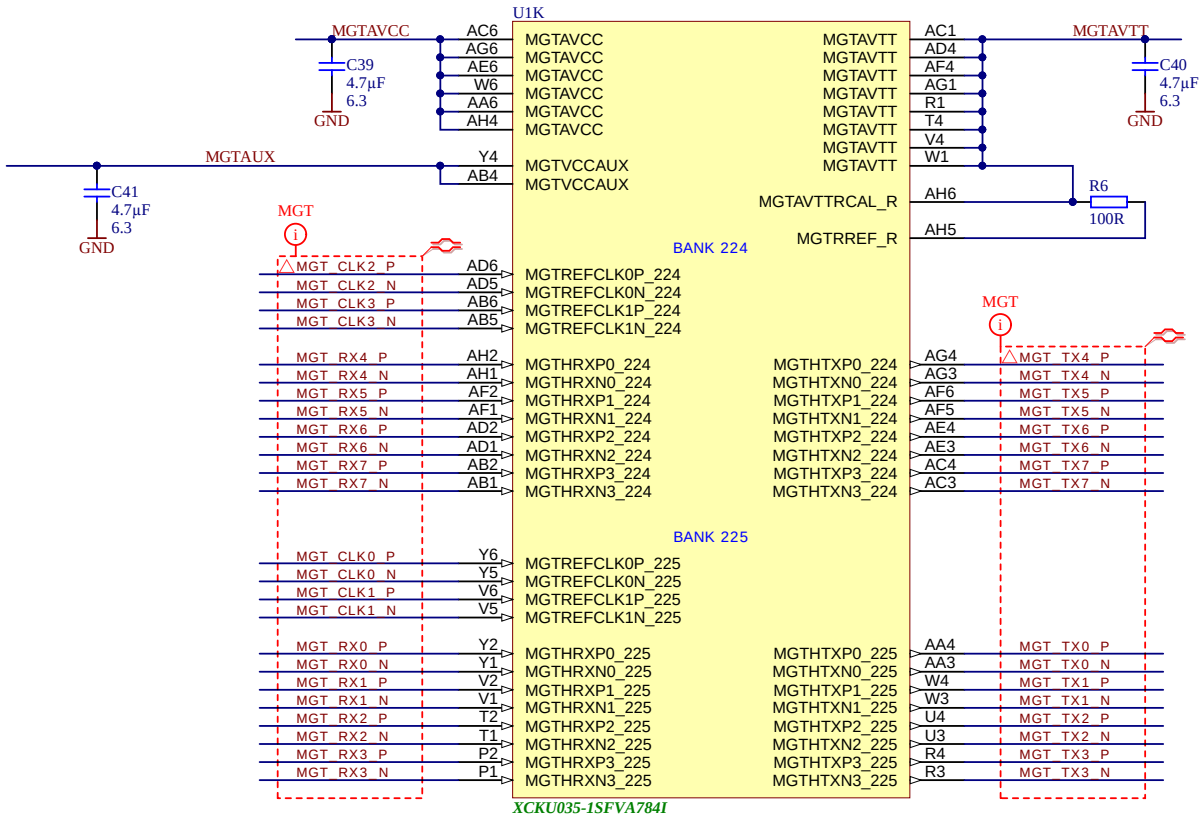
Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT		Page12 of 22
Filename: FPGA-B68.SchDoc			



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	Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT	Page13 of 22
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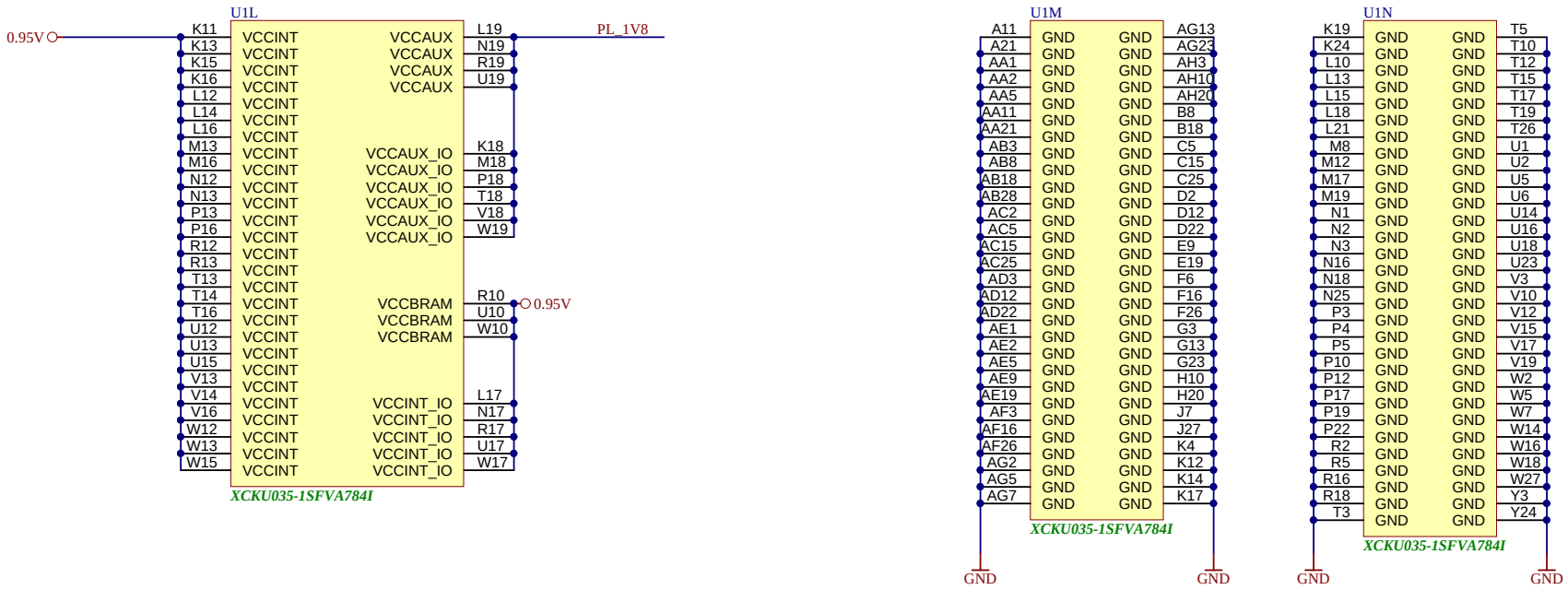

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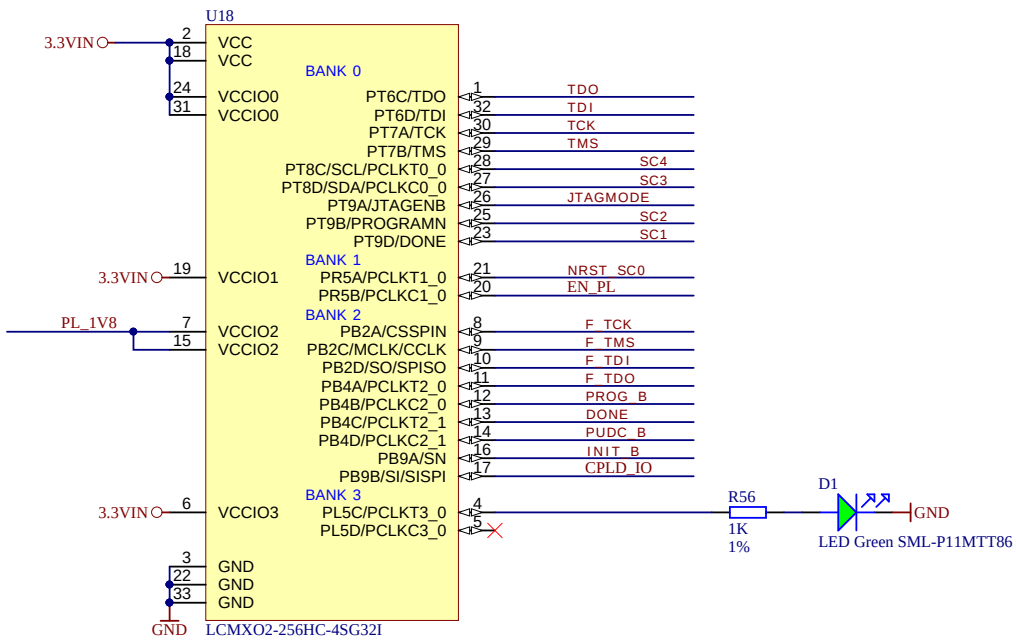
Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT		Page 14 of 22
Filename: FPGA-PWR.SchDoc			



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	Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT	Page15 of 22
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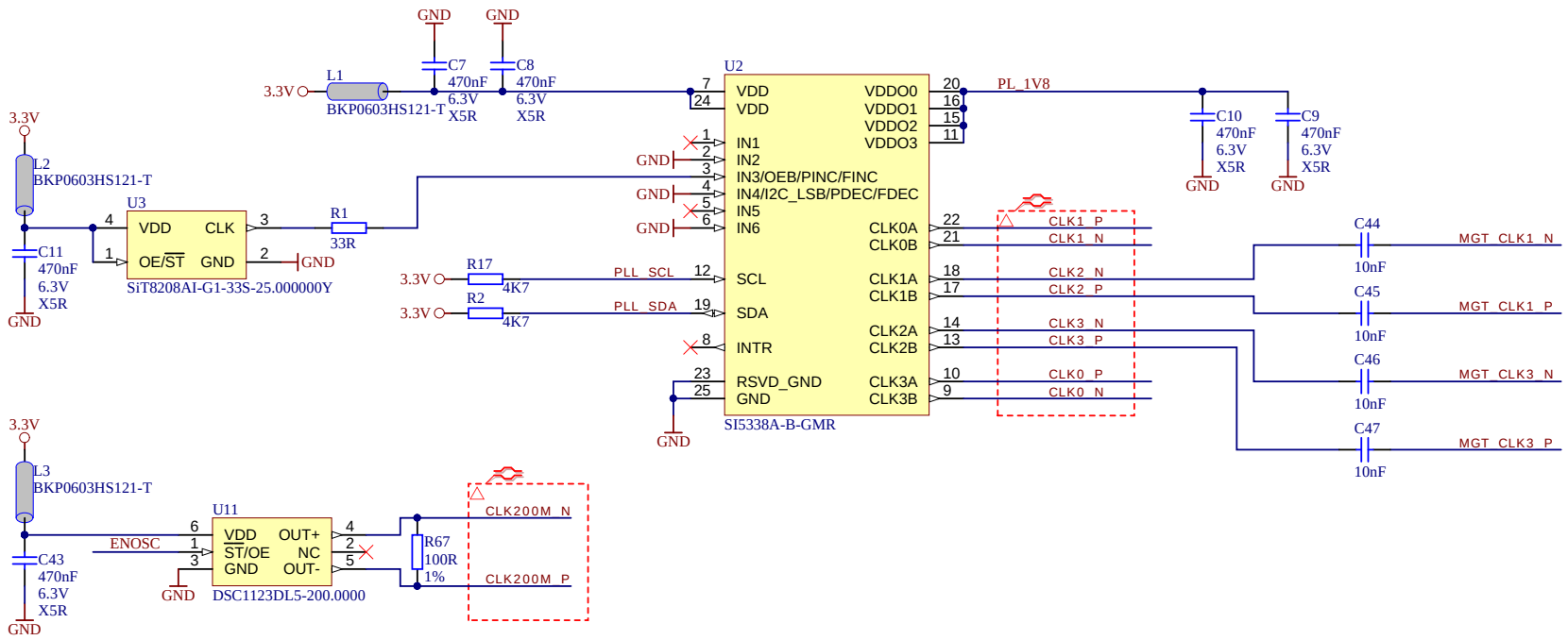

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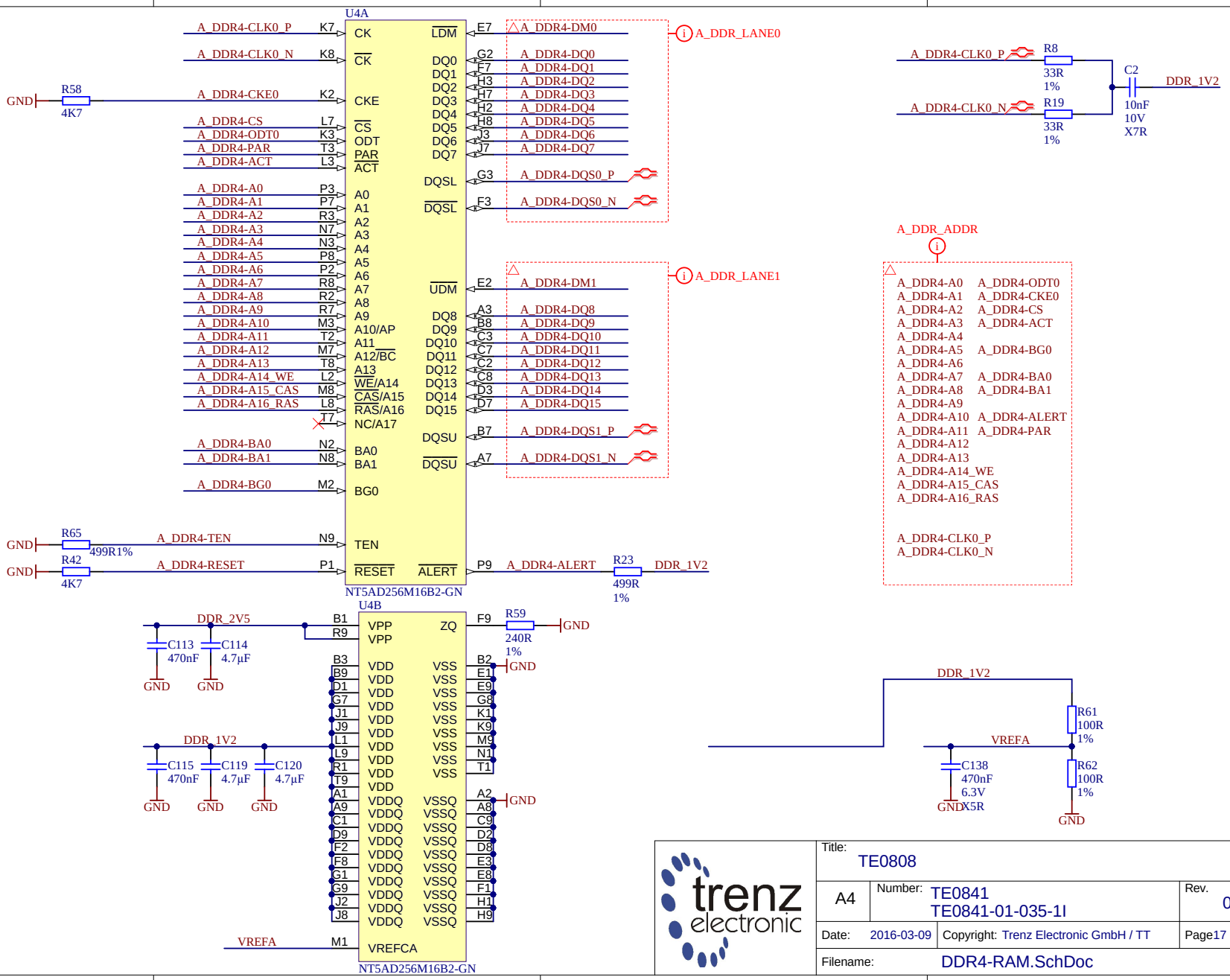
	Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT	Page16 of 22
	Filename: Clock.SchDoc		



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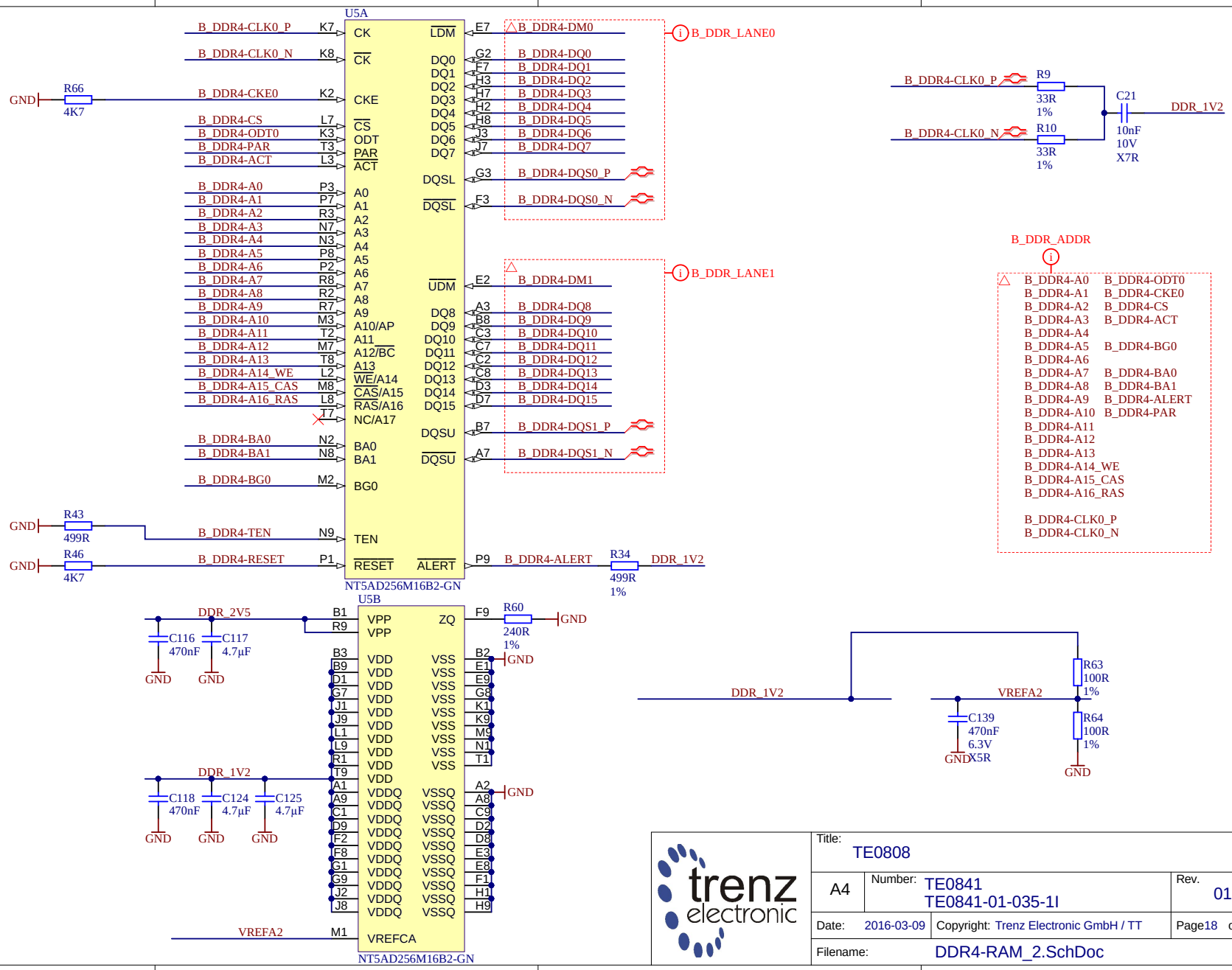
Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT		Page17 of 22
Filename: DDR4-RAM.SchDoc			



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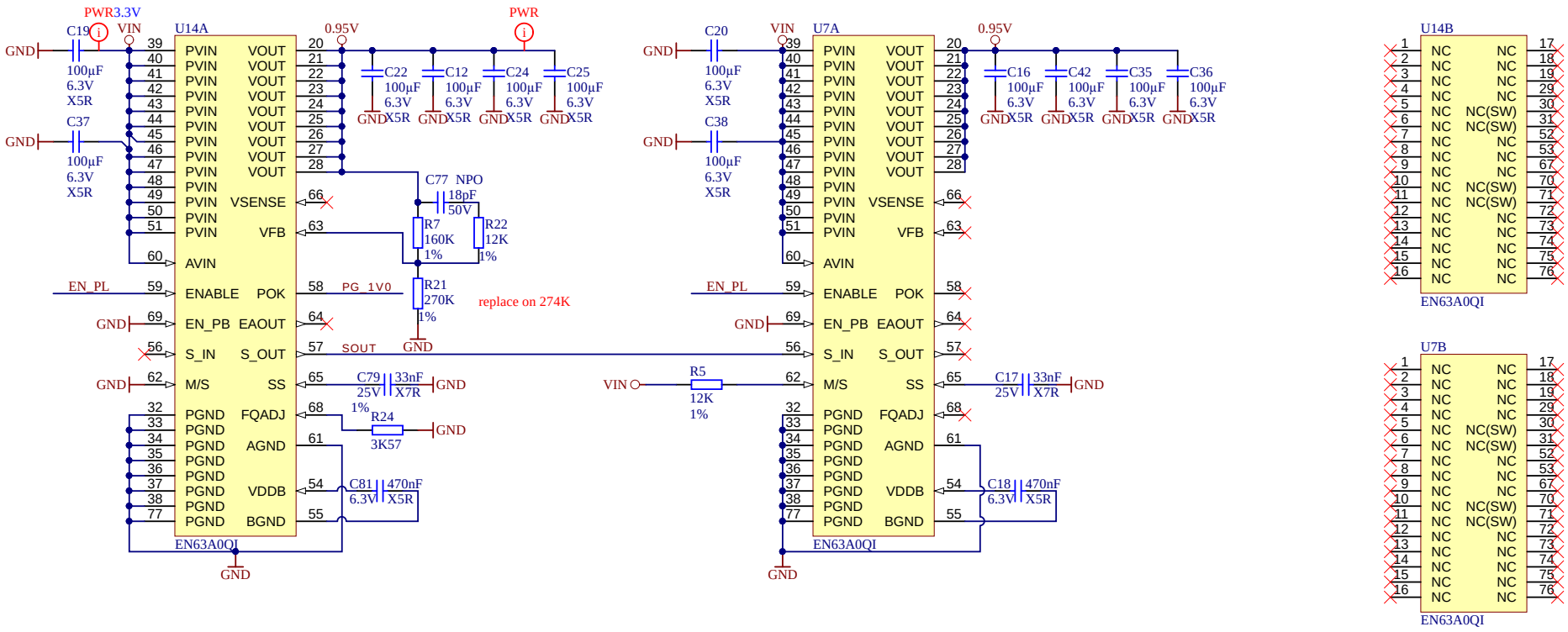
Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT		Page18 of 22
Filename: DDR4-RAM_2.SchDoc			



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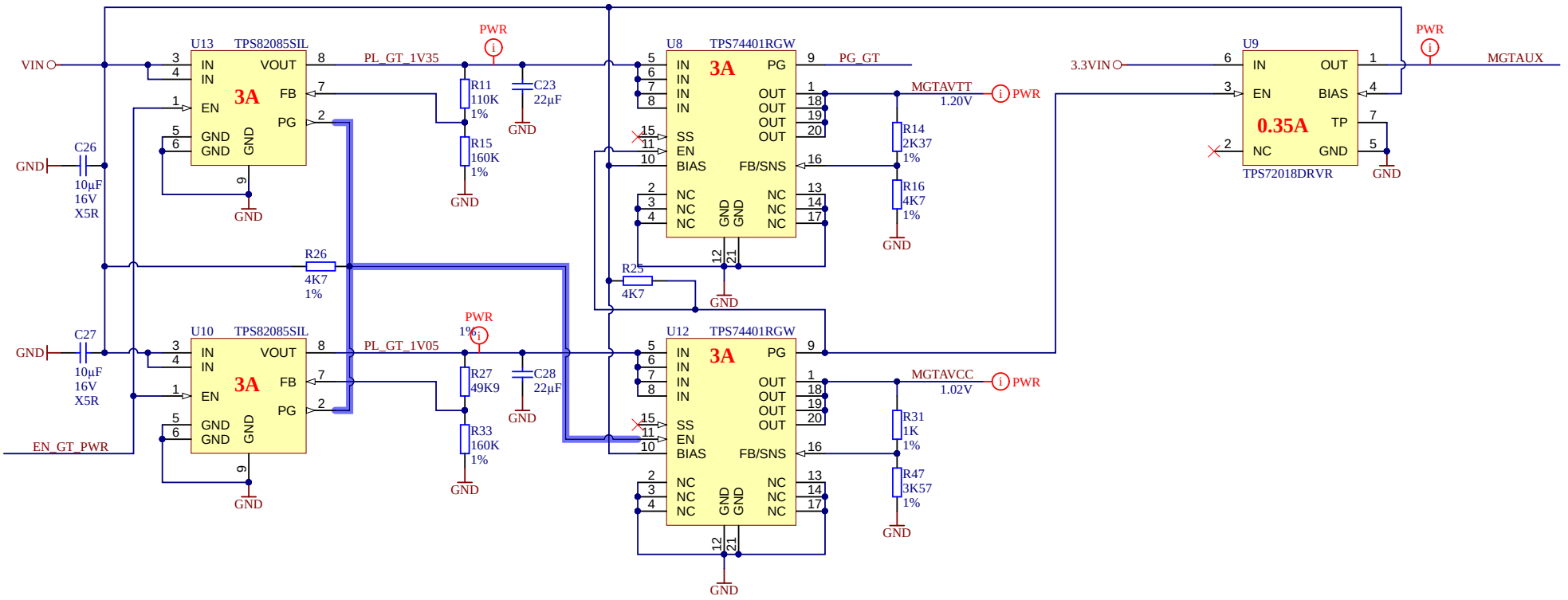
Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT		Page19 of 22
Filename: PWR1.SchDoc			



trenz electronic	Title: TE0841		
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	Date: 2016-03-09	Copyright: Trenz Electronic GmbH / TT	Page20 of 22
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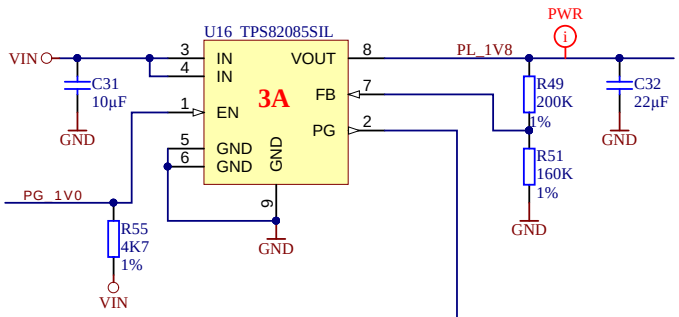

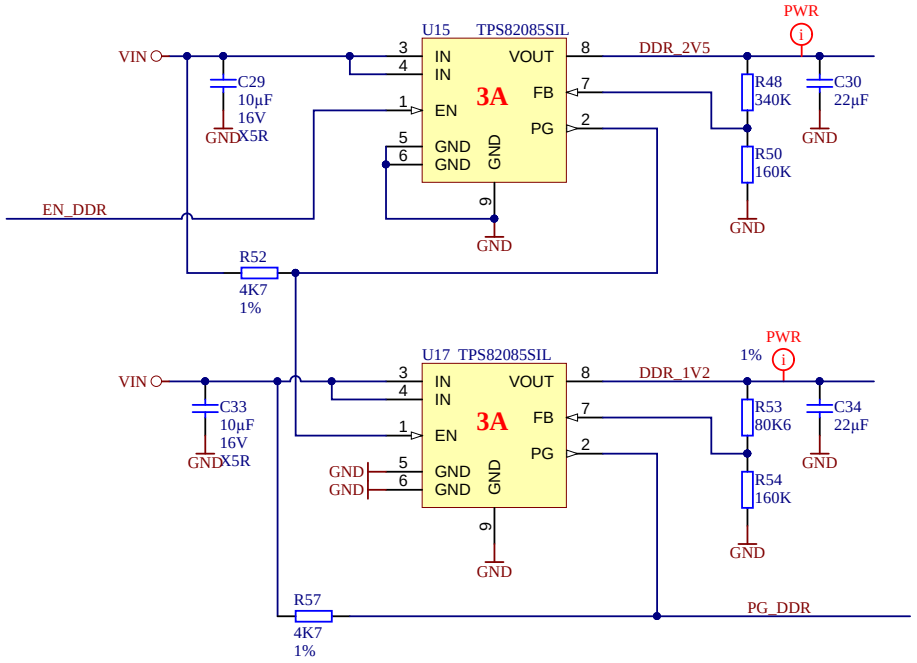

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Title: TE0808			
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Filename: POWER_2.SchDoc			

CHANGES REV01 TO REV01A (08.16.2017):

1) U1: changed schematic symbol. Next pins swapped to mach same polarity order:
-- AE13 (IO_L6P_T0U_N10_AD6P_64)/AE12 (IO_L6N_T0U_N11_AD6N_64)
-- J5 (IO_L18P_T2U_N10_AD2P_66)/J4 (IO_L18N_T2U_N11_AD2N_66)

2) Net names changed (no electrical changes):
JM1: swapped signals B64_L6:
-- B64_L6_N - pin 40 (was pin 42)
-- B64_L6_P - pin 42 (was pin 40)
JM3: swapped signals B64_L6:
-- B66_L18_N - pin 52 (was pin 54)
-- B66_L18_P - pin 54 (was pin 52)

trenz electronic	Title: TE0808 - Changes list		
	A4	Number: TE0841 TE0841-01-035-1I	Rev. 01
	Date: 2017-08-16	Copyright: Trenz Electronic GmbH	Page 22 of 22
	Filename: Revision_Changes.SchDoc		