



# **N-Channel Super Trench Power MOSFET**

#### **Description**

The RM78N100LD uses **Super Trench** technology that is uniquely optimized to provide the most efficient high frequency switching performance. Both conduction and switching power losses are minimized due to an extremely low combination of  $R_{DS(ON)}$  and  $Q_g$ . This device is ideal for high-frequency switching and synchronous rectification.

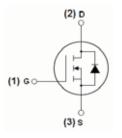
#### **General Features**

- V<sub>DS</sub> =100V,I<sub>D</sub> =78A
  - $R_{DS(ON)}$  =7.5m $\Omega$ (typical) @ V<sub>GS</sub>=10V  $R_{DS(ON)}$  =10.5m $\Omega$ (typical) @ V<sub>GS</sub>=4.5V
- Excellent gate charge x R<sub>DS(on)</sub> product(FOM)
- Very low on-resistance R<sub>DS(on)</sub>
- 175 °C operating temperature
- Pb-free lead plating
- 100% UIS tested

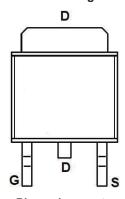
### **Application**

- DC/DC Converter
- Ideal for high-frequency switching and synchronous rectification
- Halogen-free
- P/N suffix V means AEC-Q101 qualified, e.g:RM78N100LDV

100% UIS TESTED! 100% \( \Delta V ds TESTED! \)



Schematic diagram



Pin assignment



TO-252-2L top view

### Package Marking and Ordering Information

Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
78N100	RM78N100LD	TO-252-2L	-	-	-

## Absolute Maximum Ratings (T<sub>C</sub>=25℃unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>G</sub> S	±20	V
Continuous Drain Current (T <sub>a</sub> =25℃)	I <sub>D</sub>	60	Α
Continuous Drain Current (T <sub>a</sub> =100℃)	I <sub>D</sub>	38	Α
Pulsed Drain Current (1)	I <sub>DM</sub>	200	А
Single Pulsed Avalanche Energy (2)	Eas	90	mJ
Power Dissipation	P <sub>D</sub>	63	W
Thermal Resistance from Junction to Case	Rejc	1.8	°C/W
Junction Temperature	TJ	150	$^{\circ}$
Storage Temperature	T <sub>STG</sub>	-55~ +150	$^{\circ}$

# MOSFET ELECTRICAL CHARACTERISTICS(T<sub>a</sub>=25℃ unless otherwise noted)

Parameter	Symbol	Test Condition	Min	Туре	Max	Unit	
Static Characteristics							
Drain-source breakdown voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> =250μA	100	_	-	V	
Zero gate voltage drain current	IDSS	V <sub>DS</sub> =80V, V <sub>GS</sub> = 0V	-	-	1	μA	
Gate-body leakage current	l <sub>GSS</sub>	$V_{GS} = \pm 20V$ , $V_{DS} = 0V$	-	-	±100	nA	
Gate threshold voltage <sup>(3)</sup>	$V_{GS(th)}$	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =250μA	1.2	1.8	2.5	V	
Duning 2011 100 100 100 100 100 100 100 100 10	Б	V <sub>GS</sub> =10V, I <sub>D</sub> =20A	-	7.5	9.5	C	
Drain-source on-resistance <sup>(3)</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> =4.5V, I <sub>D</sub> =10A	-	10.5	13	mΩ	
Forward Threshold Voltage	<b>G</b> fs	V <sub>DS</sub> =5V, I <sub>D</sub> =20A	-	13.5	-	S	
Gate Resistance	$R_g$	V <sub>DS</sub> =V <sub>GS</sub> =0V, f =1MHz	-	1.94	-	Ω	
Dynamic characteristics			•				
Input Capacitance	Ciss		_	2022	-	pF	
Output Capacitance	Coss	V <sub>DS</sub> =50V, V <sub>GS</sub> =0V, f =1MHz	-	580	-		
Reverse Transfer Capacitance	Crss		-	28	-		
Switching characteristics	·						
Turn-on delay time	t <sub>d(on)</sub>		-	17	-		
Turn-on rise time	tr	V <sub>DD</sub> =50V, I <sub>D</sub> =20A,	-	4	-		
Turn-off delay time	$t_{d(off)}$	$V_{GS}$ =10 $V$ , $R_{G}$ =3 $\Omega$	-	32	-	ns	
Turn-off fall time	t <sub>f</sub>		-	8	-		
Total Gate Charge	Qg	\/D0_50\/ ID_004	-	38.5	-	nC	
Gate-Source Charge	Qgs	VDS=50V, ID=20A,	-	8	-		
Gate-Drain Charge	Qgd	VGS=10V	-	9	-		
Reverse Recovery Chrage	Qrr	I <sub>F</sub> =20A,di/dt=100A/us		68		nC	
Reverse Recovery Time	Trr	I <sub>F</sub> =20A,di/dt=100A/us		50.5		ns	
Source-Drain Diode characteristics							
Diode Forward voltage <sup>(3)</sup>	V <sub>DS</sub>	V <sub>GS</sub> =0V, I <sub>S</sub> =20A	-	-	1.2	V	
Diode Forward current <sup>(4)</sup>	Is		-	-	60	Α	

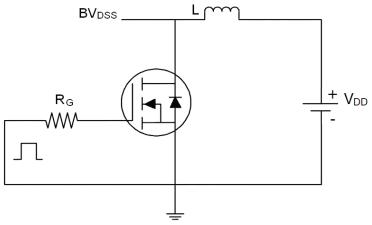
#### Notes:

- 1. Repetitive Rating: pulse width limited by maximum junction temperature
- 2. EAS Condition:TJ=25°C,VDD=50V,RG=25 $\Omega$ ,L=0.5mH
- 3. Pulse Test: pulse width≤300µs, duty cycle≤2%
- 4. Surface Mounted on FR4 Board,t≤10 sec

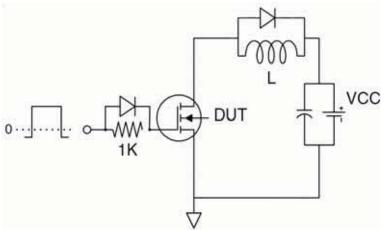


## **Test Circuit**

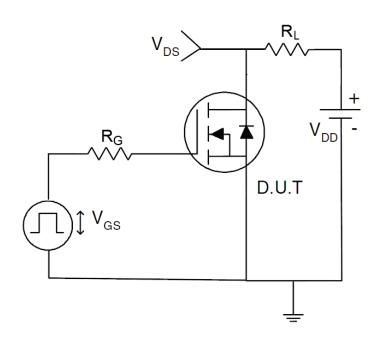
# 1) E<sub>AS</sub> test Circuit



# 2) Gate charge test Circuit



# 3) Switch Time Test Circuit





# RATING AND CHARACTERISTICS CURVES (RM78N100LD)

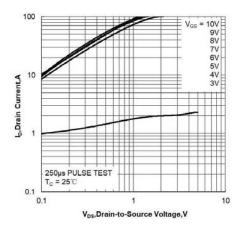


Figure 1. Output Characteristics

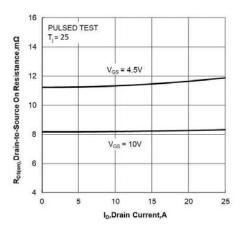


Figure 3. Drain-to-Source On Resistance vs Drain Current

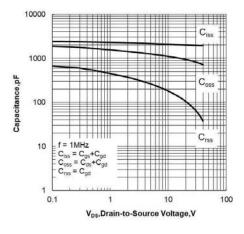


Figure 5. Capacitance Characteristics

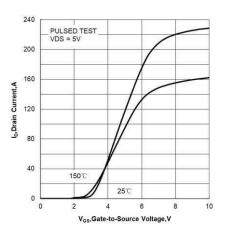


Figure 2. Transfer Characteristics

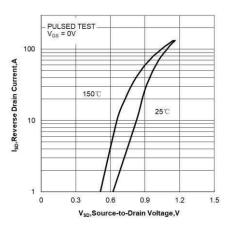


Figure 4. Body Diode Forward Voltage vs Source Current and Temperature

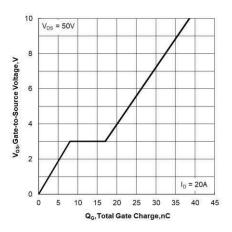


Figure 6. Gate Charge Characteristics



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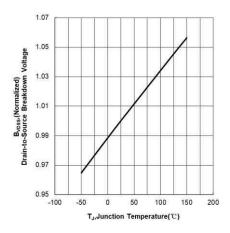


Figure 7. Normalized Breakdown Voltage vs Junction Temperature

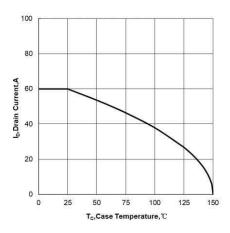


Figure 9. Maximum Continuous Drain Current vs Case Temperature

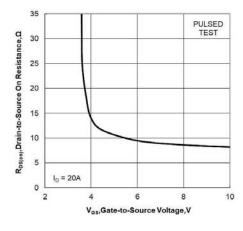


Figure 11. Drain-to-Source On Resistance vs Gate
Voltage and Drain Current

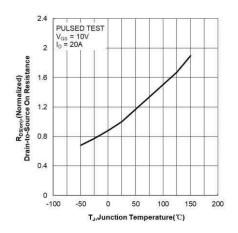


Figure 8. Normalized On Resistance vs

Junction Temperature

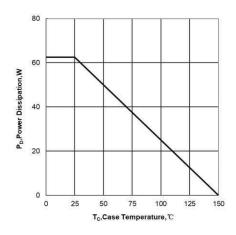


Figure 10. Maximum Power Dissipation vs Case Temperature

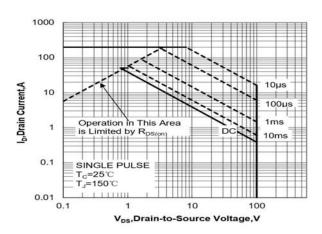


Figure 12. Maximum Safe Operating Area



# RATING AND CHARACTERISTICS CURVES (RM78N100LD)

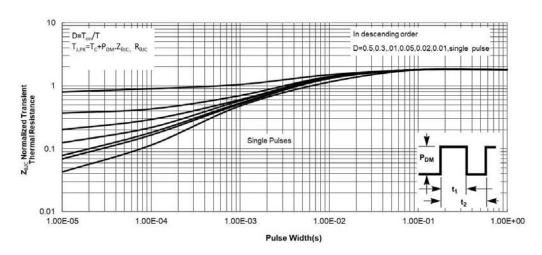
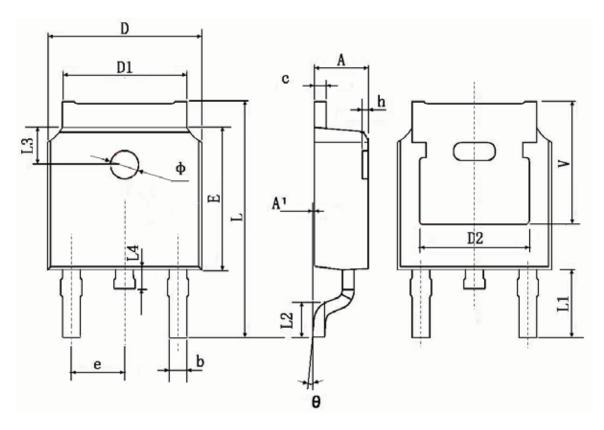


Figure 13. Maximum Effective Transient Thermal Impedance, Junction-to-Case



**TO-252 Package Information** 



0	Dimensions	In Millimeters	Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
A	2.200	2.400	0.087	0.094	
A1	0.000	0.127	0.000	0.005	
b	0.660	0.860	0.026	0.034	
С	0.460	0.580	0.018	0.023	
D	6.500	6.700	0.256	0.264	
D1	5.100	5.460	0.201	0.215	
D2	4.83	TYP.	0.190 TYP.		
E	6.000	6.200	0.236	0.244	
е	2.186	2.386	0.086	0.094	
L	9.800	10.400	0.386	0.409	
L1	2.900 TYP.		0.114 TYP.		
L2	1.400	1.700	0.055	0.067	
L3	1.600 TYP.		0.063 TYP.		
L4	0.600	1.000	0.024	0.039	
Ф	1.100	1.300	0.043	0.051	
θ	0°	8°	0°	8°	
h	0.000	0.300	0.000	0.012	
V	5.350	TYP.	0.211 TYP.		



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