PIC18F66K80 100-Pin Plug-In Module User's Guide

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Object of Declaration: PIC18F66K80 100-Pin Plug-In Module

EU Declaration of Conformity

Manufacturer: Microchip Technology Inc.
2355 W. Chandler Blvd.
Chandler, Arizona, 85224-6199
USA

This declaration of conformity is issued by the manufacturer.

The development/evaluation tool is designed to be used for research and development in a laboratory environment. This development/evaluation tool is not a Finished Appliance, nor is it intended for incorporation into Finished Appliances that are made commercially available as single functional units to end users under EU EMC Directive 2004/108/EC and as supported by the European Commission's Guide for the EMC Directive 2004/108/EC (8th February 2010).

This development/evaluation tool complies with EU RoHS2 Directive 2011/65/EU.

This development/evaluation tool, when incorporating wireless and radio-telecom functionality, is in compliance with the essential requirement and other relevant provisions of the R&TTE Directive 1999/5/EC and the FCC rules as stated in the declaration of conformity provided in the module datasheet and the module product page available at www.microchip.com.

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Signed for and on behalf of Microchip Technology Inc. at Chandler, Arizona, USA

Derek Carlson
VP Development Tools

12-Sep-14
Date

NOTES:

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Preface

NOTICE TO CUSTOMERS

All documentation becomes dated, and this manual is no exception. Microchip tools and documentation are constantly evolving to meet customer needs, so some actual dialogs and/or tool descriptions may differ from those in this document. Please refer to our website (www.microchip.com) to obtain the latest documentation available.

Documents are identified with a "DS" number. This number is located on the bottom of each page, in front of the page number. The numbering convention for the DS number is "DSXXXXXA", where "XXXXX" is the document number and "A" is the revision level of the document.

For the most up-to-date information on development tools, see the MPLAB® IDE online help. Select the Help menu, and then Topics to open a list of available online help files.

INTRODUCTION

This chapter contains general information that will be useful to know before using the PIC18F66K80 100-Pin Plug-In Module. Items discussed in this chapter include:

• Document Layout
• Conventions Used in this Guide
• Recommended Reading
• The Microchip Website
• Development Systems Customer Change Notification Service
• Customer Support
• Revision History

DOCUMENT LAYOUT

This document describes how to use the PIC18F66K80 100-Pin Plug-In Module as a development tool to emulate and debug firmware on a target board, as well as how to program devices. The document is organized as follows:

• **Chapter 1. "Introduction and Review"**
• **Chapter 2. "Important Notes"**
• **Chapter 3. "Hardware Configuration/Jumper Settings"**
• **Chapter 4. "Demo Code/Reference Firmware"**
• **Appendix A. "PIC18F66K80 100-Pin Plug-In Module Schematics"**

CONVENTIONS USED IN THIS GUIDE

This manual uses the following documentation conventions:

DOCUMENTATION CONVENTIONS

Description	Represents	Examples
Arial font:		
Italic characters	Referenced books	*MPLAB® IDE User's Guide*
	Emphasized text	...is the *only* compiler...
Initial caps	A window	the Output window
	A dialog	the Settings dialog
	A menu selection	select Enable Programmer
Quotes	A field name in a window or dialog	"Save project before build"
Underlined, italic text with right angle bracket	A menu path	*File>Save*
Bold characters	A dialog button	Click **OK**
	A tab	Click the **Power** tab
N'Rnnnn	A number in verilog format, where N is the total number of digits, R is the radix and n is a digit.	4'b0010, 2'hF1
Text in angle brackets < >	A key on the keyboard	Press <Enter>, <F1>
Courier New font:		
Plain Courier New	Sample source code	`#define START`
	Filenames	`autoexec.bat`
	File paths	`c:\mcc18\h`
	Keywords	`_asm, _endasm, static`
	Command-line options	`-Opa+, -Opa-`
	Bit values	`0, 1`
	Constants	`0xFF, 'A'`
Italic Courier New	A variable argument	`file`.o, where `file` can be any valid filename
Square brackets [ ]	Optional arguments	`mcc18 [options] file [options]`
Curly brackets and pipe character: { | }	Choice of mutually exclusive arguments; an OR selection	`errorlevel {0|1}`
Ellipses...	Replaces repeated text	`var_name [, var_name...]`

	Represents code supplied by user	`void main (void)`
		`{ ...`
		`}`

RECOMMENDED READING

This user's guide describes how to use PIC18F66K80 100-Pin Plug-In Module. Other useful documents are listed below. The following Microchip documents are available and recommended as supplemental reference resources.

Release Notes for MPLAB® ICD 3 In-Circuit Debugger

For the latest information on using the PIC18F66K80 100-Pin Plug-In Module, read the "Readme for PIC18F66K80 100-Pin Plug-In Module.htm" file (an HTML file) in the Readmes subdirectory of the MPLAB IDE installation directory. The release notes (Readme) contains update information and known issues that may not be included in this user's guide.

THE MICROCHIP WEBSITE

Microchip provides online support via our website at www.microchip.com. This website is used as a means to make files and information easily available to customers. Accessible by using your favorite Internet browser, the website contains the following information:

- **Product Support** – Data sheets and errata, application notes and sample programs, design resources, user's guides and hardware support documents, latest software releases and archived software
- **General Technical Support** – Frequently Asked Questions (FAQs), technical support requests, online discussion groups, Microchip consultant program member listing
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The Development Systems product group categories are:

*   **Compilers** – The latest information on Microchip C compilers, assemblers, linkers and other language tools. These include all MPLAB C compilers; all MPLAB assemblers (including MPASM™ assembler); all MPLAB linkers (including MPLINK™ object linker); and all MPLAB librarians (including MPLIB™ object librarian).
*   **Emulators** – The latest information on Microchip in-circuit emulators.This includes the MPLAB REAL ICE™ and MPLAB ICE 2000 in-circuit emulators.
*   **In-Circuit Debuggers** – The latest information on the Microchip in-circuit debuggers. This includes MPLAB ICD 3 in-circuit debuggers and PICkit™ 3 debug express.
*   **MPLAB® IDE** – The latest information on Microchip MPLAB IDE, the Windows® Integrated Development Environment for development systems tools. This list is focused on the MPLAB IDE, MPLAB IDE Project Manager, MPLAB Editor and MPLAB SIM simulator, as well as general editing and debugging features.
*   **Programmers** – The latest information on Microchip programmers. These include production programmers such as MPLAB REAL ICE in-circuit emulator, MPLAB ICD 3 in-circuit debugger and MPLAB PM3 device programmers. Also included are nonproduction development programmers such as PICSTART® Plus and PICkit 2 and 3.

CUSTOMER SUPPORT

Users of Microchip products can receive assistance through several channels:

*   Distributor or Representative
*   Local Sales Office
*   Field Application Engineer (FAE)
*   Technical Support

Customers should contact their distributor, representative or field application engineer (FAE) for support. Local sales offices are also available to help customers. A listing of sales offices and locations is included in the back of this document.

Technical support is available through the website at:

http://www.microchip.com/support.

REVISION HISTORY

Revision A (September, 2016)

This is the initial release of this document.

Chapter 1. Introduction and Review

1.1 INTRODUCTION

The PIC18F66K80 Plug-In Module (PIM) allows the users to easily experiment with the PIC18F66K80 family of microcontrollers in conjunction with the Automotive Networking Development Board (ADM00716). The PIC18F66K80 family of devices fully supports applications requiring cost-effective, low-power CAN solutions with high performance and robust peripheral set. The PIM has an integrated ECAN™ module that conforms to the CAN 2.0B Active specification.

TABLE 1-1:   64-PIN TO 100-PIN PIM MAPPING

Device Pin	PIC18F66K80 64-Pin TQFP	PIM Pin	Device Pin	PIC18F66K80 64-Pin TQFP	PIM Pin
1	RC7/CCP4	80	33	VDDCORE/VCAP	85
2	RD4/ECCP1/P1A/PSP4	60	34	RA5/AN4/HLVDIN/T1CKI/SS	84
3	RD5/P1B/PSP5	61	35	RF2/MDCIN1	76
4	RD6/P1C/PSP6	91	36	RF3	77
5	RD7/P1D/PSP7	92	37	RE0/AN5/RD	94
6	RG0/RX1/DT1	52	38	RE1/AN6/C1OUT/WR	99
7	RG1/CANTX2	12	39	RE2/AN7/C2OUT/CS	100
8	Vss	75	40	AVDD	30
9	AVDD	30	41	VDD	37
10	VDD	2	42	AVss	31
11	RG2/T3CKI	14	43	Vss	36
12	RG3/TX1/CK1	51	44	RF4/MDCIN2	81
13	RB0/AN10/FLT0/INT0 (Jumper J3)	21	45	RF5	82
		66	46	OSC1/CLKIN/RA7	63
14	RB1/AN8/CTDIN/INT1 (Jumper J4)	19	47	OSC2/CLKOUT/RA6	64
		67	48	RC0/SOSCO/SCLKI	74
15	RB2/CANTX/CTED1/INT2	88	49	RC1/SOSCI	73
16	RB3/CANRX/CTED2/INT3	87	50	RC2/T1G/CCP2	4
17	RF0/MDMIN	47	51	RC3/REFO/SCL/SCK (Jumper J2)	55
18	RG4/T0CKI	35			57
19	RF1	48	52	RF6/MDOUT	5
20	RB4/AN9/CTPLS/KBI0	83	53	RF7	18
21	RB5/T0KI/T3CKI/CCPS/KBI1	72	54	RD0/C1INA/PSPO	17
22	RB6/PGC/KBI2	26	55	RD1/C1INB/PSP1	38
23	RB7/PGD/T3G/KBI3	27	56	Vss	45
24	RE5/CANTX	89	57	VDD	46
25	VDD	16	58	RD2/C2INA/PSP2	58
26	Vss	15	59	RD3/C2INB/CTMUI/PSP3	59
27	RE4/CANRX	90	60	RE6/RX2/DT2	49

TABLE 1-1:   64-PIN TO 100-PIN PIM MAPPING (CONTINUED)

Device Pin	PIC18F66K80 64-Pin TQFP	PIM Pin	Device Pin	PIC18F66K80 64-Pin TQFP	PIM Pin
28	MCLR/RE3	13	61	RE7/TX2/CK2	50
29	RA0/CVREF/AN0/ULPWU	20	62	RC4/SDA/SDI	54
30	RA1/AN1/C1INC	93		(Jumper J1)	56
31	RA2/VREF-/AN2/C2INC	28	63	RC5/SDO	53
32	RA3/VREF+/AN3	29	64	RC6/CCP3	3

1.2   HIGHLIGHTS

Items discussed in this chapter include:

- PIC18F66K80 Plug-In Module Kit Contents
- Overview of the PIC18F66K80 Family CAN Capabilities

1.3   PIC18F66K80 PLUG-IN MODULE KIT CONTENTS

The PIM module kit contains the following items:

- PIC18F66K80 100-pin Plug-In Module

1.4   OVERVIEW OF THE PIC18F66K80 FAMILY CAN CAPABILITIES

- Conforms to CAN 2.0B Active Specification
- Three Operating modes:
  - Legacy mode (full backward compatibility with existing PIC18CXX8/FXX8 CAN modules)
  - Enhanced mode (programmable TX/RX buffers)
  - FIFO mode (programmable TX/RX buffers)
- Message Bit Rates up to 1 Mbps
- DeviceNet™ Data Byte Filter Support
- Six Programmable Receive/Transmit Buffers
- Three Dedicated Transmit Buffers with Prioritization
- Two Dedicated Receive Buffers
- Sixteen Full, 29-Bit Acceptance Filters with Dynamic Association
- Three Full, 29-Bit Acceptance Masks

Chapter 2. Important Notes

2.1 HIGHLIGHTS

This chapter discusses:

- Using the PIM with the Automotive Networking Board
- Programming the Microcontroller

2.2 USING THE PIM WITH THE AUTOMOTIVE NETWORKING DEVELOPMENT BOARD

The PIC18F66K80 100-pin PIM brings out SPI and I^2C on the same PIM pins. Use the jumpers J1 and J2 on the PIM to select between the two.

To enable INT access to all four click connections on the Automotive Networking Development Board, the PIC18F66K80 PIM allows the user to jumper the PIC18F INT0 or INT1 into the various INT pins on the four click headers by using jumpers J3 and J4 on the PIM.

Refer to **Section 3.4 "Jumpers"** for more details.

2.3 PROGRAMMING THE MICROCONTROLLER

When the PIM is mated to the Automotive Networking Development Board, it may be reprogrammed directly through the modular RJ11 jack (JP1) or the PICkit™ connection (J11) on the Automotive Networking Development Board.

NOTES:

Chapter 3. Hardware Configuration/Jumper Settings

3.1 HIGHLIGHTS

This chapter discusses:

- Hardware Features
- Crystal
- Jumpers

3.2 HARDWARE FEATURES

The PIC18F66K80 PIM has a number of hardware features intended to make it useful as an initial development platform and demonstration board. Refer to **Appendix A. "PIC18F66K80 100-Pin Plug-In Module Schematics"** for the Schematics of the PIM. The board offers the following hardware features:

- 32.768 kHz surface-mounted crystal
- Jumpers for added configurability

3.3 CRYSTAL

The PIM is populated with a 32.768 kHz crystal oscillator on the secondary oscillator pins.

3.4 JUMPERS

The PIM has four jumper headers on it to provide additional flexibility for experimentation and development purposes. The default position for these jumpers are:

- J1 – Jumper
  - Pins 1 and 2 (connects port 54 to RC4): SPI SDI connection to PIM (default setting)
  - Pins 2 and 3 (connects port 56 to RC4): I^{2}C SDA connection to PIM
- J2 – Jumper
  - Pins 1 and 2 (connects port 55 to RC3): SPI connection click headers (default setting)
  - Pins 2 and 3 (connects port 57 to RC3): I^{2}C connection click headers
- J3 – Jumper
  - Pins 1 and 2 (connects port 21 to RB0/INT0): click 1 INT connection (default setting)
  - Pins 2 and 3 (connects port 66 to RB0/INT0): click 3 INT connection
- J4 – Jumper
  - Pins 1 and 2 (connects port 19 to RB1/INT1): click 2 INT connection (default setting)
  - Pins 2 and 3 (connects port 67 to RB1/INT1): click 4 INT connection

FIGURE 3-1:   **3D VIEW OF PIC18F66K80 100-PIN PIM**
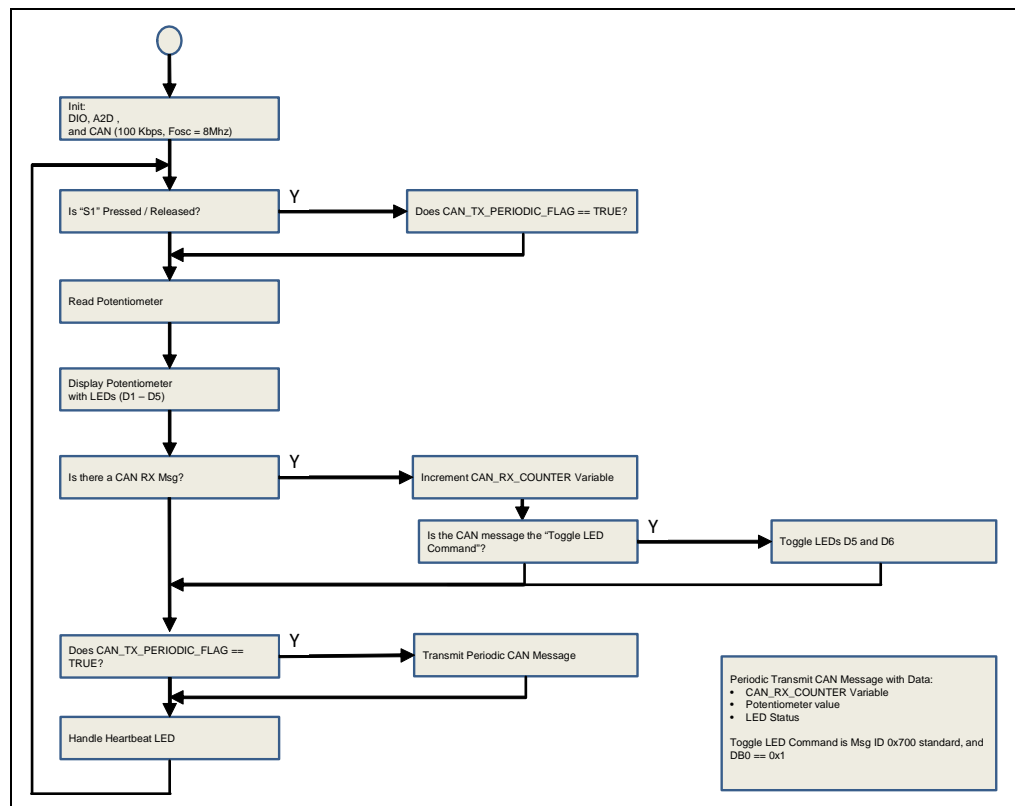
Chapter 4. Demo Code/Reference Firmware

4.1   PREPROGRAMMED DEMONSTRATION FIRMWARE

The PIC18F66K80 100-pin PIM is programmed with firmware demonstrating basic features on the Automotive Networking Development Board. The source for the PIC18F66K80 PIM demo code may be downloaded from the Microchip website www.microchip.com.

FIGURE 4-1:   FIRMWARE DETAILS
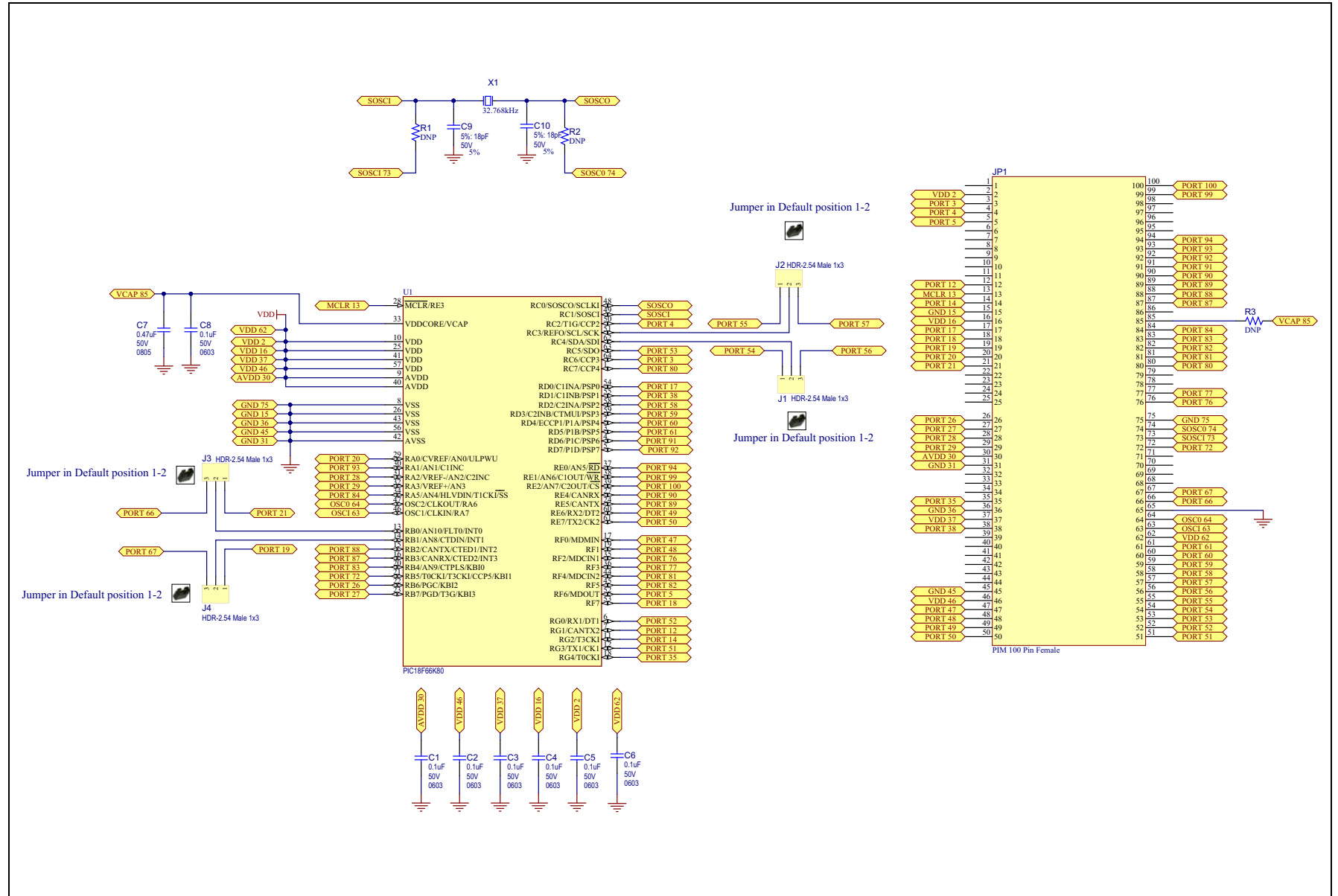
NOTES:

Appendix A. PIC18F66K80 100-Pin Plug-In Module Schematics

A.1 SCHEMATICS

The PIC18F66K80 100-Pin Plug-In Module schematics are shown in Figure A-1. By default, the PIM ships from the factory with the PIC18F66K80 pre-installed in location U1. The board revision number can be found etched into the copper on the bottom side of the PCB.

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