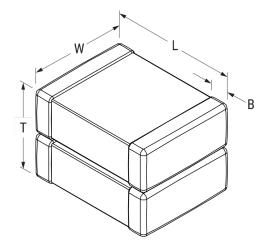


C1812X665M1RLCTU

Aliases (C1812X665M1RLC7800)

KONNEKT Comm X7R, Ceramic, 6.6 uF, 20%, 100 VDC, X7R



Click here for the 3D model.

Dimensions	
L	4.5mm +/-0.4mm
W	3.2mm +/-0.3mm
т	3.9mm +/-0.4mm
В	0.7mm +/-0.35mm

Packaging Specifications				
Packaging	T&R, 180mm, Plastic Tape			
Packaging Quantity	275			

General Information				
Series	KONNEKT Comm X7R			
Style	KONNEKT			
Description	SMD, MLCC, KONNEKT, Ultra-Stable, Class II			
Features	High Density Packaging			
RoHS	Yes			
Termination	Flexible Termination			
AEC-Q200	No			
Component Weight	280 mg			
Chip Size	1812-2			
Shelf Life	78 Weeks			
MSL	1			

Specifications			
Capacitance	6.6 uF		
Measurement Condition	120 Hz 0.5Vrms		
Capacitance Tolerance	20%		
Voltage DC	100 VDC		
Dielectric Withstanding Voltage	250 VDC		
Temperature Range	-55/+125°C		
Temperature Coefficient	X7R		
Capacitance Change with Reference to +25°C and 0 VDC Applied (TCC)	15%, 120Hz 0.5Vrms		
Dissipation Factor	2.5% 120 Hz 0.5Vrms		
Aging Rate	3% Loss/Decade Hour		
Insulation Resistance	76 MOhms		

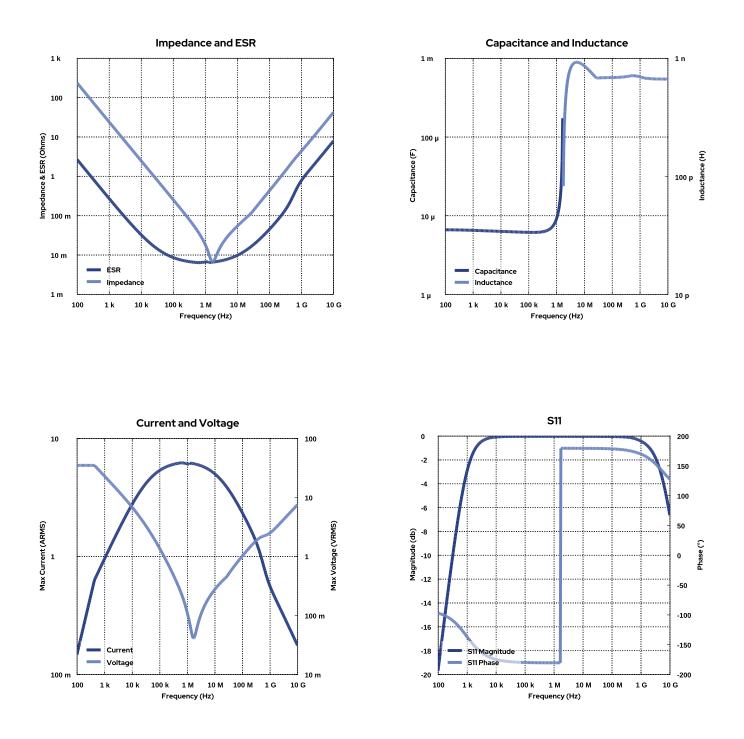
Statements of suitability for certain applications are based on our knowledge of typical operating conditions for such applications, but are not intended to constitute - and we specifically disclaim - any warranty concerning suitability for a specific customer application or use. This Information is intended for use only by customers who have the requisite experience and capability to determine the correct products for their application. Any technical advice inferred from this Information or otherwise provided by us with reference to the use of our products is given gratis, and we assume no obligation or liability for the advice given or results obtained.



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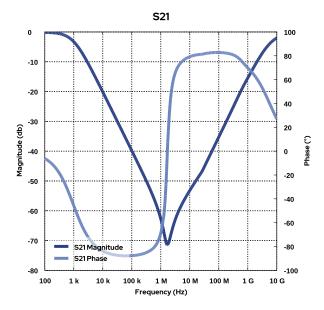
Simulations

For the complete simulation environment please visit K-SIM.

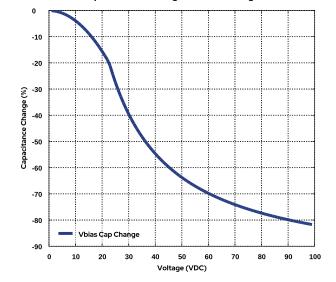




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Capacitance Change vs. DC Voltage Bias





These are simulations.

This is not a specification!

The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

The responses shown do not represent a specified or implied maximum capability of the device for all applications.

- The ESR used for ripple "Ripple Current/Voltage vs. Frequency" plots is the ESR at ambient temperature.
- The ESR in the "Temperature Rise vs. Ripple Current" plots is adjusted to each incremental temperature rise before the power and ripple current is calculated.
- The effects shown herein are based on measured data from a multiple part sample of the parts in question.
- Ripple capability of this device will be factored by thermal resistance (Rth) created by circuit traces (addi affects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance. The peak voltages generated in the "Temperature Rise vs. Combined Ripple Currents" plot are calculated for each frequency and are not combined with voltages generated at any other
- harmonics.
- Please consult with the catalog or field applications engineer for maximum capability of the device in specific applications.

All product information and data (collectively, the "Information") are subject to change without notice.

KEMET K-SIM is designed to simulate behavior of components with respect to frequency, ambient temperature, and DC bias levels. The responses shown represent the typical response for each part type. Specific responses may vary, depending on manufacturing variation effects of all parameters involved, including the specified tolerances applied to capacitance and unspecified variations of ESR, ESL, and leakage resistance.

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If you have any questions please contact K-SIM.