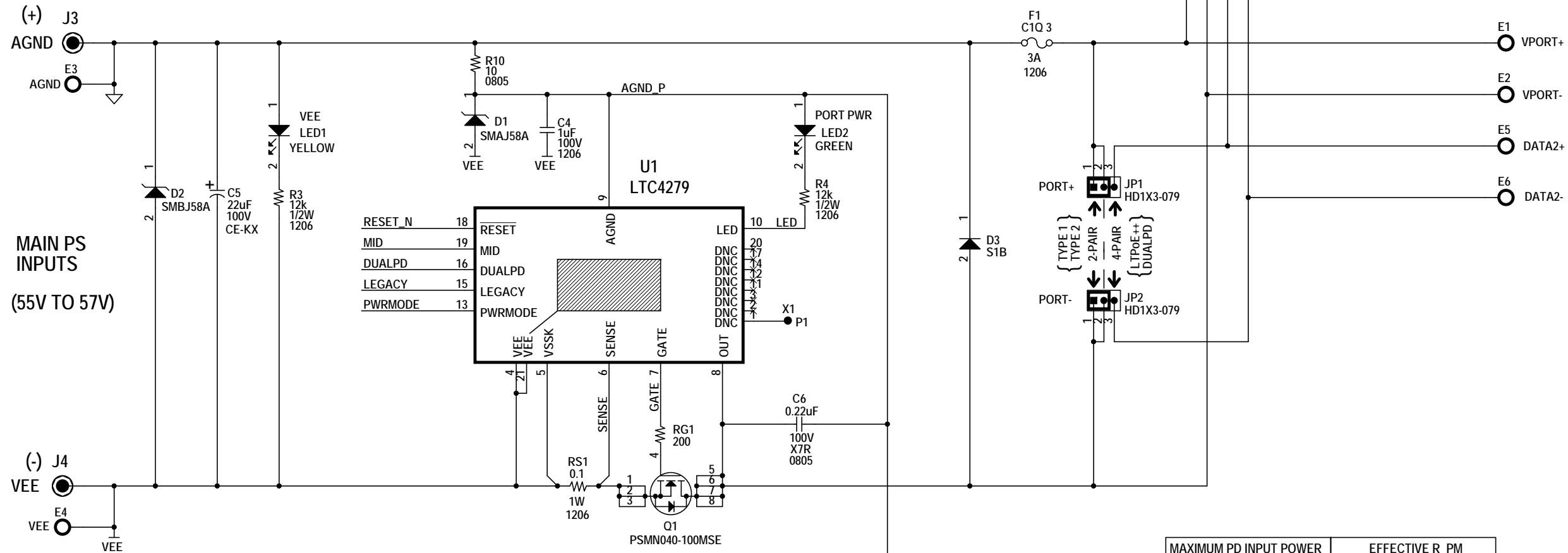
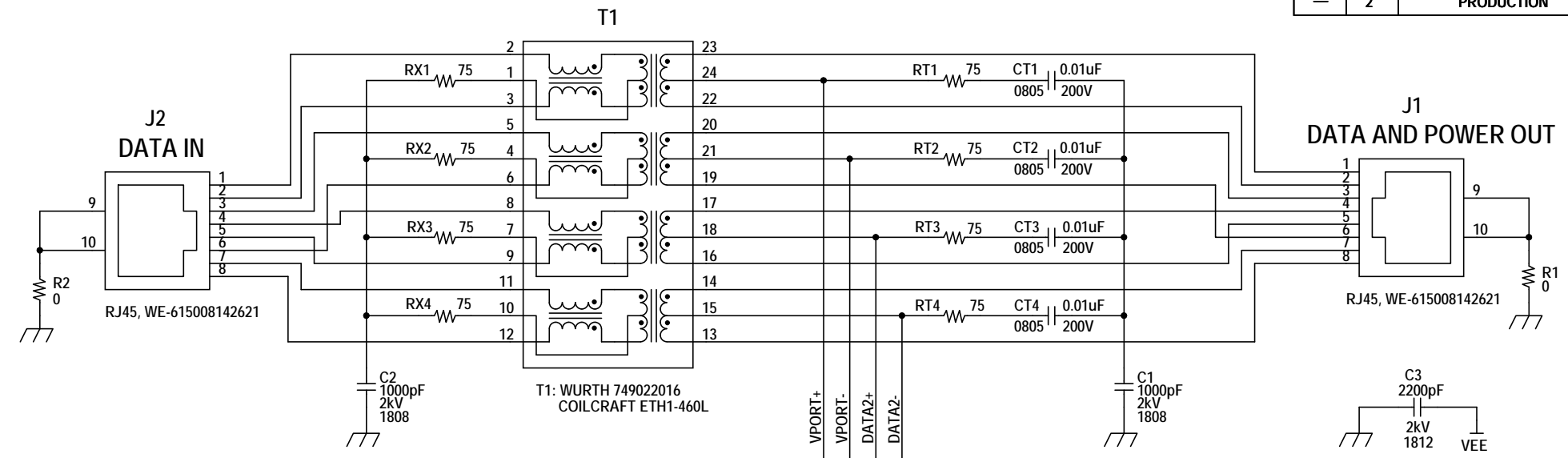


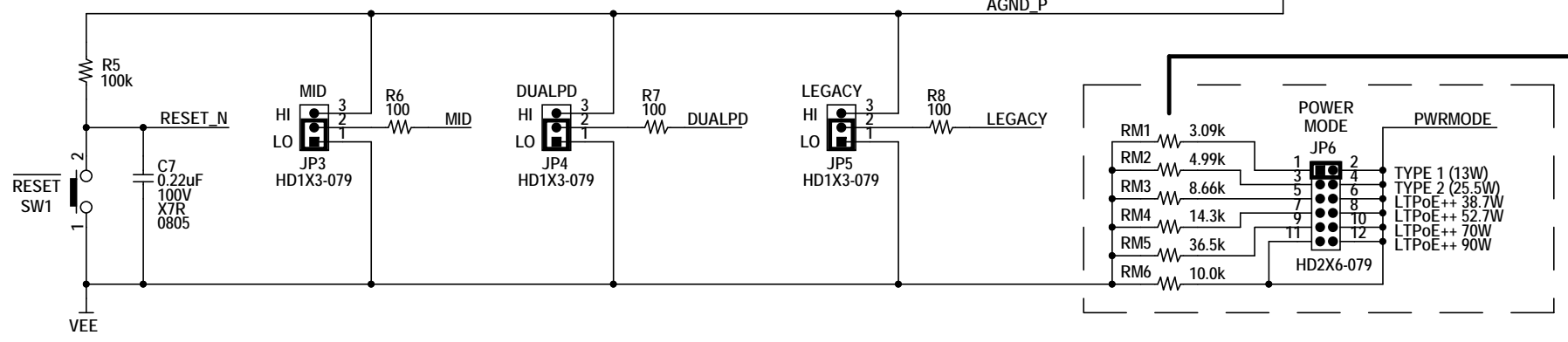
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	2	PRODUCTION	DILIAN R.	10-13-16

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0603.
ALL CAPACITORS ARE IN MICROFARADS, 0603.
2. INSTALL SHUNTS AS SHOWN.
3. FOR TEMPERATURE SENSITIVE 90W APPLICATIONS,
TWO OF THE 749012013 MAY BE USED AS AN ALTERNATE
TO T1 WHICH HAS A HIGHER CURRENT RATING



MAXIMUM PD INPUT POWER	EFFECTIVE R_PM
TYPE 1 (13W)	2.37k
TYPE 2 (25.5W)	3.32k
LTPoE++ 38.7W	4.64k
LTPoE++ 52.7W	5.90k
LTPoE++ 70W	7.87k
LTPoE++ 90W	10.0k



CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS

PCB DES.	KIM T.
APP ENG.	DILIAN R.



TITLE: SCHEMATIC
SINGLE PORT PoE/PoE+/LTPoE++ PSE CONTROLLER

SIZE N/A IC NO. **LTC4279IUFD** REV. 2
DEMO CIRCUIT 2541A

DATE: Thursday, October 13, 2016 SHEET 1 OF 1

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

SCALE = NONE