

# Documentation Errata

**Document Title:** Data\_\_ADSP\_2185L datasheet rev\_A date\_11\_98

**Chapter:** N/A **Page:** 29

**Revision Needed:**

In the 100-Lead LQFP Package Pinout diagram (page 29) and LQFP Pin Configurations diagram (page 30), pins 37 through 40 dont show the multiplexed SPORT1 pin names. These pins should be changed to the following:

Pin Table	
Pin#	Pin Name
37	DT1/FO
38	TFS1/IRQ1
39	RFS1/IRQ0
40	DR1/FI

**Prognosis:** Open

**Reference number:** 8416

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**Document Title:** Data\_\_ADSP\_2185L datasheet rev\_A date\_11\_98

**Chapter:** N/A **Page:** 11

**Revision Needed:**

In the note just before the instruction set description section, the text should change to eliminate the reference to PF3. The note should read as follows:

Note: Pins PF0, PF1, and PF2 are also used for device configuration during reset.

**Prognosis:** Open

**Reference number:** 3996

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*Last Updated: June 19, 2003*