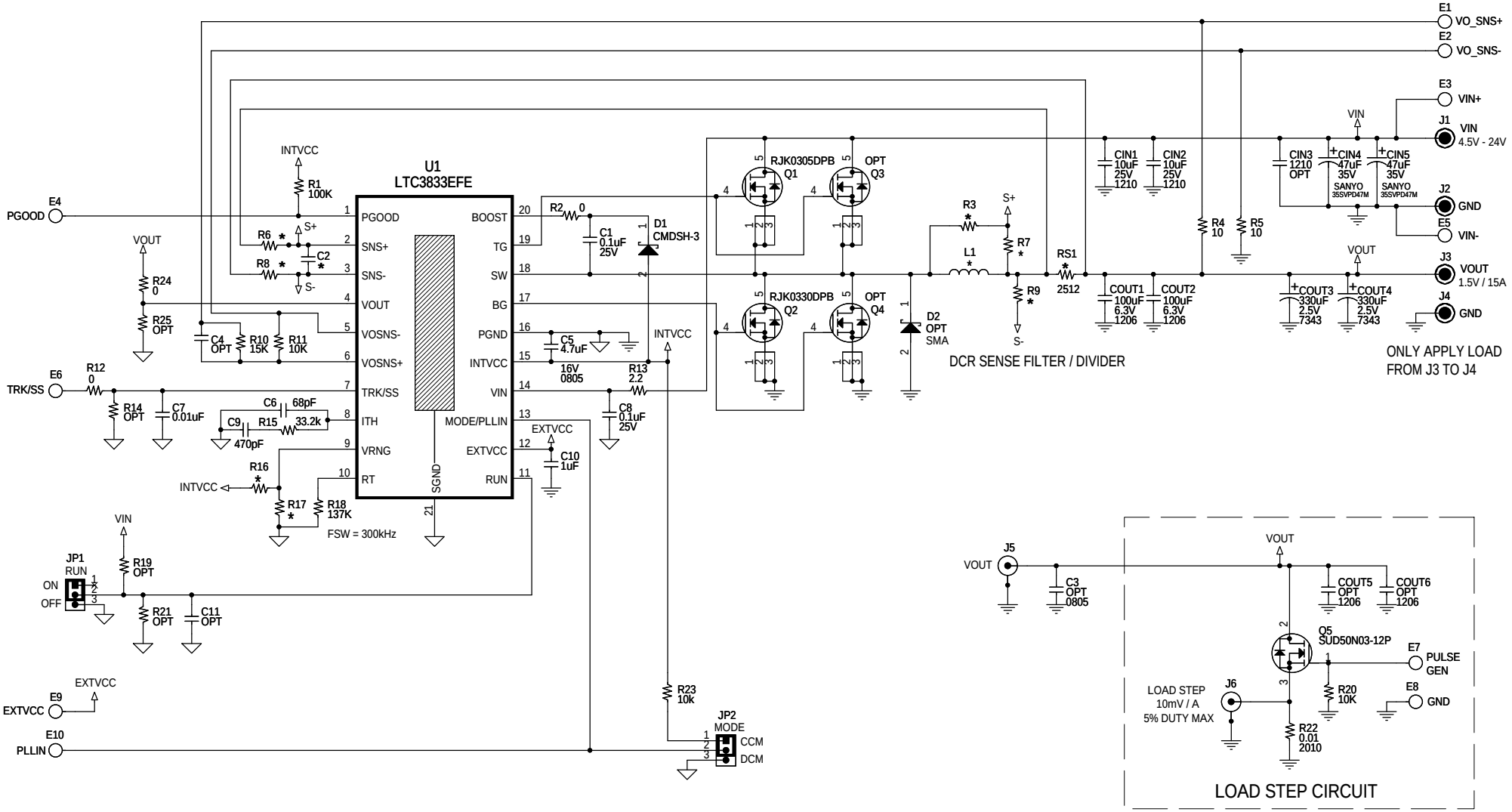
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	2	PRODUCTION	MIKE S.	06-07-11



NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0603.
   ALL CAPACITORS ARE IN MICROFARADS, 0603.
2. INSTALL SHUNTS ON JUMPERS AS SHOWN.

*

ASSY	C2	R3	R6,R8	R7	R9,R17	R16	RS1	L1
-A (DCR SENSE)	0.1uF	4.02k	OPT	13.3k	0	OPT	0	0.56uH
-B (RSENSE)	1000pF	OPT	10	OPT	OPT	0	0.0025	0.68uH

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS	
PCB DES.	KT
APP ENG.	MIKE S.

SCALE = NONE

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TITLE: SCHEMATIC

WIDE INPUT RANGE, HIGH EFFICIENCY STEP-DOWN DC-DC CONVERTER

SIZE	IC NO.	REV.
N/A	LTC3833EFE DEMO CIRCUIT 1516A	2

DATE: Tuesday, June 07, 2011

SHEET 1 OF 1