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ISM14585-L35

5.0 BLE + Cortex M0 + PMU + PA + RTC + Flash

Product Brief

The Inventek Systems **ISM14585-L35** SiP (System in Package), is the smallest, lowest power and most integrated Bluetooth® 5.0 solution available. The **ISM14585-L35** platform is the first BLE module from Inventek's **FLEXiBLE** product family. The **ISM14585-L35** SiP is an embedded wireless Bluetooth Low Energy (BLE) IoT radio, based on the Dialog Semiconductor DA14585 radio SoC (System on Chip). The **ISM14585-L35** offers designers all the benefits of the industry-leading DA14580 technology but with even greater flexibility to create more advanced

<ul style="list-style-type: none"> • FLEXiBLE configuration options (Flash, PA, Clk, Antenna) 	<ul style="list-style-type: none"> • Integrated Audio Unit (AU)
<ul style="list-style-type: none"> • Frequency Band: 2.4GHz 	<ul style="list-style-type: none"> * Pulse-Density Modulation (PDM)
<ul style="list-style-type: none"> • Complies to the Bluetooth 5 core specification 	<ul style="list-style-type: none"> * PDM interface connects to up to 2 input/output devices (MEMS, Mics).
<ul style="list-style-type: none"> • Supports up to 8 Bluetooth LE connections 	<ul style="list-style-type: none"> * Pulse Code modulation (PCM) controller
<ul style="list-style-type: none"> • Network Standard: Bluetooth Low Energy 	<ul style="list-style-type: none"> * 24-bit Sample Rate Converting unit (SRC)
<ul style="list-style-type: none"> • Longest battery life 	<ul style="list-style-type: none"> * Integrated DMA controller
<ul style="list-style-type: none"> • Low operating voltage (1.8 V to 3.6 V) 	<ul style="list-style-type: none"> • Processing power
<ul style="list-style-type: none"> • MSL level 3 	<ul style="list-style-type: none"> * 16 MHz 32 bit ARM Cortex-M0 with SWD interface
<ul style="list-style-type: none"> • Low system Bill of Materials 	<ul style="list-style-type: none"> * Dedicated Link Layer Processor
<ul style="list-style-type: none"> • Power Management 	<ul style="list-style-type: none"> * AES-128 bit encryption Processor
<ul style="list-style-type: none"> * Integrated Buck/Boost DCDC converter 	<ul style="list-style-type: none"> • Memory Resources
<ul style="list-style-type: none"> * P0, P1 and P2 ports with 3.3 V tolerance 	<ul style="list-style-type: none"> * Large memory to build complex applications
<ul style="list-style-type: none"> * Easy decoupling of only 4 supply pins 	<ul style="list-style-type: none"> * Integrated 64 kB (OTP)
<ul style="list-style-type: none"> * Coin (typ. 3.0 V) & Alkaline (typ. 1.5 V) cells 	<ul style="list-style-type: none"> * 96 kB Data/Retention SRAM
<ul style="list-style-type: none"> * 1.8 V cold boot support 	<ul style="list-style-type: none"> * 128 kB ROM
<ul style="list-style-type: none"> * 10-bit ADC for battery voltage meas. 	<ul style="list-style-type: none"> * 8Mb Flash as the standard configuration option
<ul style="list-style-type: none"> * Integrated Power Amplifier 	<ul style="list-style-type: none"> * 4Mb or 16Mb Flash configuration option
<ul style="list-style-type: none"> • Digital controlled oscillators 	<ul style="list-style-type: none"> • Package LGA 35, 6.0mm x 8.6mm x 1.2mm
<ul style="list-style-type: none"> * 16 MHz clk (±20 ppm max) and RC osc 	<ul style="list-style-type: none"> • Digital interfaces
<ul style="list-style-type: none"> * 32 kHz clk(±50 ppm, ±500 ppm max) & RCX osc 	<ul style="list-style-type: none"> * Gen. Purpose I/Os: 14
<ul style="list-style-type: none"> * Configuration option to remove the 32 kHz clk. 	<ul style="list-style-type: none"> * 2 x UARTs with hardware flow control up to 1 MBd
	<ul style="list-style-type: none"> * SPI+™ interface
	<ul style="list-style-type: none"> * I2C bus at 100 kHz, 400 kHz



Ordering Information:

Standard MODULE Ordering Number: ISM14585-L35-P8

ISM14585-L35-XX

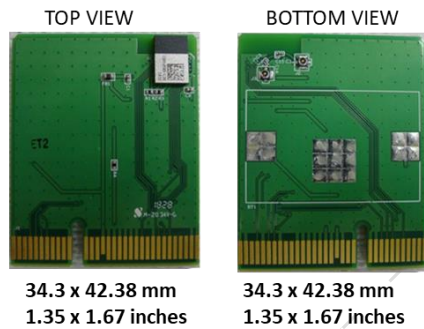
- **Integrated Flash Memory:**
 - Blank = No Flash
 - 4 = 4Mb Flash
 - 8 = 8Mb of Flash
- **Integrated Power Amplifier:**
 - Blank = No PA
 - P = Power Amplifier

Note:

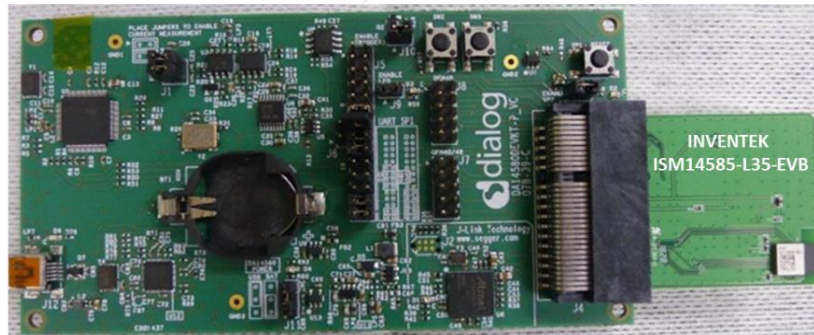
- Dialog CodeLess AT Commands also supported.
- Module ordering option for no 32KHz clock also available.

EVALUATION BOARD Ordering Number:

ISM14585-L35-P8-EVB



ISM14585-L35-EVB + Dialog Dev Kit Pro Motherboard



The ISM14585-L35-EVB Daughter Card Evaluation Board is a pin to pin compatible evaluation board that seamlessly interfaces to the Dialog DA14585 Development Kit-Pro Mother Board including application support via Dialog's SDK SmartSnippets Studio and downloaded via USB (USB to UART transfer IC) to the ISM14585-L35-EVB.

PLEASE REFERENCE THE INVENTEK ISM14585-L35 COMPLETE DATA SHEET FOR DETAILS

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