

<b>PCN Number:</b>	20230224000.2	<b>PCN Date:</b>	February 24, 2023
<b>Title:</b>	Qualification of DMOS6 as an additional Fab site and additional wafer Probe site (CDAT-PR) options for select devices		
<b>Customer Contact:</b>	<a href="#">PCN Manager</a>	<b>Dept:</b>	Quality Services
<b>Proposed 1<sup>st</sup> Ship Date:</b>	Aug 27, 2023	<b>Sample requests accepted until:</b>	Mar 27, 2023*

\*Sample requests received after March 27, 2023 will not be supported.

**Change Type:**

<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Design	<input type="checkbox"/>	Wafer Bump Site
<input type="checkbox"/>	Assembly Process	<input type="checkbox"/>	Data Sheet	<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>	Assembly Materials	<input type="checkbox"/>	Part number change	<input type="checkbox"/>	Wafer Bump Process
<input type="checkbox"/>	Mechanical Specification	<input checked="" type="checkbox"/>	Test Site	<input checked="" type="checkbox"/>	Wafer Fab Site
<input type="checkbox"/>	Packing/Shipping/Labeling	<input type="checkbox"/>	Test Process	<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>		<input type="checkbox"/>	Wafer Fab Process

**PCN Details**

**Description of Change:**

Texas Instruments is pleased to announce the qualification of its DMOS6 fabrication facility as an additional Wafer Fab source and CDAT-PR as an additional probe site options for the selected devices listed in the "Product Affected" section.

Current Fab Site			Additional Fab Site		
Current Fab Site	Process	Wafer Diameter	Additional Fab Site	Process	Wafer Diameter
RFAB	LBC9	300 mm	DMOS6	LBC9	300mm

Probe site changes are as follows:

Current Probe Site	New Probe Site
CLARK-PR	CDAT-PR

Test coverage, insertions, conditions will remain consistent with current testing.

**Reason for Change:**

Continuity of Supply

**Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):**


None

**Changes to product identification resulting from this PCN:**

**Fab Site Information:**

Chip Site	Chip Site Origin Code (20L)	Chip Site Country Code (21L)	Chip Site City
RFAB	RFB	USA	Richardson
<b>DMOS6</b>	<b>DM6</b>	<b>USA</b>	<b>Richardson</b>


Sample product shipping label (not actual product label)



MADE IN: Malaysia  
2DC: 20:

MSL '2 / 260C / 1 YEAR	SEAL DT
MSL 1 / 235C / UNLIM	03/29/04

OPT: 39  
ITEM: 5A (L)T0:1750



(1P) SN74LS07NSR  
(Q) 2000 (D) 0336  
(31T) LOT: 3959047MLA  
(4W) TKY (1T) 7523483S12  
(P)  
(2P) REV: (V) 0033917  
(20L) CSO: SHE (21L) CCO: USA  
(22L) ASO: MLA (23L) ACO: MYS

**Product Affected:**

SN1805032CRHBR	TPS25831QCWRHBRQ1	TPS25840QCWRHBRQ1
SN2004001CRHBR	TPS25832QCWRHBRQ1	TPS25842QCWRHBRQ1
TPS25830QCWRHBRQ1	TPS25833QCWRHBRQ1	TPS25846QCWRHBRQ1

## Automotive New Product Qualification Summary (As per AEC-Q100 and JEDEC Guidelines)

Approved 18-Jan-2023

### Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	#	Test Spec	Min Lot Qty	SS / Lot	Test Name	Condition	Duration	Qual Device: TPS25831QCWRHBRQ1	QBS Reference: DRV8735PWPRQ1	QBS Reference: TPS25831QWRHBRQ1	QBS Reference: DRV8735RHBRQ1	QBS Reference: TTPS2584PUEGDSKRD1	QBS Reference: TPS25831QWRHBRQ1	QBS Reference: TPS25830QWRHBRQ1	QBS Reference: TPS25831QWRHBRQ1
<b>Test Group A - Accelerated Environment Stress Tests</b>															
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL2 260C	1 Step	1/0/0	-	3/0/0	3/0/0	-	1/0/0	1/0/0	3/0/0
PC	A1	JEDEC J-STD-020 JESD22-A113	3	77	Preconditioning	MSL3 260C	1 Step	-	3/0/0	-	-	-	-	-	-
HAST	A2	JEDEC JESD22-A110	3	77	Biased HAST	130C/85%RH	96 Hours	-	3/231/0	3/231/0	3/231/0	3/231/0	-	-	3/231/0
ACU/HAST	A3	JEDEC JESD22-A102 JEDEC JESD22-A118	3	77	Autoclave	121C/15psig	96 Hours	-	-	3/231/0	-	-	-	-	3/231/0
ACU/HAST	A3	JEDEC JESD22-A102 JEDEC JESD22-A118	3	77	Unbiased HAST	130C/85%RH	96 Hours	-	3/231/0	-	3/231/0	3/231/0	-	-	-
TC	A4	JEDEC JESD22-A104 and Appendix 3	3	77	Temperature Cycle	-65C/150C	500 Cycles	1/77/0	-	-	3/231/0	3/231/0	-	1/77/0	-
TC-8P	A4	MIL-STD883 Method 2011	1	5	Post Temp Cycle Bond Pull	-	-	1/5/0	-	-	3/15/0	1/5/0	-	1/5/0	-
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	150C	1000 Hours	-	3/135/0	-	3/135/0	3/135/0	-	-	3/135/0
HTSL	A6	JEDEC JESD22-A103	1	45	High Temperature Storage Life	175C	500 Hours	-	-	3/135/0	-	-	-	-	-
<b>Test Group B - Accelerated Lifetime Simulation Tests</b>															
HTCL	B1	JEDEC JESD22-A108	1	77	Life Test	125C	1000 Hours	-	3/231/0	3/231/0	-	-	-	-	3/231/0
ELFR	B2	AEC Q100-008	1	77	Early Life Failure Rate	125C	48 Hours	-	3/2400/0	1/800/0	-	3/2400/0	-	-	1/800/0
<b>Test Group C - Package Assembly Integrity Tests</b>															
WBS	C1	AEC Q100-001	1	30	Wire Bond Shear	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	-	3/90/0	3/90/0	-	-	3/90/0
WBP	C2	MIL-STD883 Method 2011	1	30	Wire Bond Pull	Minimum of 5 devices, 30 wires Cpk>1.67	Wires	1/30/0	3/90/0	-	3/90/0	3/90/0	-	-	3/90/0
SD	C3	JEDEC J-STD-002	1	15	PB Solderability	>95% Lead Coverage	-	1/15/0	1/15/0	-	1/15/0	1/15/0	1/15/0	-	1/15/0
SD	C3	JEDEC J-STD-002	1	15	PB-Free Solderability	>95% Lead Coverage	-	1/15/0	1/15/0	-	1/15/0	1/15/0	1/15/0	-	1/15/0
PD	C4	JEDEC JESD22-B100 and B108	1	10	Physical Dimensions	Cpk>1.67	-	1/10/0	3/30/0	3/30/0	3/30/0	3/30/0	-	-	3/30/0
<b>Test Group D - Die Fabrication Reliability Tests</b>															
EM	D1	JESD61	-	-	Electromigration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
TDOB	D2	JESD35	-	-	Time Dependent Dielectric Breakdown	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
HCI	D3	JESD60 & 28	-	-	Hot Carrier Injection	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
NBTI	D4	-	-	-	Negative Bias Temperature Instability	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
SM	D5	-	-	-	Stress Migration	-	-	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements	Completed Per Process Technology Requirements
<b>Test Group E - Electrical Verification Tests</b>															
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	2000 Volts	-	1/3/0	-	1/3/0	1/3/0	-	-	-
ESD	E2	AEC Q100-002	1	3	ESD HBM	-	4000 Volts	-	-	-	-	-	-	1/3/0	1/3/0
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	1000 Volts	-	-	-	-	-	-	-	1/3/0
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	1500 Volts	-	-	-	-	-	-	-	1/3/0
ESD	E3	AEC Q100-011	1	3	ESD CDM	-	500 Volts	-	1/3/0	-	1/3/0	1/3/0	-	-	-
LU	E4	AEC Q100-004	1	6	Latch-Up	Per AEC Q100-004	-	-	1/6/0	1/6/0	1/6/0	1/6/0	-	1/6/0	1/6/0
ED	E5	AEC Q100-009	3	30	Electrical Distributions	Cpk>1.67 Room, hot, and cold	-	1/30/0	3/90/0	3/90/0	3/90/0	3/90/0	1/30/0	1/30/0	3/90/0

TPS25830QCWRHBRQ1	TPS25846QCWRHBRQ1
TPS25840QCWRHBRQ1	TPS25833QCWRHBRQ1
TPS25831QCWRHBRQ1	SN1805032CRHBR
TPS25842QCWRHBRQ1	SN2004001CRHBR
TPS25832QCWRHBRQ1	

QBS: Qual By Similarity

Qual Device TPS25831QCWRHBRQ1 is qualified at MSL2 260C

Each product in the family passed yield analysis

Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

The following are equivalent HTOL options based on an activation energy of 0.7eV : 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

The following are equivalent HTSL options based on an activation energy of 0.7eV : 150C/1k Hours, and 170C/420 Hours

The following are equivalent Temp Cycle options per JESD47 : -55C/125C/700 Cycles and -65C/150C/500 Cycles

**Ambient Operating Temperature by Automotive Grade Level:**

Grade 0 (or E): -40C to +150C

Grade 1 (or Q): -40C to +125C

Grade 2 (or T): -40C to +105C

Grade 3 (or I) : -40C to +85C

**E1 (TEST): Electrical test temperatures of Qual samples (High temperature according to Grade level):**

Room/Hot/Cold : HTOL, ED

Room/Hot : THB / HAST, TC / PTC, HTSL, ELFR, ESD & LU

Room : AC/uHAST

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com>

ZVEI ID reference: SEM-PW-13, SEM-TF-01

For questions regarding this notice, e-mails can be sent to the contact below or your local Field Sales Representative.

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